# **TSMLA™** Technical Manual v1.2

### **Table of Contents**

# 1. Executive Summary

- 1.1 What TSMLA<sup>TM</sup> Is
- 1.2 Executable Profile
- 1.3 Platform Delivery
- 1.4 Four-Phase Development Trajectory
- 1.5 Applications and Licensing
- 1.6 Innovation and Protection
- 1.7 Category Definition
- 1.8 Document Roadmap

## 2. The Problem Space

- 2.1 Current Landscape
- 2.2 The Missing Capability
- 2.3 User-Level Pain Points
- 2.4 Why Existing Tools Fail
- 2.5 Formal Problem Definition
- 2.6 Requirements for a Viable Solution
- 2.7 Observable Failure Modes Without Such a System
- 2.8 What Decision Entropy Means Here
- 2.9 Why a Resonance Physics Mirror is Necessary
- 2.10 Section Summary

# 3. TSMLA™ Architecture Overview

3.0 Purpose and Scope

3.1 Fundamental Principles 3.2 Public versus NDA Boundary 3.3 Core Processing Layers • 3.3.1 Boolean Logic (Propositional Contradiction Detection) • 3.3.2 Tag-Weight Signal Quantification • 3.3.3 Recursive Loop Detection • 3.3.4 Boolean Disambiguation Layer (BDL™) 3.4 Processing Pipeline 3.5 Mathematical Notes 3.6 TSMLA™ Recursion versus AGI Recursion 3.7 Interfaces and Implementation Posture 3.8 Notation Table 3.9 Phase Interfaces (Public Artifacts; Mechanisms NDA) 4. Platform User Experience 4.1 Modular Interface Architecture 4.2 Dual-Hemisphere Delivery System 4.3 User Flow 4.4 Guarantees and Boundaries in User Experience 4.5 Interaction Contracts 4.6 Module Protocol 4.7 Visual User Experience Abstraction 4.8 Accessibility and Localization 4.9 Data Handling and Privacy 4.10 Performance and Reliability

4.11 Module Lifecycle and Governance

4.12 Phase 3–4 User Experience Posture (Public View)

## 5. Demonstration: Three Live Modules

- 5.0 Purpose & Demonstration Protocol
- 5.1 Module A: Decision Loop Entrapment (Full Walkthrough)
- 5.2 Module B: Freedom vs Approval Loop (Abbreviated)
- 5.3 Module C: Recursive Delay Pattern (Abbreviated)
- 5.4 Comparative Snapshot (Public)
- 5.5 Why This Matters (Non-Prescriptive)
- 5.6 NDA Boundary
- 5.7 Reproducibility, Exportability & User Agency

## 6. Technical Validation & Innovation Claims

- 6.0 Purpose and Scope
- 6.1 Mathematical Foundations (Public)
- 6.2 Patent Protection Status (Public)
- 6.3 Competitive Landscape (Public)
- 6.4 Structural Neutrality Safeguards (SNS)
- 6.5 Validation Roadmap (Public)

# 7. Market Applications & Business Model

- 7.1 Target Markets (Public)
- 7.2 Revenue Streams (Public)
- 7.3 Scalability Model (Public)
- 7.4 Competitive Differentiation (Public)

# 8. Development Roadmap & Future Vision

- 8.0 Overview
- 8.1 Phase 1: Beta Platform (Current)
- 8.2 Phase 2: AI Integration + API Access (Projected 6–12 Months)

- 8.3 Phase 3: Fractal Probability Rendering<sup>™</sup> (Projected 9–21 Months, Overlapping Phase 2)
- 8.4 Phase 4: Timeline Evolution™ (Projected 12–18 Months Post-FPR™)
- 8.5 Phase 5+ Vision: The Mirrorfield™ Recursive Reality Simulation
- 8.6 Capital Efficiency and Milestone-Based Development

## 9. Engagement Pathways

- 9.1 Primary Contact
- 9.2 Research & Institutional Collaboration
- 9.3 Licensing & API Access
- 9.4 Media & Speaking

## **Authorship & IP**

### Addenda

## Addendum A. Substrate Architecture & Licensing Framework

**Purpose:** This addendum positions TSMLA<sup>TM</sup> as a logic substrate rather than merely a software application, defining substrate properties, licensing boundaries, and integration architectures.

#### **Sections:**

A.1 Introduction: Why Substrate Matters

A.2 Five Core Properties of TSMLA™ as Substrate

- A.2.1 Foundational
- A.2.2 Recursive
- A.2.3 Application-Agnostic
- A.2.4 Licensing-Ready
- A.2.5 Integrity-Preserving

A.3 Public versus Protected: Substrate Disclosure Boundaries

A.4 Licensing Implications and Boundaries

• A.4.1 Core Policy: No Pre-Prototype or Beta Licensing

- A.4.2 Licensable Components (Post-Market Only)
- A.4.3 Non-Licensable Components (Locked)
- A.4.4 Components Not Offered at This Time (TBD)
- A.4.5 License Terms and Conditions

A.5 Substrate as Sovereign Layer: Positioning and Value Proposition

A.6 Integration Architectures: Black-Box Deployment Models

A.7 Mirror Breach Definition and Enforcement

# **Appendices**

## Appendix A. Glossary

Key terms and symbols used throughout the TSMLA™ architecture and technical manual.

### **Appendix B. (Reserved)**

## **Appendix C. Sample Module Architecture**

### **Module C1: Coherence Audit (Finance)**

- Input: Statement Set S with Weights W
- Processing Pipeline
- Output: Resonance Profile P(S)
- Recompute Demonstration: Statement Edit
- Audit & Export
- NDA Boundary

# **Appendix D. IP Protection Summary**

- Patent Protection
- Trade Secret Protection
- Trademark Protection
- Inventor Attribution
- Public Disclosure Boundaries
- Licensing Framework

### **Appendix E. References & Academic Validation**

- Mathematical Foundations
- Foundational References
- Validation Framework
- Limitations and Scope

## **Appendix F. Notation & Symbol Reference**

- Quick Lookup Table
- Symbol Typography Notes

### **Document Control**

**Doc ID:** TSMLA-Manual-v1.2

**Build Date: 2025-10-28** 

Contact: contact@fractalconsciousness.ai

**Hash:** [SHA-256 to be computed post-export]

Prepared by: Califa Anu Bey, Founder & Chief Architect

Company: Fractal Labyrinth Systems LLC

© 2025 Fractal Labyrinth Systems LLC. All rights reserved.

### **Forward Compatibility Note**

This Table of Contents is subject to expansion as the TSMLA™ architecture evolves and additional documentation is integrated into the Technical Manual.

**Structural Integrity:** Sections 1–9 constitute the canonical spine of this manual and are referenced across patent filings, repository documentation, and formal disclosures. These section numbers remain stable. New technical content will be added as:

- Addenda (Addendum B, C, etc.) for substantial new topics that extend the core architecture
- Section 10+ for future chapters that naturally follow the current structure
- Appendices for reference materials, glossaries, and supplementary documentation

Future addenda may include:

• Addendum B: Advanced Classifier Stack Documentation (RSF<sup>TM</sup>, HCL<sup>TM</sup>, CAPF<sup>TM</sup>)

- Addendum C: Phase 3–4 Implementation Specifications
- Addendum D: Field-Specific Licensing Schemas and Deployment Guides
- Addendum E: Integration Case Studies and Reference Implementations
- Addendum F: Validation Results and Academic Peer Review Documentation

All additions will maintain the current structure's public/NDA boundary framework and trademark conventions.

## **Version History:**

- v1.0 (2025-10-06): Initial technical manual structure with Sections 1–9 and Appendices A–F
- v1.2 (2025-10-28): Added Addendum A (Substrate Architecture & Licensing Framework); preserves original section numbering for cross-document consistency