Version List

Version	Change List	Date	Author
1.0.0	First Release	2020-02-25	Mu.Chou Marlon.Zhang
4.0	BT_UART2_CTS/BT_UART2_RTS线序反	2020-02-25	Miller.zhang

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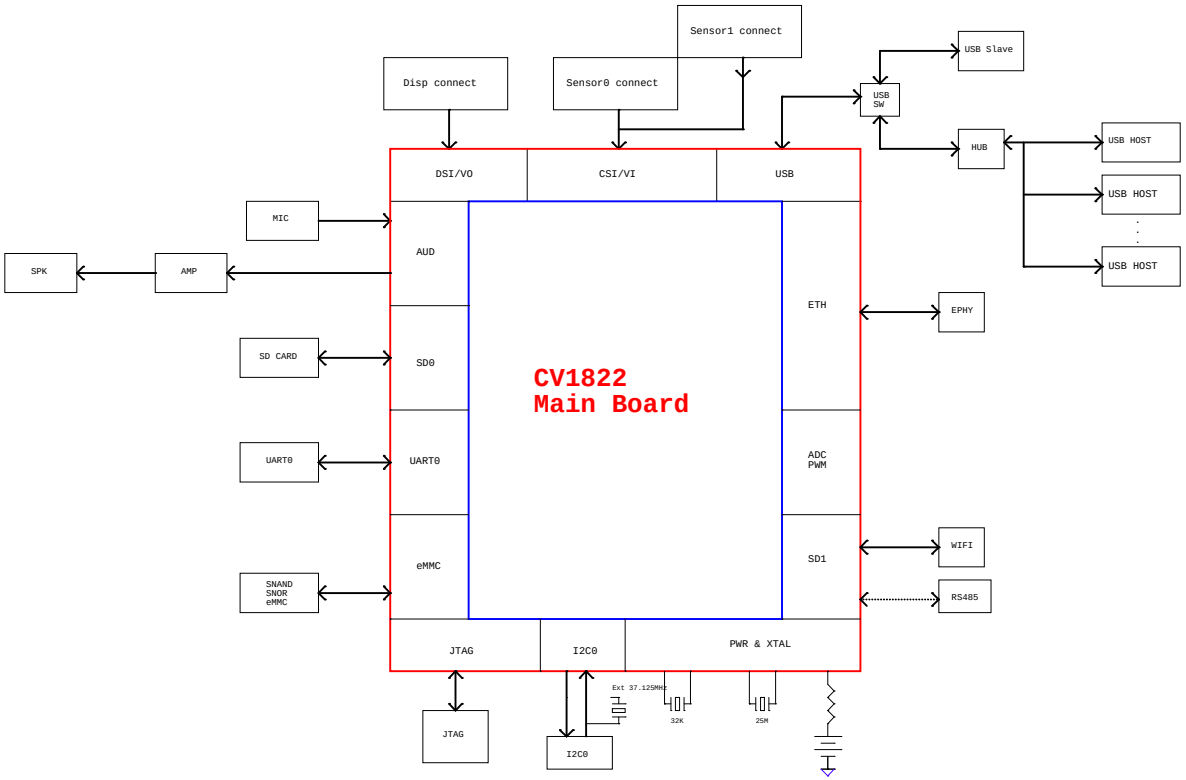
Block Diagram for CV1822



			Plug in Power			Battery		
			Ext RTC	SOC RTC QFN not support	32Kless	Ext RTC	SOC RTC QFN not support	32Kless
0.9V	RB1	SEQ1				V	V	V
	RB12	always on	V	V	V			
1.8V	RB2	SEQ1				V	V	V
	RB13	always on	V	V	V			
VDDQ	RB4	SEQ1				V	V	V
	RB15		V	V	V			
3.3V	RB3	SEQ2	V	V	V	V	V	V
	RB14	always on soc 3.3V switch that need meet power sequence						
VDDIO_RTC	RB6	USE MAIN +1.8V	V	V				
VDDIO_RTC	RB7	USE V18AO				V	V	
VDDIO_RTC	RB9	USE V18RTC			V			V
VDDBKUP	RB8	USE V18AO						
VDDBKUP	RB10	USE V18RTC	V		V	V		V
VDDBKUP	RB11	USE VBACKUP		V			V	
VBACKUP	DB1	fource V33AO				V	V	V
VBACKUP	DB2	fource MAIN_+3.3V	V	V	V			
VAO18		WIFI or Other AO Can Share				V	V	V
VAO33		WIFI or Other AO Can Share				V	V	V
V18RTC_LDO					V			V
Exl RTC			V			V		
SOC_RTC				V			V	

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File	02_Diagram	
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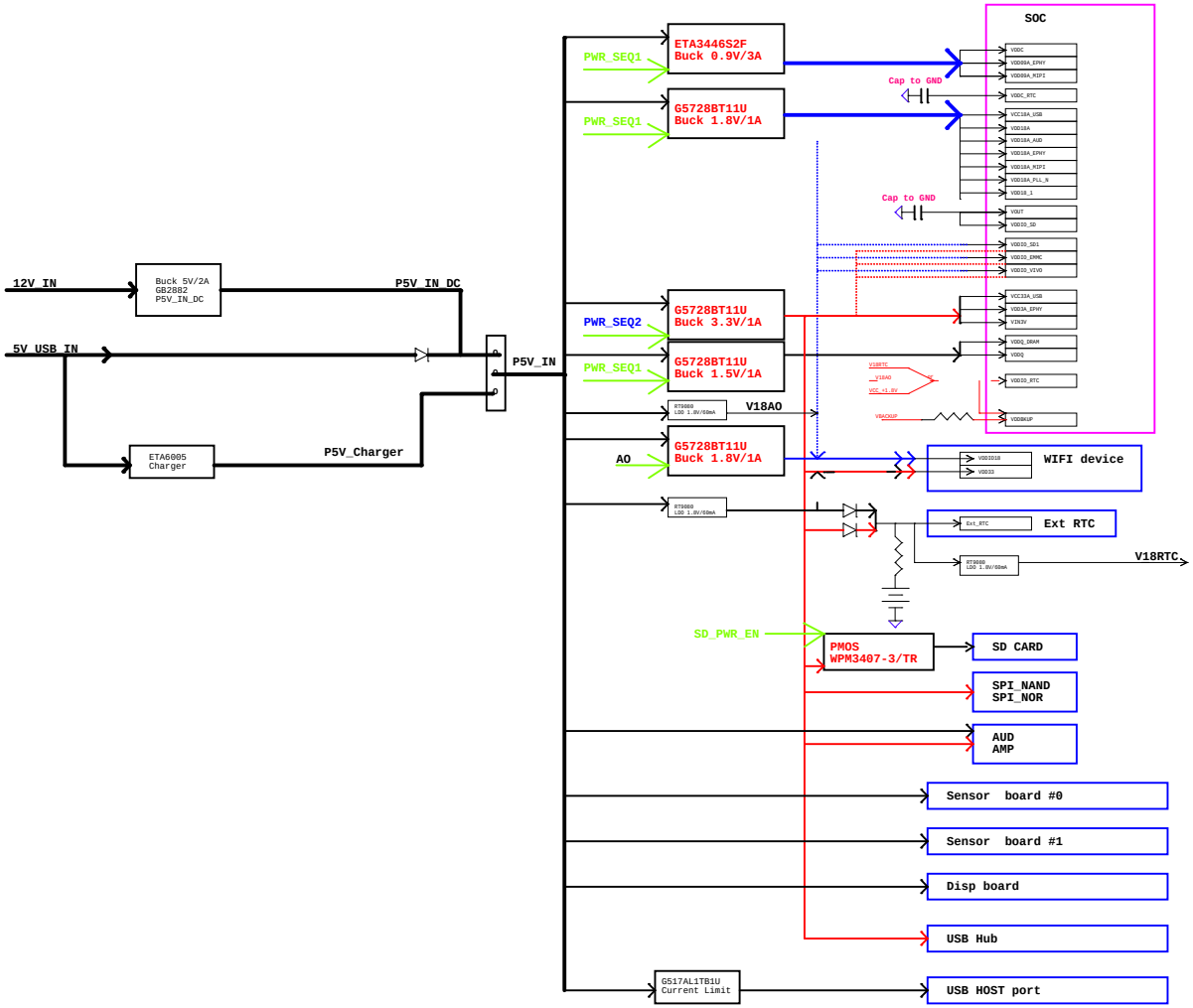

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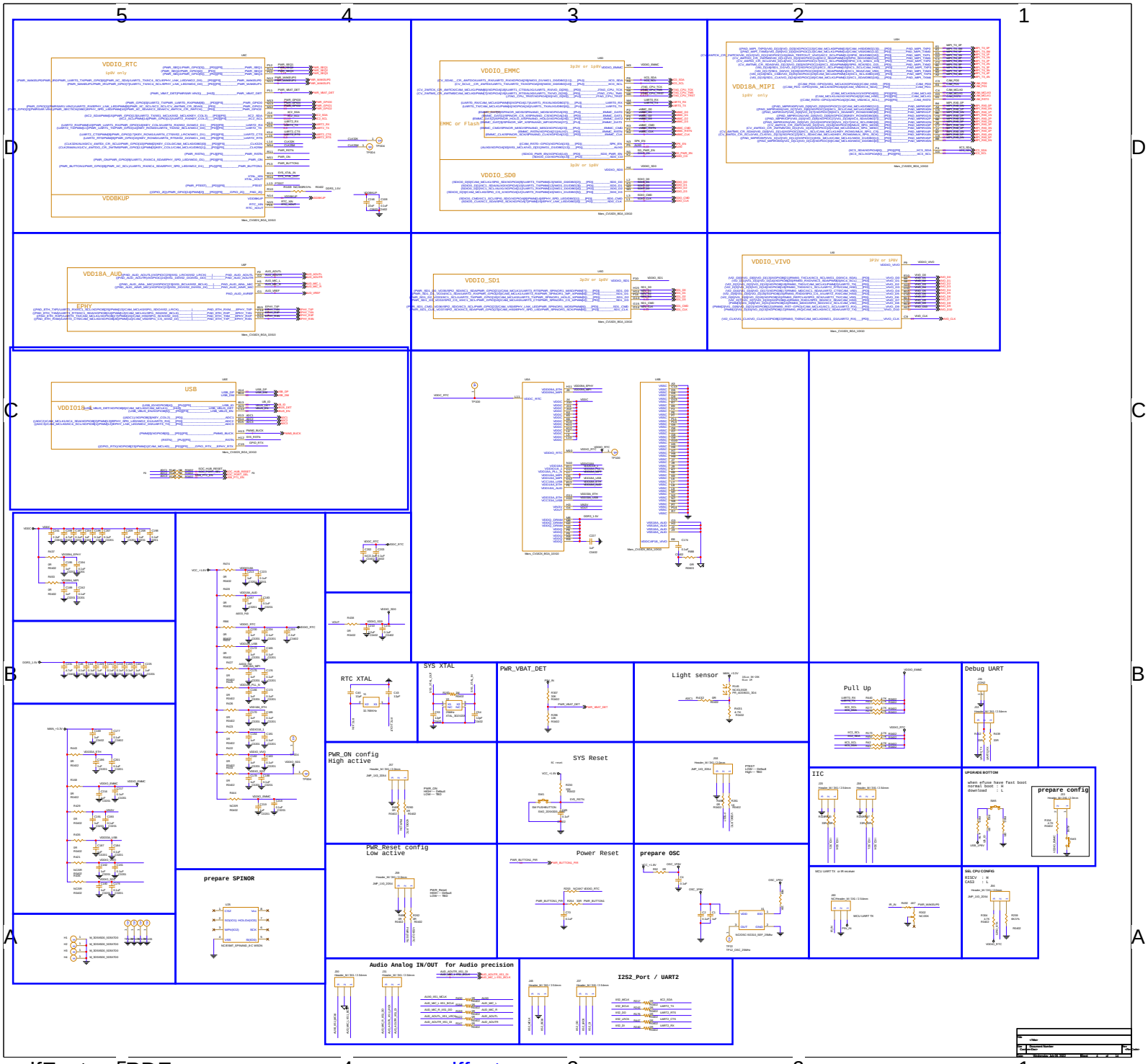

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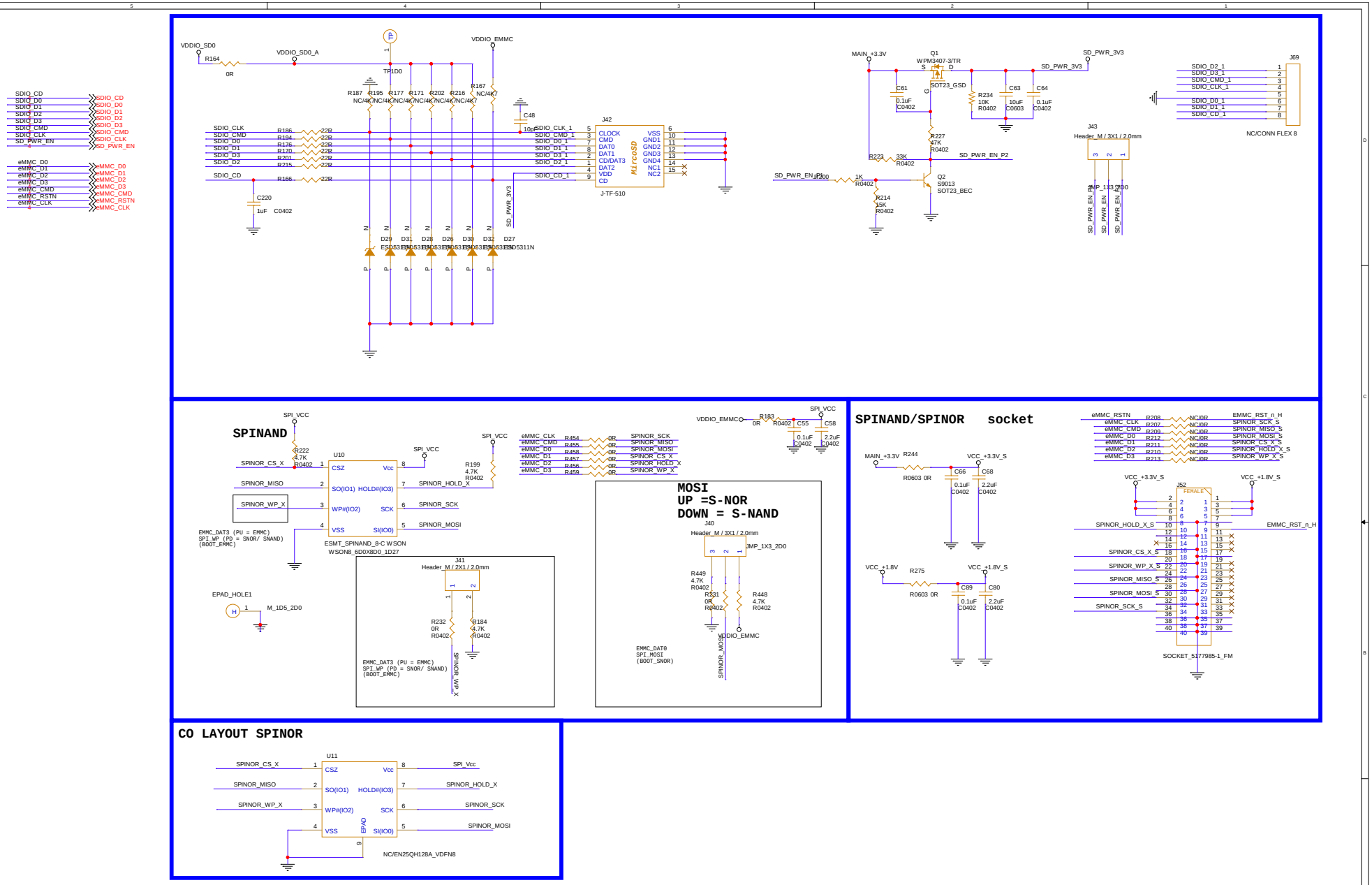

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Title	06_eMMC_SDIO_SPIFLASH		
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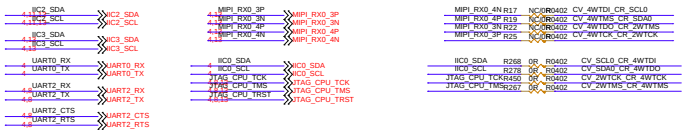
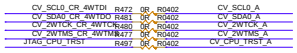
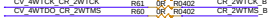
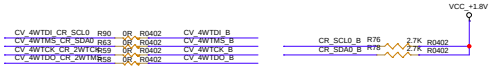


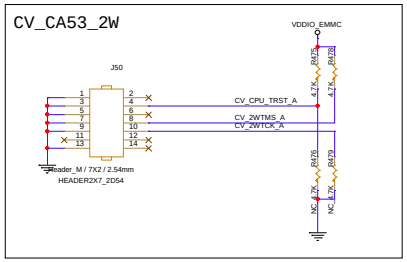
JTAG_0

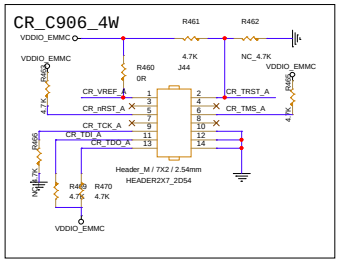


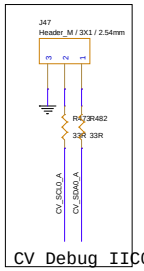
JTAG_1

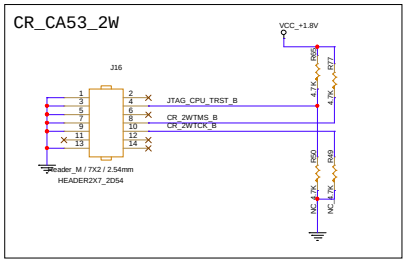


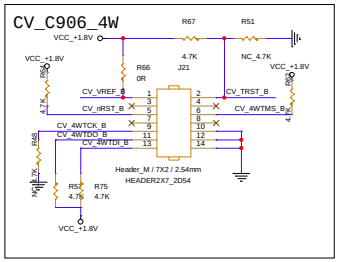


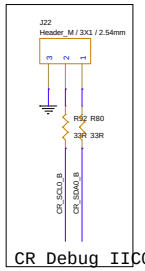


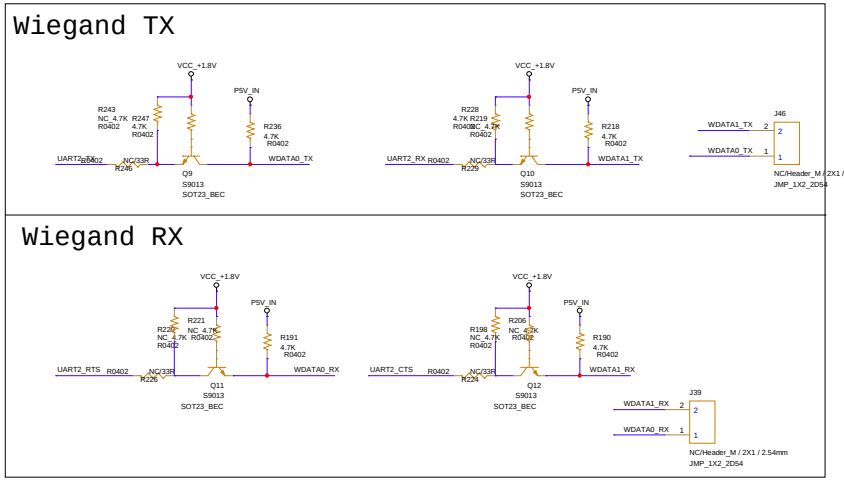


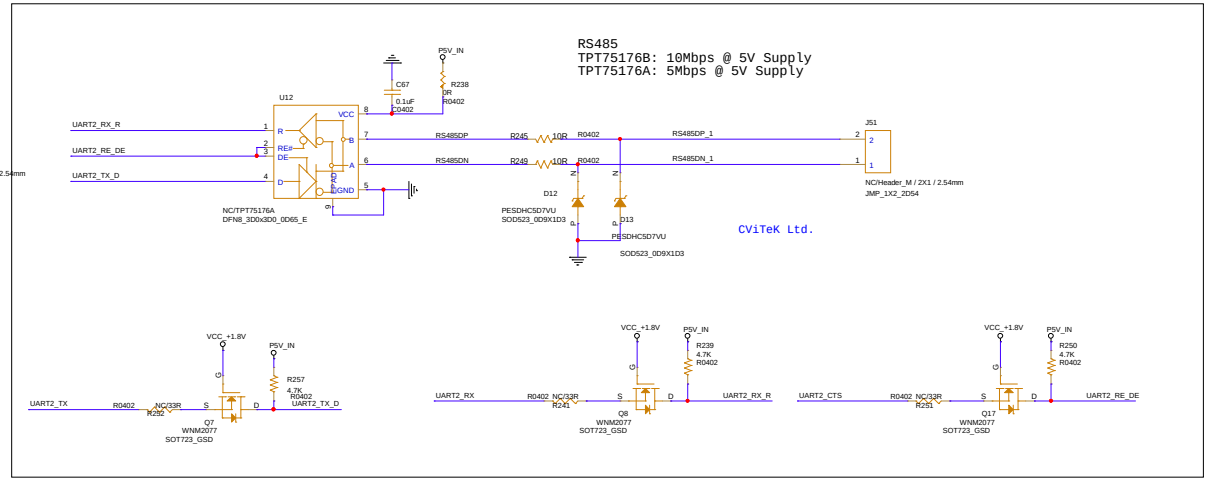












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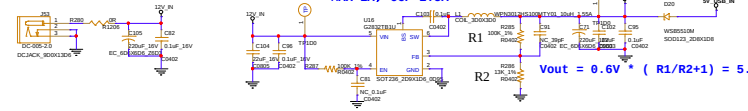
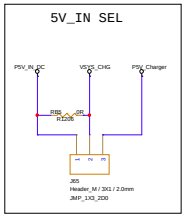
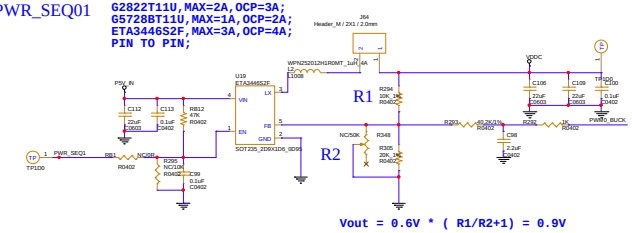




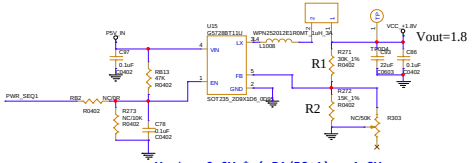
PWR_SEQ01

G2822T11U,MAX=2A,OCP=3A;
G5728BT11U,MAX=1A,OCP=2A;
ETA3446S2F,MAX=3A,OCP=4A;
PIN TO PIN;



R2 >20k, for stability consideration.

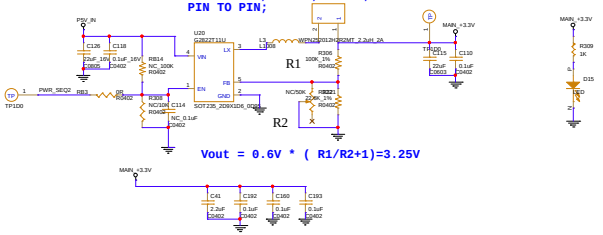
G2822T11U,MAX=2A,OCP=3A;
G5728BT11U,MAX=1A,OCP=2A;
ETA3446S2F,MAX=3A,OCP=4A;
PIN TO PIN;



P5V_IN-->PWR_SEQ01-->PWR_SEQ02-->PWR_SEQ03

PWR_SEQ02

G2822T11U,MAX=2A,OCP=3A;
G5728BT11U,MAX=1A,OCP=2A;
ETA3446S2F,MAX=3A,OCP=4A;
PIN TO PIN;

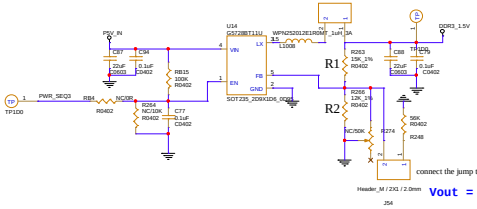


PWR_SEQ03

G2822T11U,MAX=2A, OCP=3A;
G5728BT11U,MAX=1A, OCP=2A;
ETA3446S2F,3A, OCP=4.5A;
PIN TO PIN;

Vin-->DDR31P35

Note
VDDQ_POWER
Vout=1.5V



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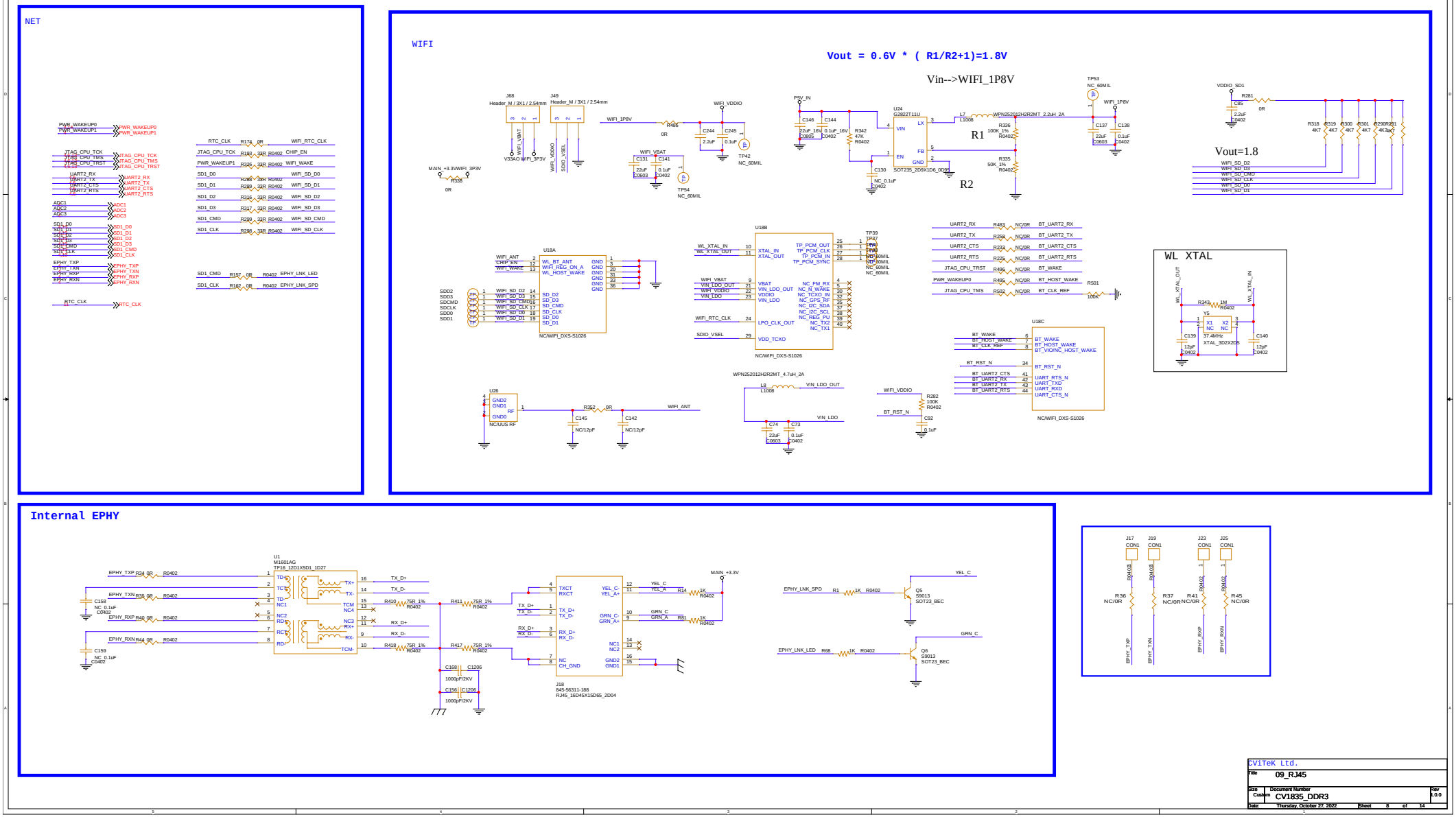

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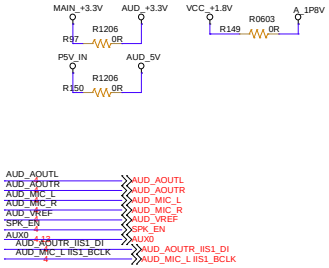

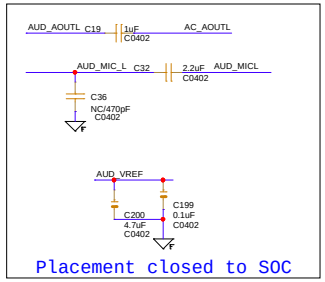

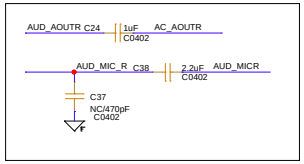

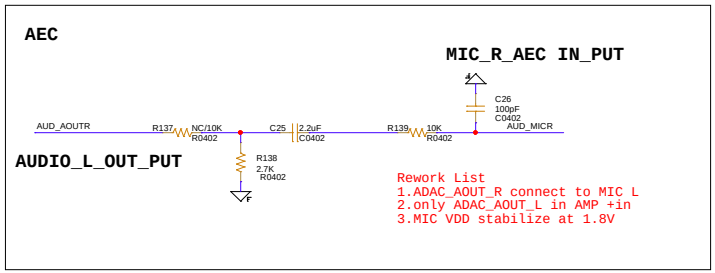

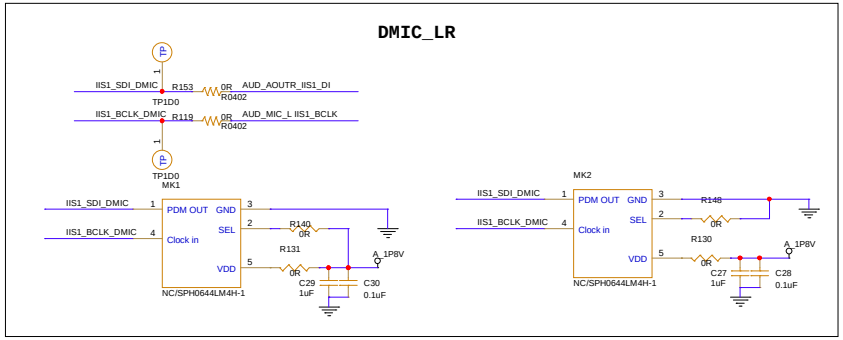

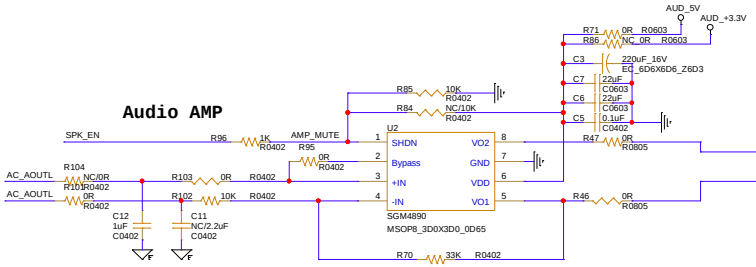

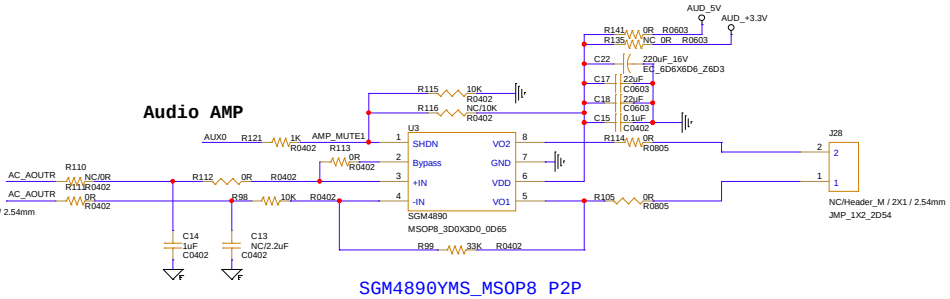

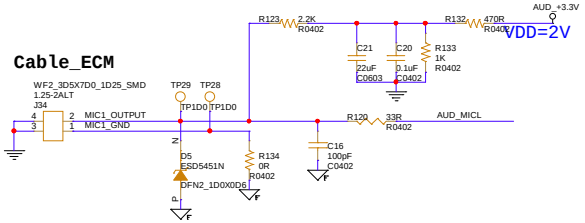

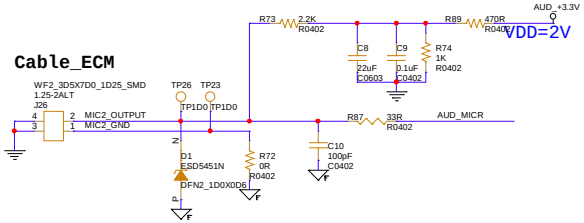

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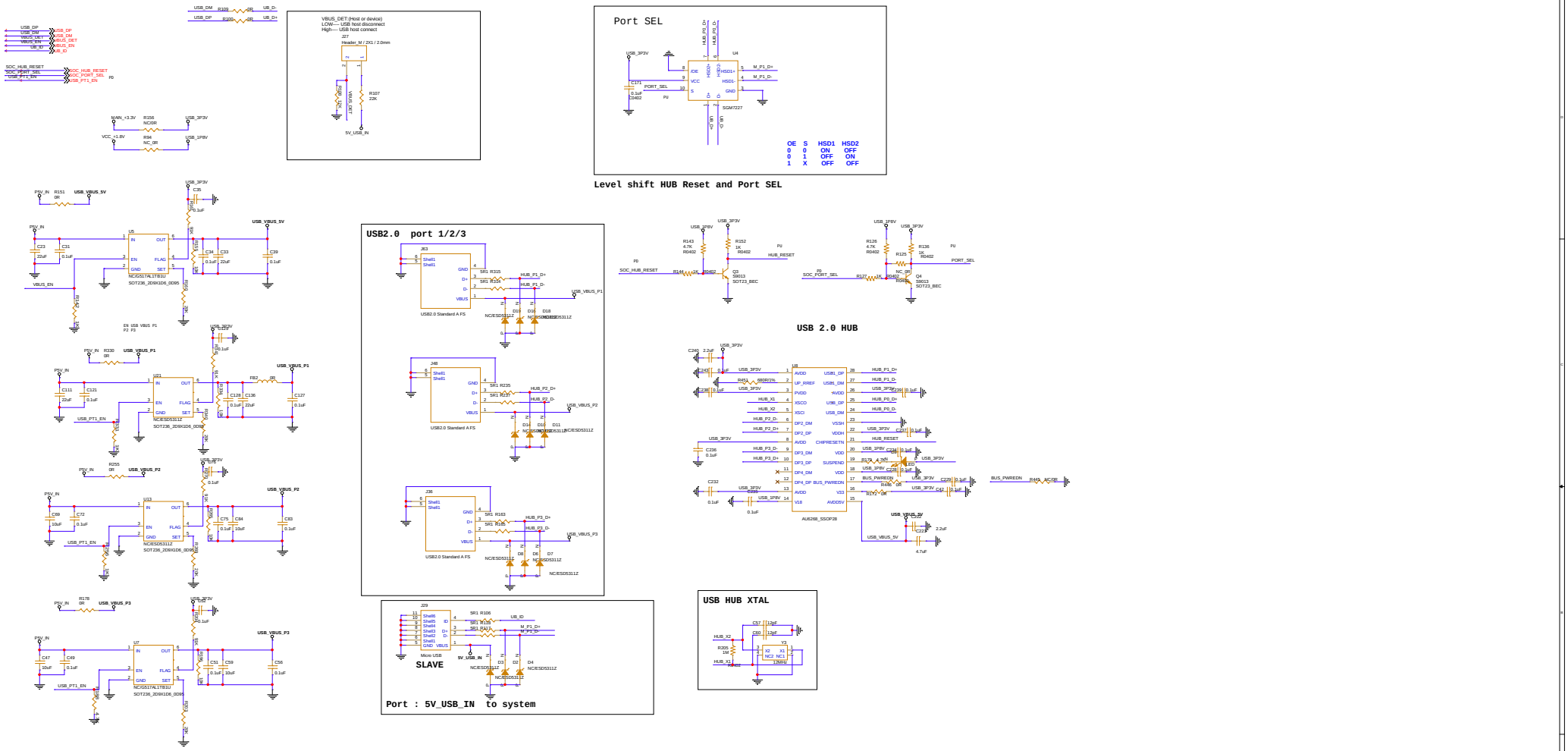

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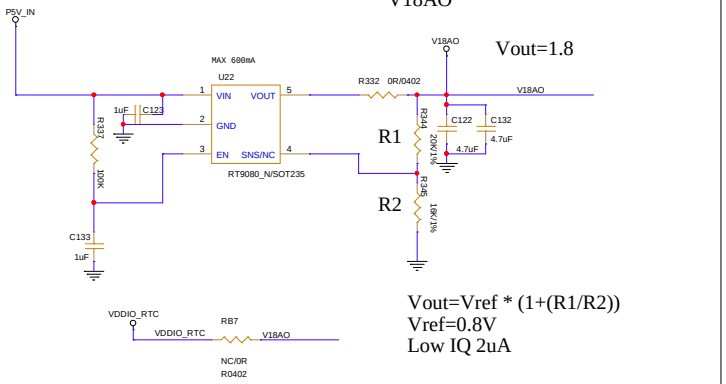
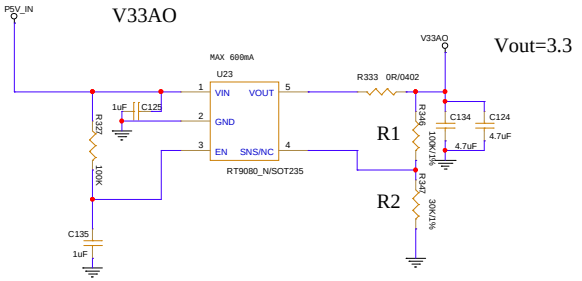
V18AO



Vout=1.8

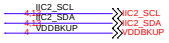
Vout=Vref * (1+(R1/R2))
Vref=0.8V
Low IQ 2uA

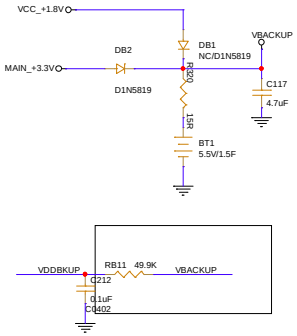
V33AO

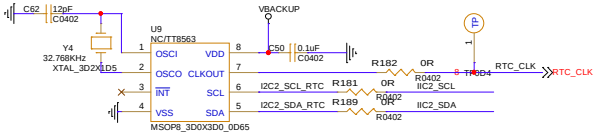


Vout=3.3

Vout=Vref * (1+(R1/R2))
Vref=0.8V
Low IQ 2uA







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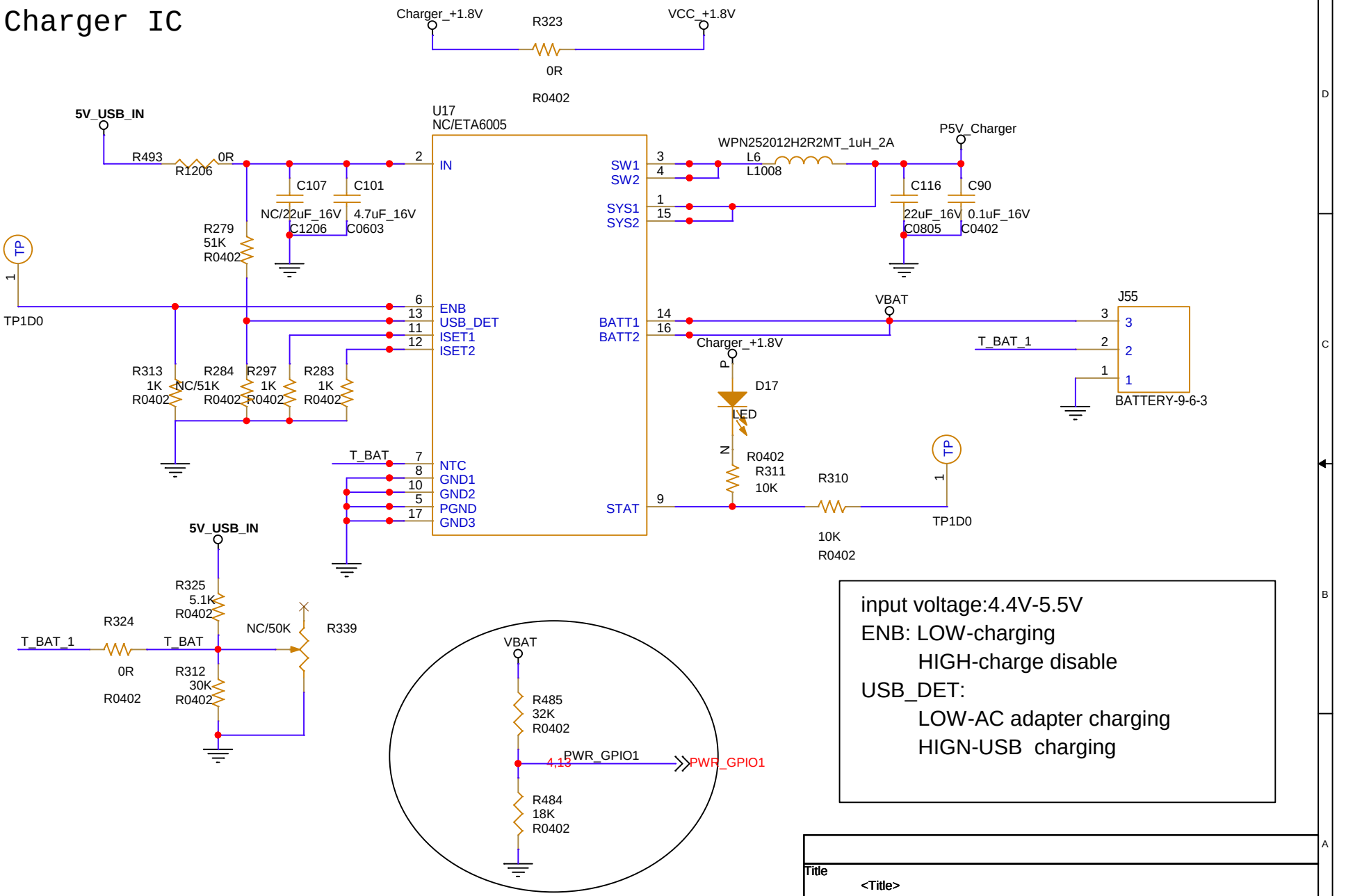
Charger IC



input voltage:4.4V-5.5V
ENB: LOW-charging
HIGH-charge disable
USB_DET:
LOW-AC adapter charging
HIGN-USB charging

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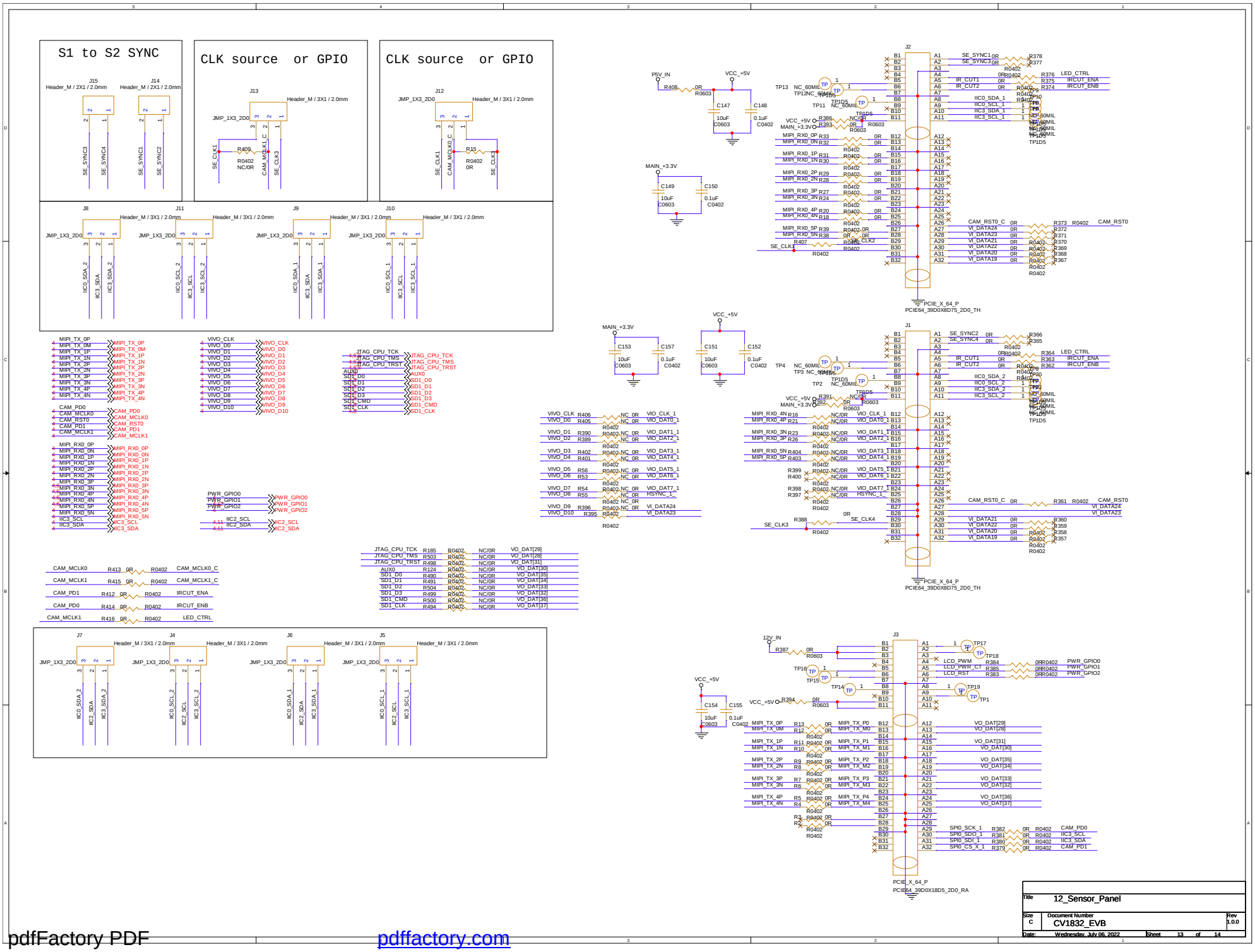

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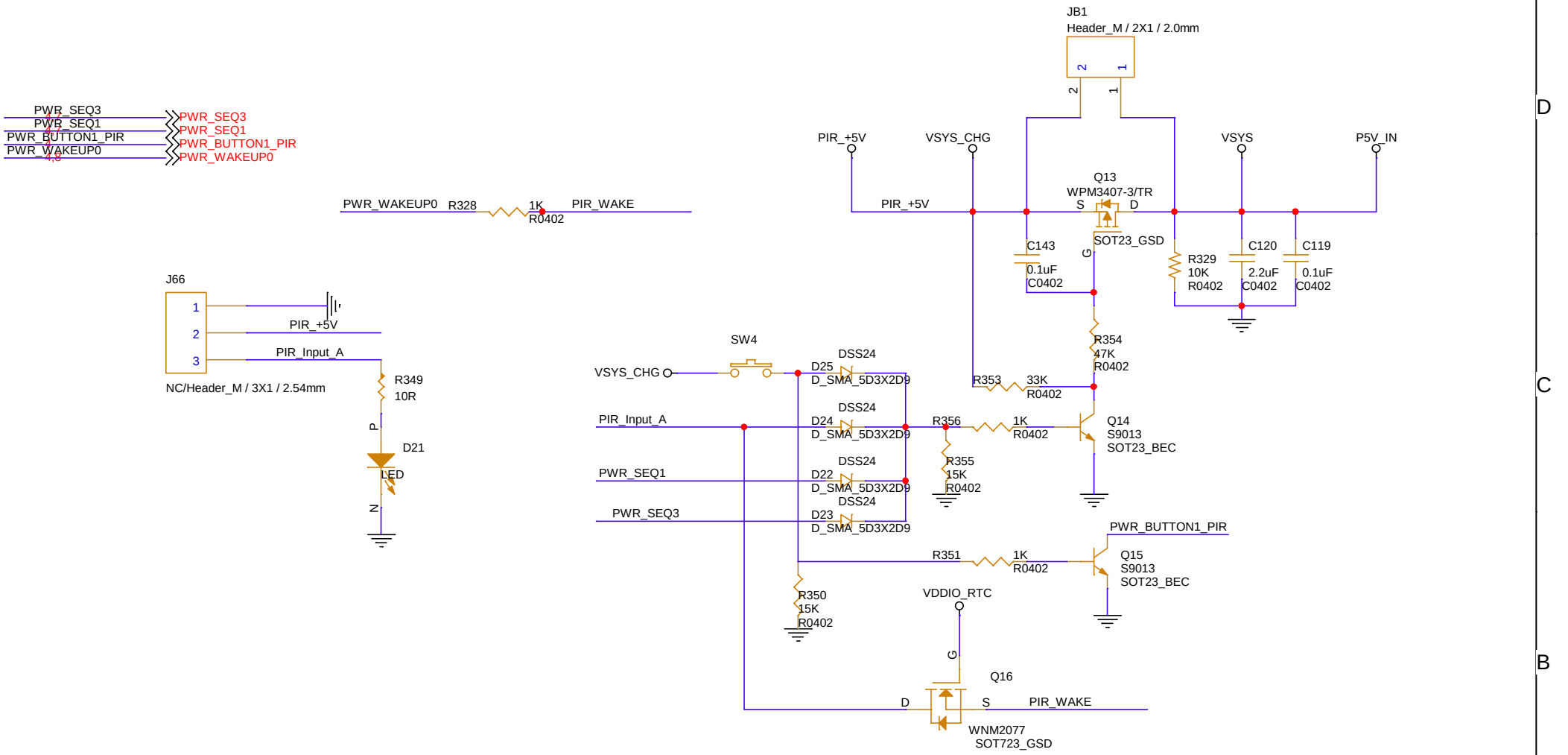

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