Version List

Version	Change List	Date	Author
1.0.0	First Release	2022-3-21	Miller.zhang
1.1.0	1、C91,C93 NC 2、增加R1 3、增加R41,R43	2022-6-10	Miller.zhang
1.2.0	1、C205从10uF改为22uF	2023-3-1	Miller.zhang

Title	01_Cover	
Size: Custom	Document Number: CV181xH_SIP_BGA	Rev: 1.0.0
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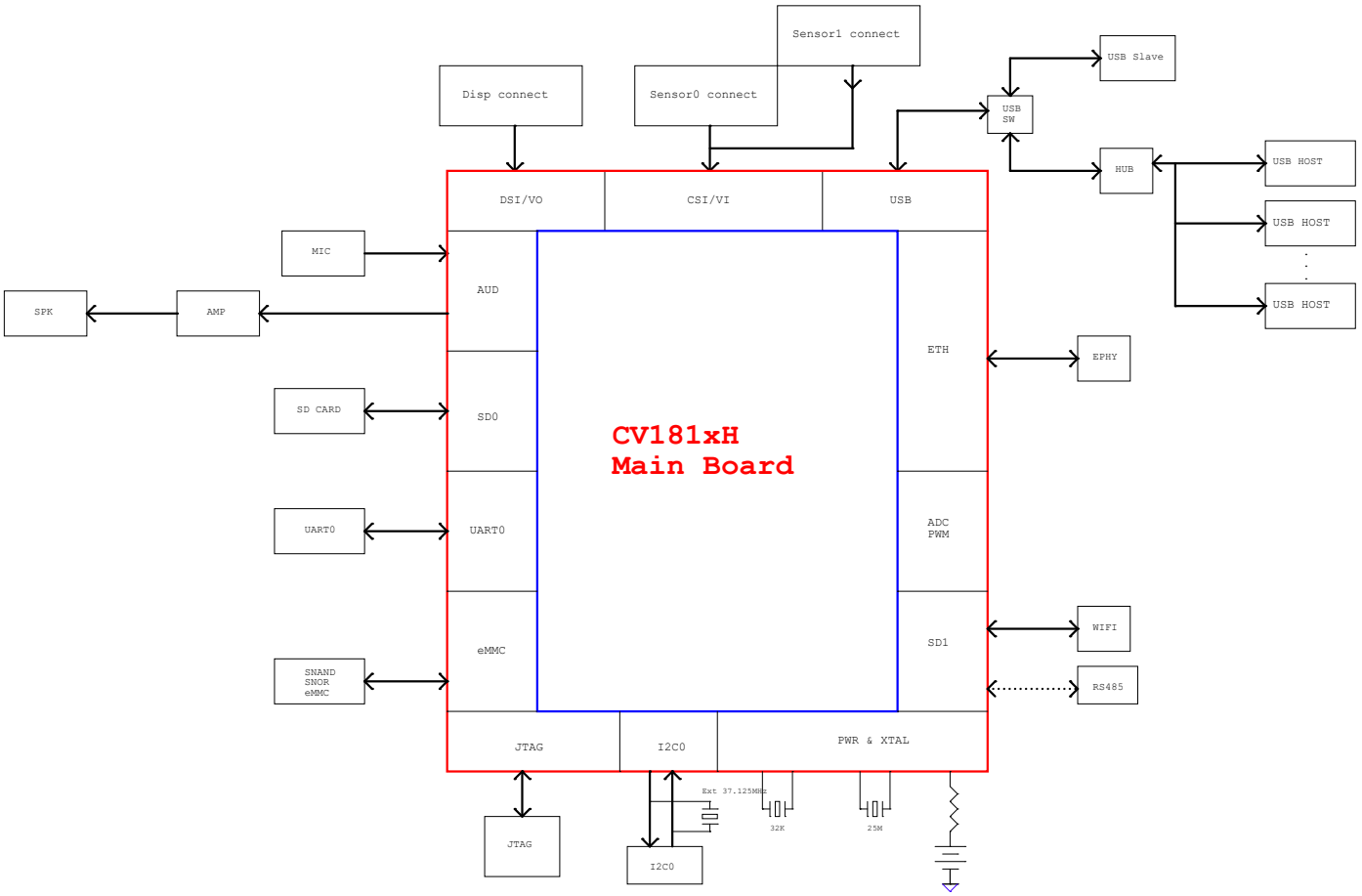
Block Diagram for CV181xH



CVITEK		
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Size: C	Document Number: CV181xH_SIP_BGA	Rev: 0.0.0
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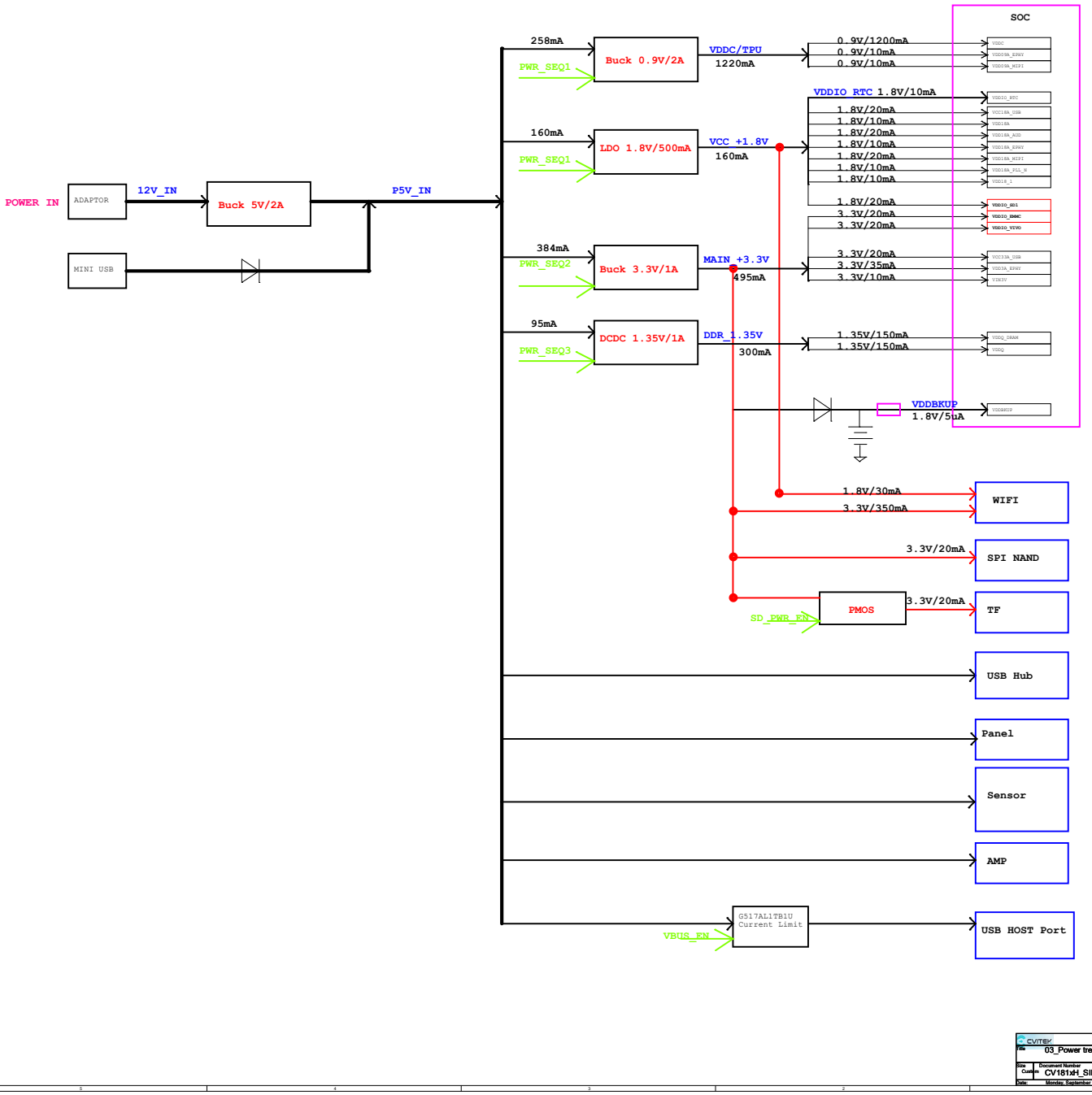

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Size:	Document Number: CV181xH_SIP_BGA		Rev: 0.0.0
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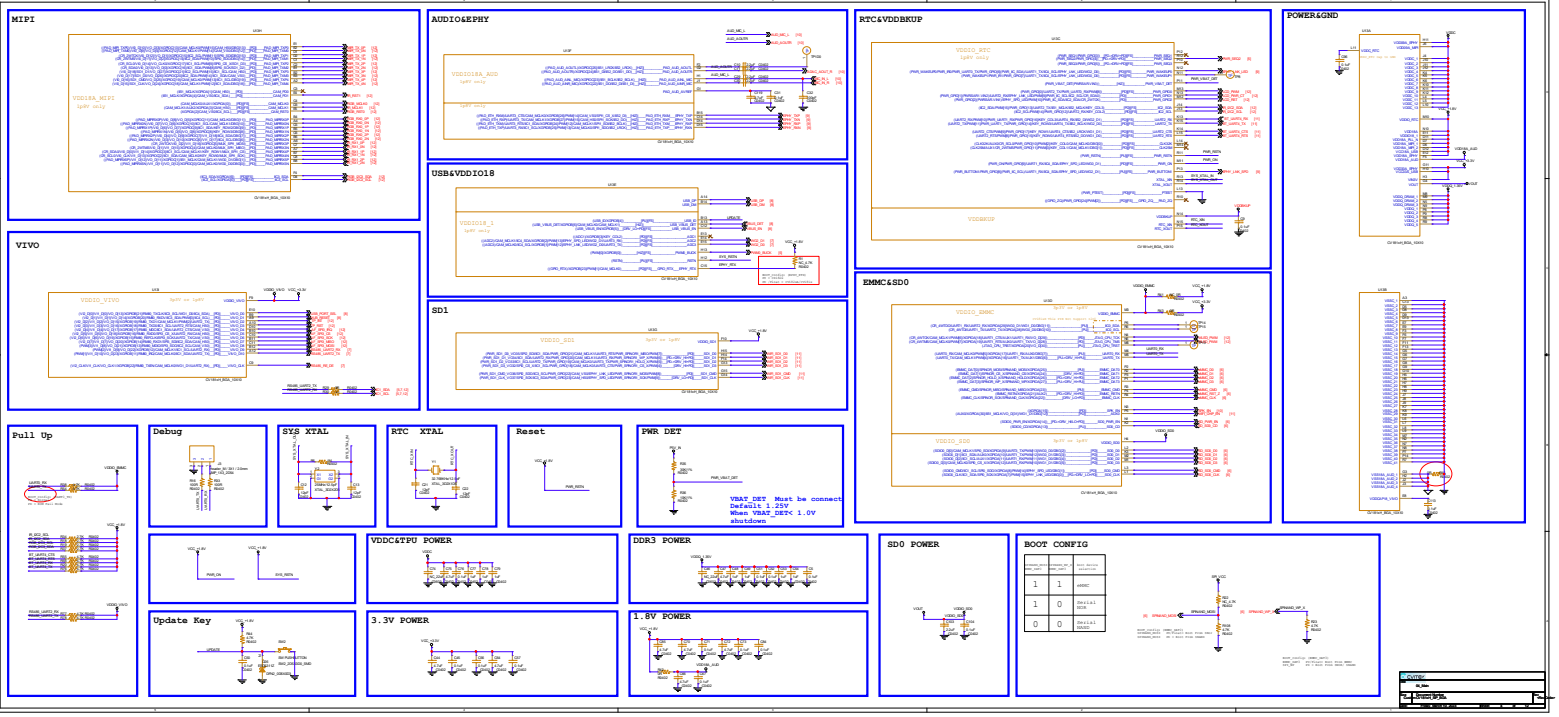
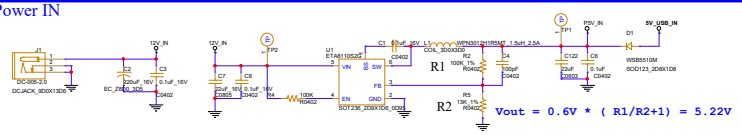
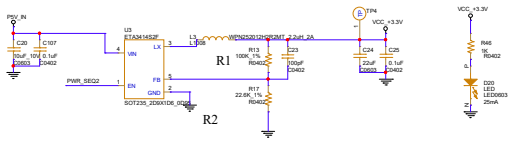
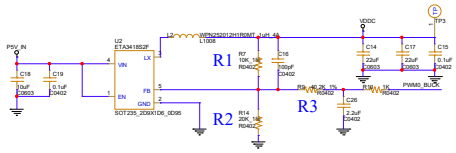

MIPI
AUDIO&EPHY
RTC&VDDBKUP
POWER&GND
VIVO
USB&VDDIO18
SD1
EMMC&SD0
Pull Up
Debug
SYS XTAL
RTC XTAL
Reset
PWR DET
VBAT_DET Must be connect
Default 1.25V
When VBAT_DETC 1.0V
shutdown
VDDC&TPU POWER
DDR3 POWER
SD0 POWER
BOOT CONFIG
Update Key
3.3V POWER
1.8V POWER

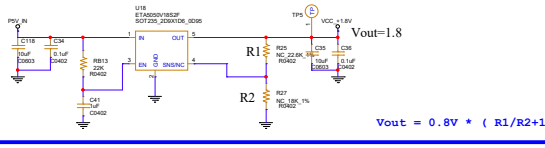
Power IN



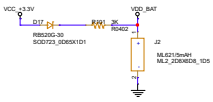
PWR_SEQ02



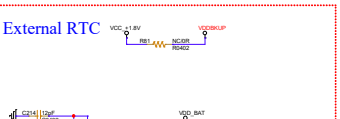




RTC



External RTC



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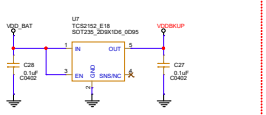
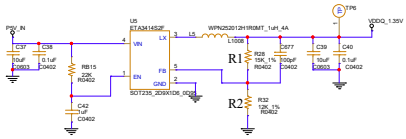


CV1810H:64MB DDR2,VDDQ=1.5V
CV1811H:128MB DDR3,VDDQ=1.35V
CV1812H:256MB DDR3,VDDQ=1.35V
CV1813H:512MB DDR3,VDDQ=1.35V



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File	05_SYS_Power
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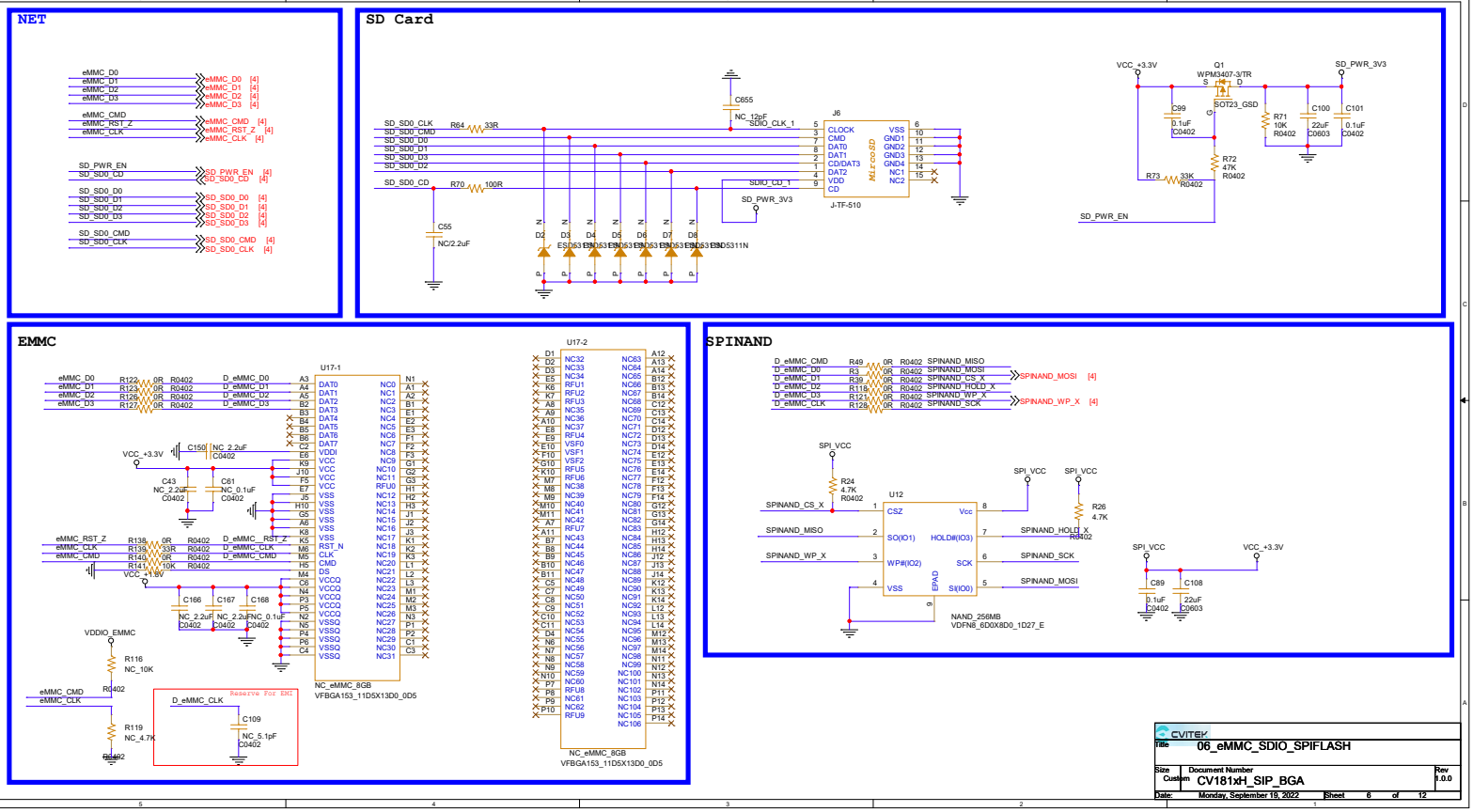

Title	06_eMMC_SDIO_SPIFLASH		
Size	Document Number		Rev
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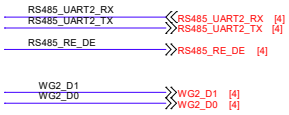
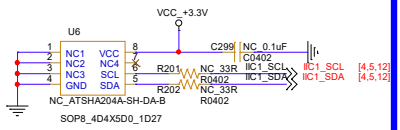
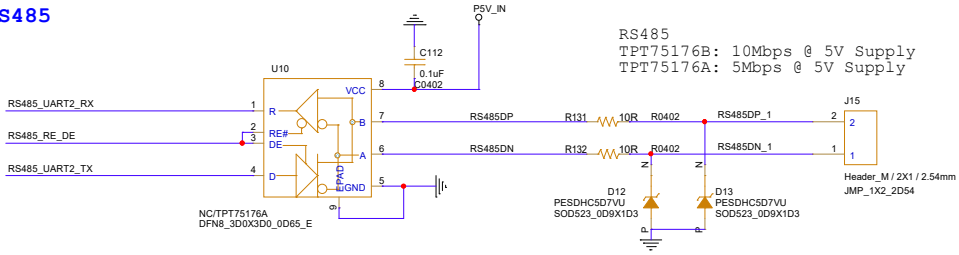


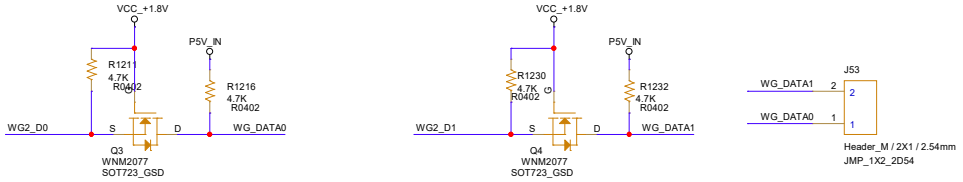
Encryption



RS485



Weigand



Title	07_UART_WG	
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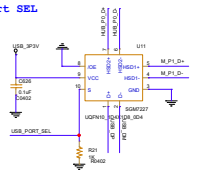
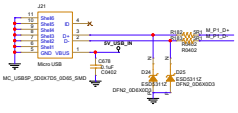
Port SEL

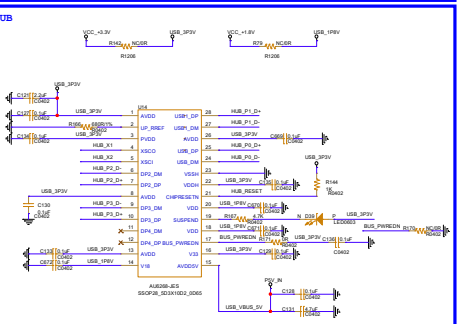


OE	S	HSD1	HSD2
0	0	ON	OFF
0	1	OFF	ON
1	X	OFF	OFF

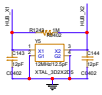


SLAVE

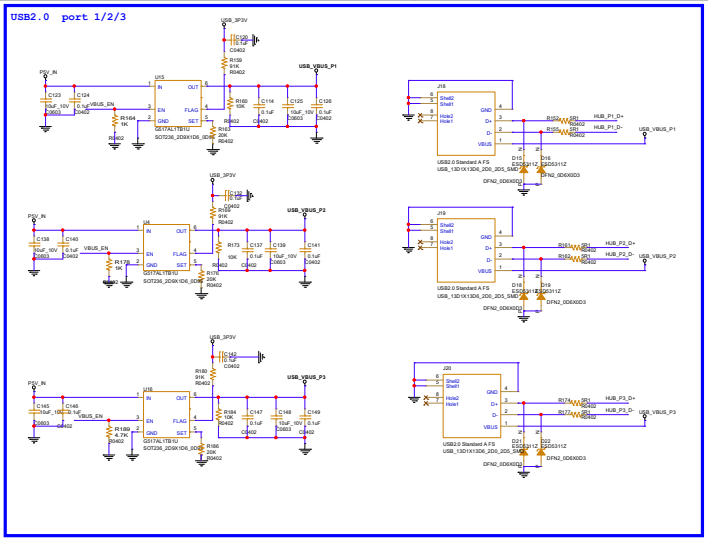
USB 2.0 HUB

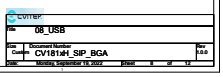


USB HUB XTAL



USB2.0 port 1/2/3





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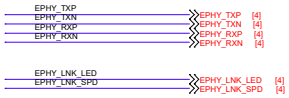
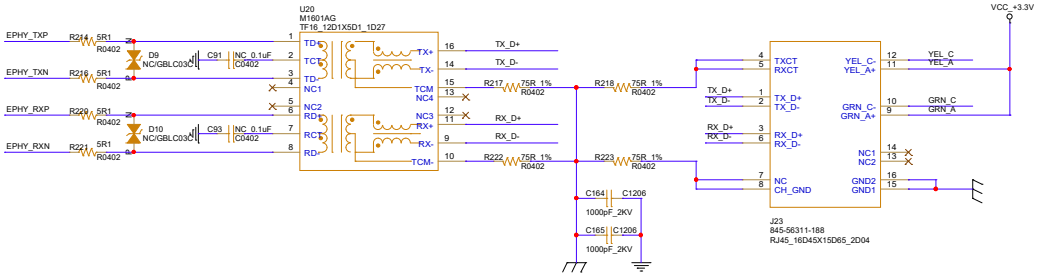
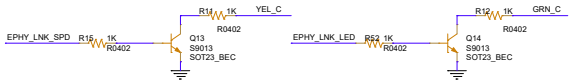


ETH





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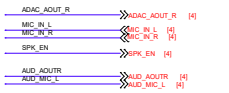
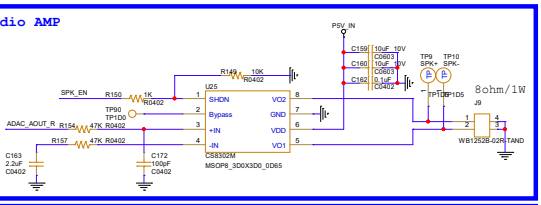
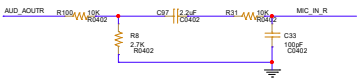


Audio AMP



AEC



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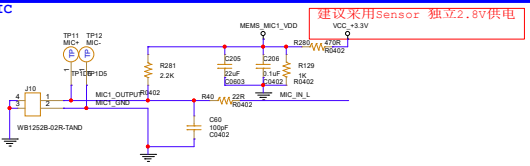

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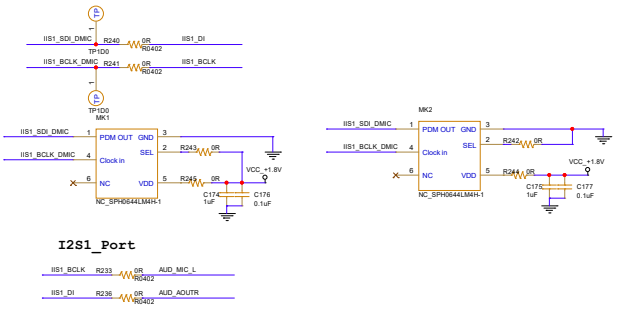

I2S1_Port

CVITEK

Title	10_AUDIO	
Size: Custom	Document Number: CV181xH_SIP_BGA	Rev: 1.0.0
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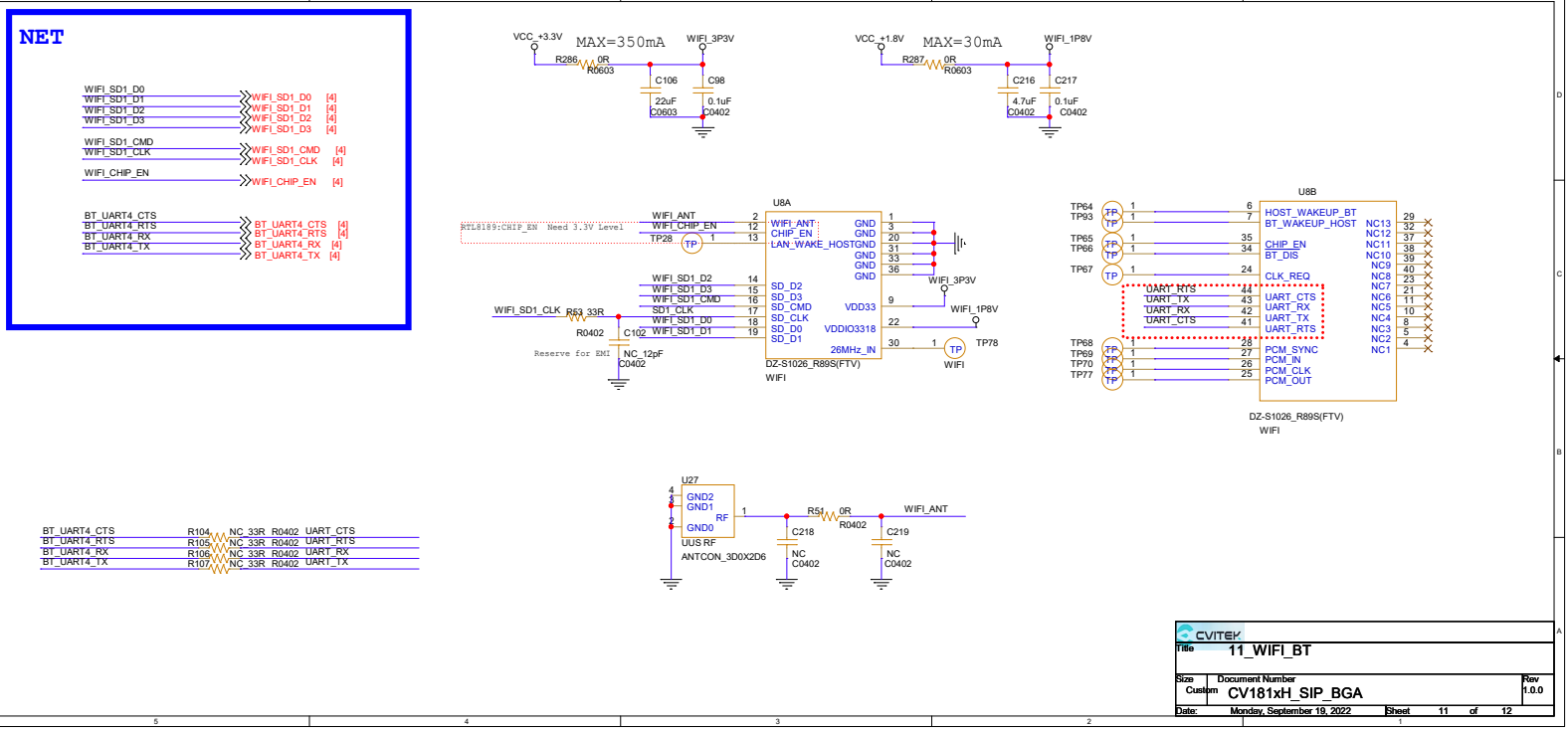

CVITEK

Title	11_WIFI_BT		
Size: Custom	Document Number: CV181xH_SIP_BGA		Rev: 1.0.0
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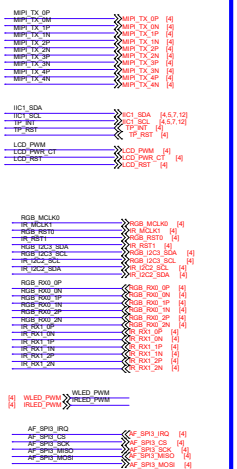

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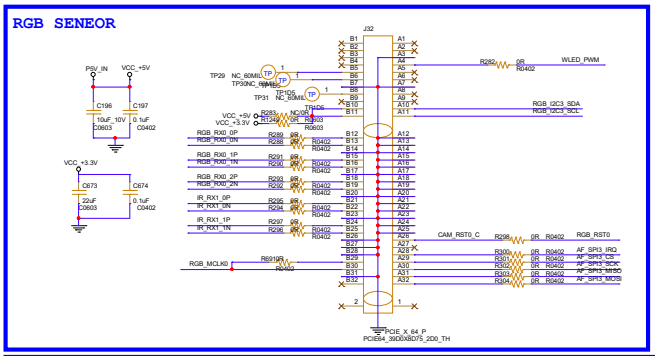

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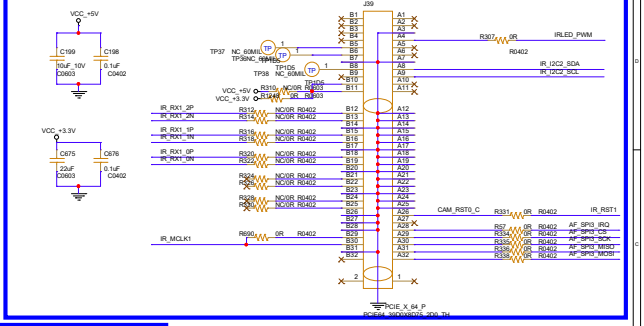
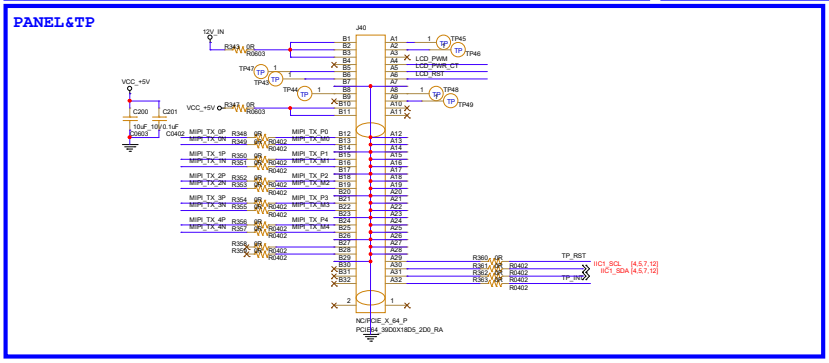


PANEL&TP



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Size	Document Number		Rev
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