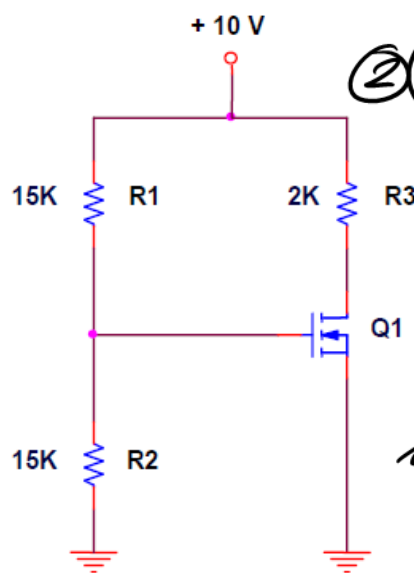
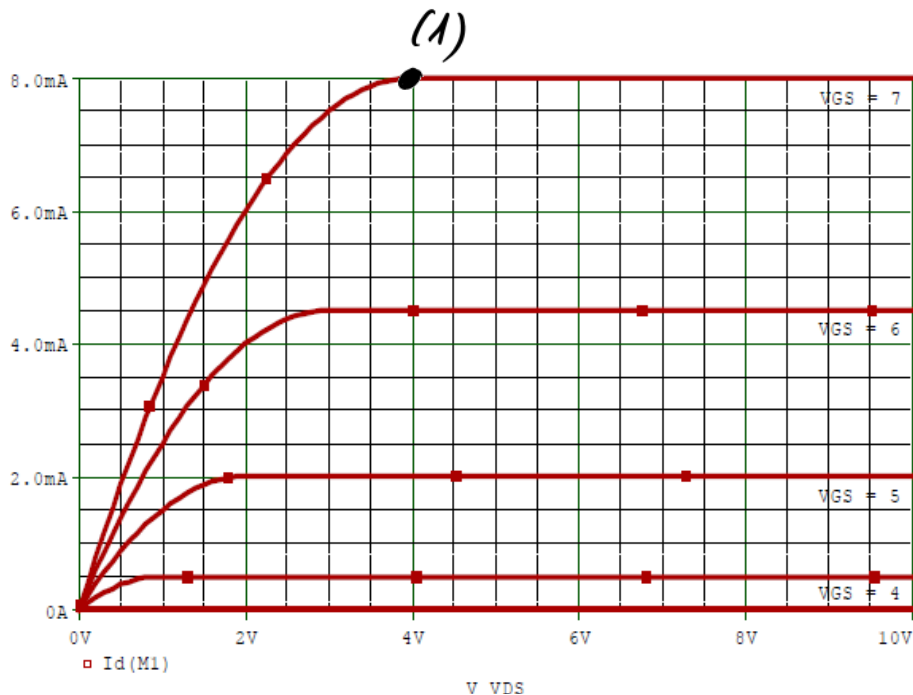


UNIT 2. MOSFETS: OPERATING POINT

3.6 The MOS transistor in the circuit of Figure B has a characteristic curve as shown in Figure A. Answer the following questions.



② V_T Easy way
 ΔV_{GS} of curves: 1V
 $\Rightarrow V_T = 3V$
 More accurate:
 point (1) is in
 limit OHMIC-SAT:
 $V_{DS} = V_{GS} - V_T \Rightarrow$
 $V_T = V_{GS} - V_{DS} = 7 - 4$
 $= 3V$

③ K Saturation

$$I_{DS} = K(V_{GS} - V_T)^2 \Rightarrow$$

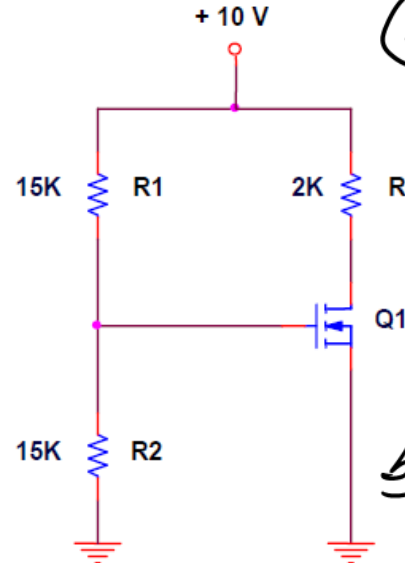
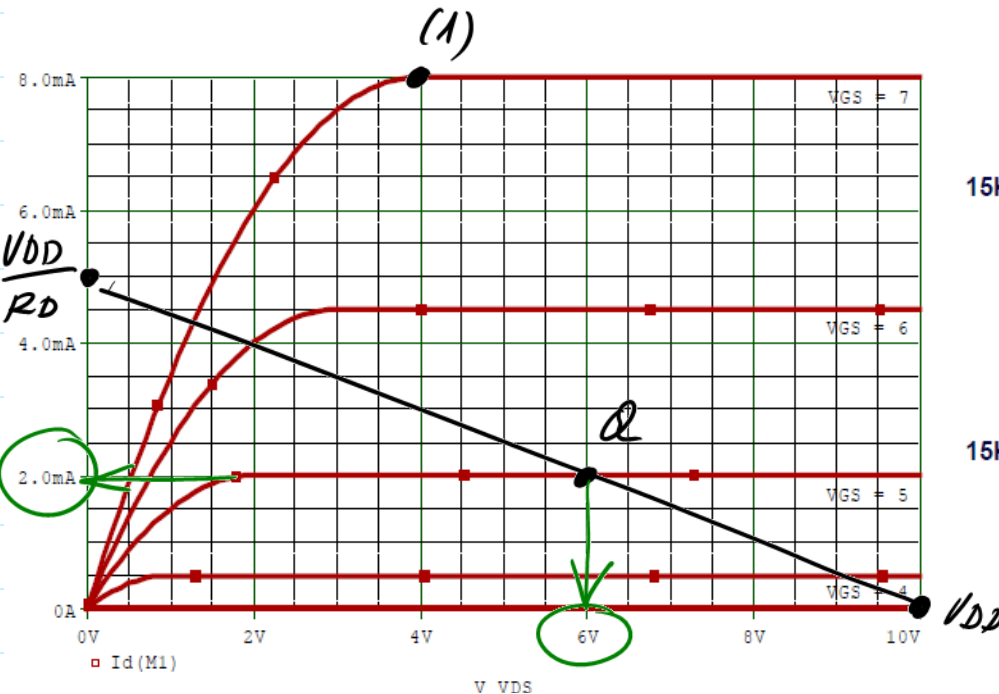
$$K = \frac{I_{DS}}{(V_{GS} - V_T)^2} = \frac{8}{(7 - 3)^2} = 0.5 \frac{mA}{V^2}$$

④ $V_{GS} = 10 \frac{R_2}{R_1 + R_2} = 5V$

QUESTIONS	ANSWERS
1. Indicate the type of transistor:	n
2. V_T value	3 (Volts)
3. K value	0.5 (mA/V ²)
4. Obtain the voltage V_{GS} in the circuit.	5V (Volts)
5. Obtain the current I_D .	(mA)
6. Obtain the voltage V_{DS} .	(Volts)
7. ¿What is the limit value of R_3 to put the MOSFET in the ohmic region?	(k Ω)

UNIT 2. MOSFETS: OPERATING POINT

3.6 The MOS transistor in the circuit of Figure B has a characteristic curve as shown in Figure A. Answer the following questions.



5) a) Load line:

X axis $\rightarrow V_{DD} = 10V$

Y axis $\rightarrow \frac{V_{DD}}{R_D} = 5mA$

Q point ($I_{DS} = 2mA$
 $V_{DS} = 6V$)

b) analytically:

$V_{GS} = 5V > V_T \Rightarrow Q_{ON}$

assume sat \Rightarrow

$$I_{DS} = K (V_{GS} - V_T)^2 = 0.5(5 - 3)^2$$

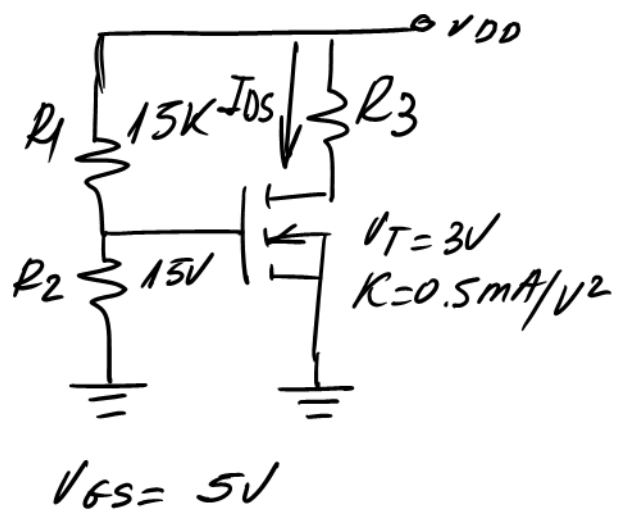
$$I_{DS} = 2mA$$

$$V_{DS} = 10 - I_{DS} \cdot R_3 = 6V$$

As $V_{DS} > V_{GS} - V_T \Rightarrow Q_{SAT}$

and Q point is
Q ($V_{GS} = 5V$; $I_{DS} = 2mA$; $V_{DS} = 6V$)

QUESTIONS	ANSWERS
1. Indicate the type of transistor:	n
2. V_T value	3 (Volts)
3. K value	0.5 (mA/V ²)
4. Obtain the voltage V_{GS} in the circuit.	5 (Volts)
5. Obtain the current I_D .	2 (mA)
6. Obtain the voltage V_{DS} .	6 (Volts)
7. ¿What is the limit value of R_3 to put the MOSFET in the ohmic region?	4 (k Ω)



⑦ limit OHMIC - SAT

a) $V_{DS} = V_{GS} - V_T = 5 - 3 = 2V$

b) $I_{DS} = K (V_{GS} - V_T)^2 = 0.5(5-3)^2 = 2 \text{ mA}$

From output loop:

$$V_{DD} = R_3 I_{DS} + V_{DS} \Rightarrow R_3 = \frac{V_{DD} - V_{DS}}{I_{DS}}$$

$$R_3 = \frac{10 - 2}{2} = 4 \text{ k}$$

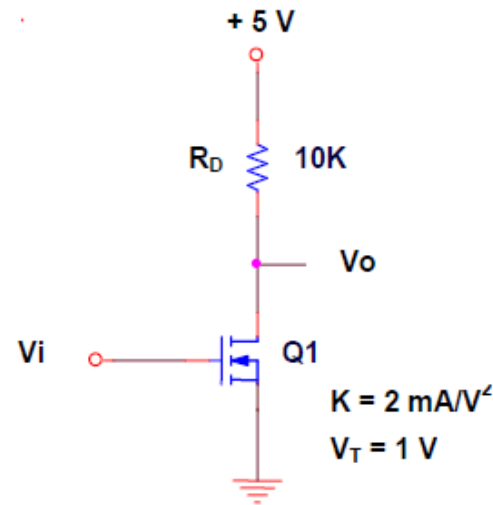
$R_3 > 4 \text{ k} \Rightarrow \text{ohmic}$

$R_3 < 4 \text{ k} \Rightarrow \text{saturation}$

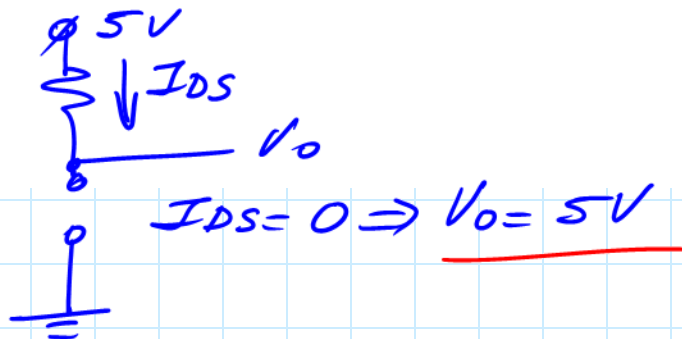
UNIT 2. MOSFETS: OPERATING POINT

5.1 Indicate the output voltage levels of the logic inverter of figure if V_i is a square wave with values from 0V to 5V. Use the approximate expression for the ohmic region of transistor: $I_{DS} \approx 2K(V_{GS} - V_T)V_{DS}$.

- [A] 5 V y 1 V
- [B] 5 V y 0.03 V**
- [C] 5 V y 0.5 V
- [D] 2.5 V y 0.5 V



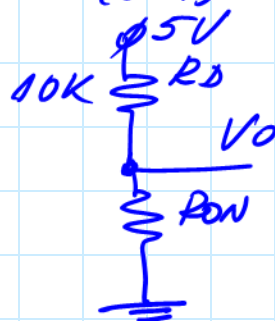
a) $V_i = 0V < V_T \Rightarrow Q_1 \text{ OFF} \Rightarrow$



b) $V_i = 5V > V_T \Rightarrow Q_1 \text{ ON}; \text{ assume OHMIC} \Rightarrow R_{ON} = \frac{1}{2K(V_{GS} - V_T)}$

$$R_{ON} = \frac{1}{2 \cdot 2(5-1)} = 0.0625K$$

$$\boxed{V_o = 5 \frac{R_{ON}}{R_D + R_{ON}} = 0.031V = V_{OS}}$$



as $V_{DS} = 0.031 < V_{GS} - V_T = 4V \Rightarrow Q_1$
is in ohmic (linear) region

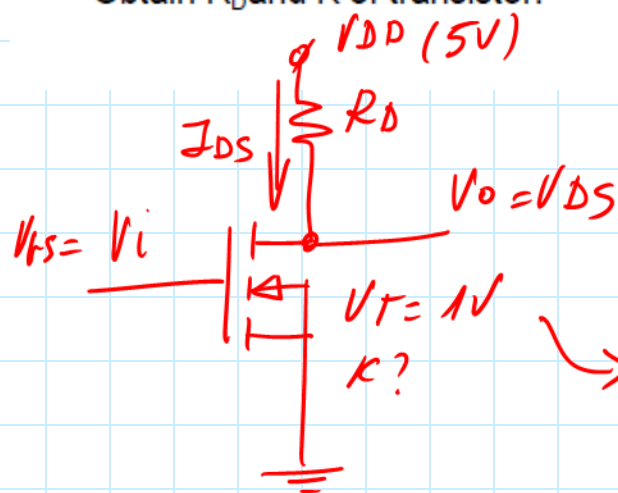
UNIT 2. MOSFETS: OPERATING POINT

(5.8) Design a NMOS inverter with a pull-up resistor R_D , with the following requirements:

- Static power consumption when the output is low = 0.10 mW
- $V_{OL} = 0.5V$
- $V_{OH} = V_{DD} = 5V$
- Transistor: $V_T = 1V$

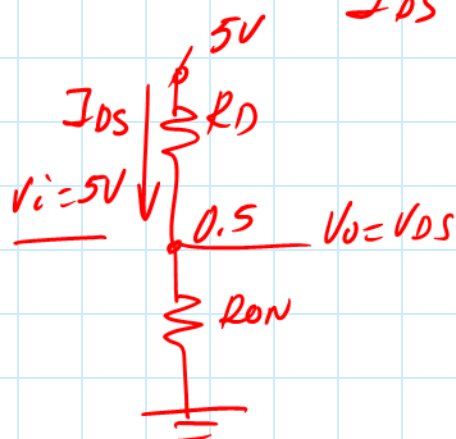
$$P_L = V_{DD} \cdot I_{DS}, \quad I_{DS} = \frac{P_L}{V_{DD}} = \frac{0.1}{5} = 0.02 \text{ mA}$$

Obtain R_D and K of transistor.



$$\textcircled{2} \quad V_{DD} - I_{DS} R_D - V_{DS} = 0 \Rightarrow$$

$$R_D = \frac{V_{DD} - V_{DS}}{I_{DS}} = \frac{5 - 0.5}{0.02} = \underline{\underline{225 \text{ K}}}$$



$$r_{ON} = \frac{V_O}{I_{DS}} = \frac{0.5}{0.02} = \underline{\underline{25 \text{ K}}}$$

$$R_{ON} = \frac{1}{2K(V_{GS} - V_T)}$$

$$K = \frac{1}{2R_{ON}(V_{GS} - V_T)} = \frac{1}{2 \cdot 25(5 - 1)}$$

$$= 5 \cdot 10^{-3} \text{ mA/V}^2 = \underline{\underline{5 \mu\text{A/V}^2}}$$

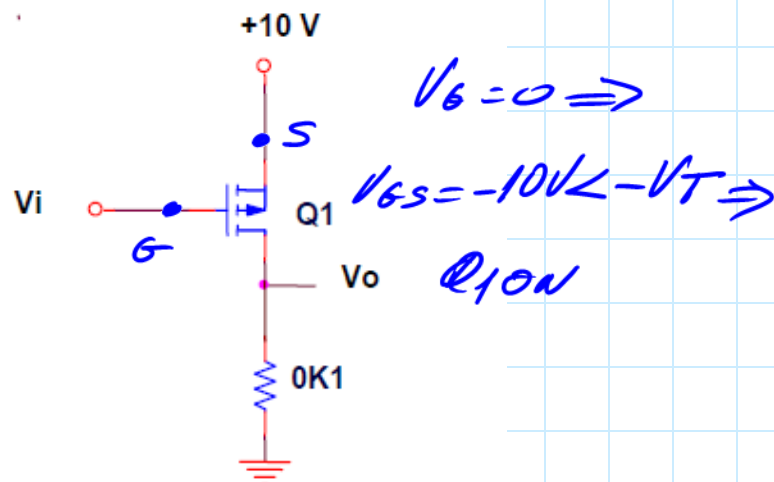
UNIT 2. MOSFETS: OPERATING POINT

(5.7) Given the switch of the figure, with a control input V_i which is a square wave varying between 0 and V_{DD} , answer the following questions:

[A] Considering the transistor as a voltage-controlled variable resistor, calculate its resistance value when closed, so that the value of the output voltage is 0.01 V.

[B] If V_T of transistor is 2 V, find the value of K of the transistor. To do so, consider that in the ohmic region (also called linear region) the current value I_{DS} can be approximated by the following expression:

$$I_{SD} \approx K [2 (V_{GS} + V_T) V_{DS}].$$



[A]



$$V_o = 10 \frac{0.1}{0.1 + R_{ON}} = 0.01$$

$$0.01(0.1 + R_{ON}) = 1 \Rightarrow \underline{R_{ON} = 99.9K}$$

[B]

$$R_{ON} = \left| \frac{1}{2K(V_{GS} + V_T)} \right|; \quad K = \left| \frac{1}{2R_{ON}(V_{GS} + V_T)} \right| = \left| \frac{1}{2 \cdot 99.9(-10 + 2)} \right|$$

$$\underline{K = 0.625 \mu A/V^2}$$