5402/DM5402/DM7402 Quad 2-Input NOR Gates

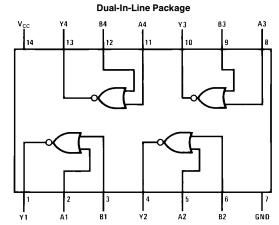
General Description

This device contains four independent gates each of which performs the logic NOR function.

Features

Alternate Military/Areospace device (5402) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.

Connection Diagram



Order Number 5402DMQB, 5402FMQB, DM5402J, DM5402W or DM7402N

See NS Package Number J14A, N14A or W14B

Function Table

$$\mathbf{Y} = \overline{\mathbf{A} + \mathbf{B}}$$

Inp	uts	Output		
Α	В	Υ		
L	L	Н		
L	Н	L		
Н	L	L		
Н	Н	L		

H = High Logic Level

L = Low Logic Level

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 5.5V

Operating Free Air Temperature Range

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM5402			DM7402			Units
	i didilictei	Min	Nom	Max	Min	Nom	Max	Omis
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V_{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
loh	High Level Output Current			-0.4			-0.4	mA
l _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

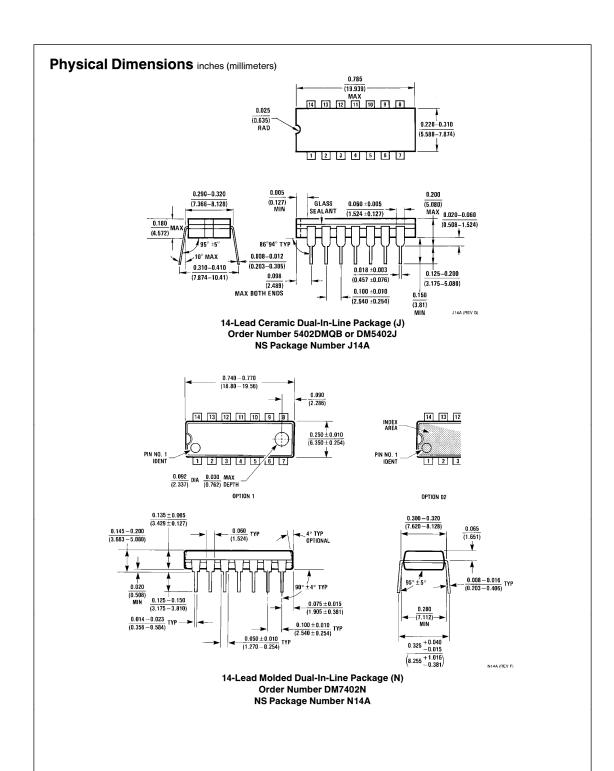
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I _I =	= -12 mA			-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH}$ $V_{IL} = Max$	_H = Max	2.4	3.4		V
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{Ol}$ $V_{IH} = Min$	_ = Max		0.2	0.4	V
lı	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I$	= 5.5V			1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.6	mA
Ios	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM54	-20		-55	- mA
			DM74	-18		-55	
ICCH	Supply Current with Outputs High	V _{CC} = Max			8	16	mA
Iccl	Supply Current with Outputs Low	V _{CC} = Max			14	27	mA

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

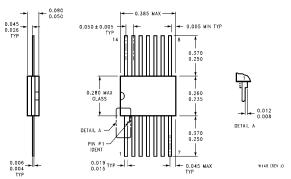
Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time Low to High Level Output	$C_L = 15 pF$ $R_L = 400 \Omega$		22	ns
t _{PHL}	Propagation Delay Time High to Low Level Output			15	ns

Note 1: All typicals are at $V_{CC}=5V,\,T_A=25^{\circ}C.$

Note 2: Not more than one output should be shorted at a time.



Physical Dimensions inches (millimeters) (Continued)



14-Lead Ceramic Flat Package (W)
Order Number 5402FMQB or DM5402W
NS Package Number W14B

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