Introduction to Formal Methods Chapter 02: Modeling Transition Systems

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Outline

- Transition Systems as Kripke Models
- Languages for Transition Systems
- Properties of Transition Systems

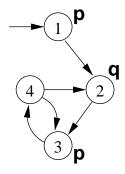
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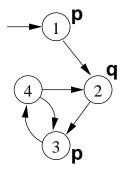
Modeling the system: Kripke models

- Kripke models are used to describe reactive systems:
 - nonterminating systems with infinite behaviors (e.g. communication protocols, hardware circuits);
 - represent the dynamic evolution of modeled systems;
 - a state includes values to state variables, program counters, content of communication channels.
 - can be animated and validated before their actual implementation

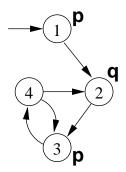
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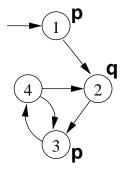
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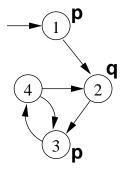
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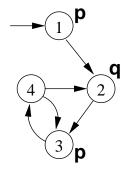
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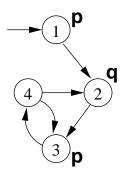
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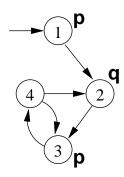
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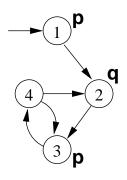
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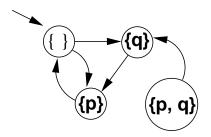


Remark

Unlike with other types of Automata (e.g., Buechi), in Kripke structures the value of every variable is always assigned in each state.

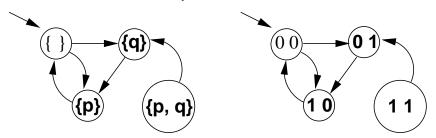
Kripke Structures: two alternative representations:

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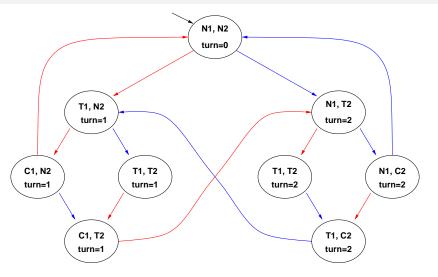
- each state identifies univocally the values of the atomic propositions which hold there
- each state is labeled by a bit vector



Other representations of finite state machines

- Moore machines
- Mealy machines
- Finite automata
- Büchi automata
- ..

Example: a Kripke model for mutual exclusion

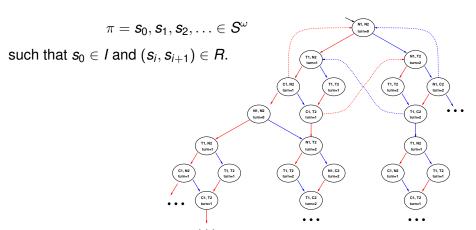


N = noncritical, T = trying, C = critical

User 1 User 2

Path in a Kripke Model

A path in a Kripke model M is an infinite sequence of states



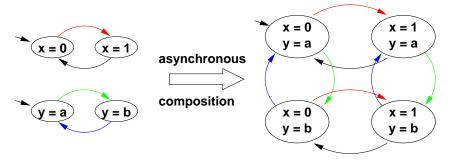
A state s is reachable in M if there is a path from the initial states to s.

Composing Kripke Models

- Complex Kripke Models are tipically obtained by composition of smaller ones
- Components can be combined via
 - asynchronous composition.
 - synchronous composition,

Asynchronous Composition

- Interleaving of evolution of components.
- At each time instant, one component is selected to perform a transition.



• Typical example: communication protocols.

Asynchronous Composition/Product: formal definition

Asynchronous product of Kripke models

Let $M_1 \stackrel{\text{def}}{=} \langle S_1, I_1, R_1, AP_1, L_1 \rangle$, $M_2 \stackrel{\text{def}}{=} \langle S_2, I_2, R_2, AP_2, L_2 \rangle$. Then the asynchronous product $M \stackrel{\text{def}}{=} M_1 || M_2$ is $M \stackrel{\text{def}}{=} \langle S, I, R, AP, L \rangle$, where

- $S \subseteq S_1 \times S_2$ s.t., $\forall \langle s_1, s_2 \rangle \in S, \ \forall I \in AP_1 \cap AP_2, I \in L_1(s_1) \ \textit{iff} \ I \in L_2(s_2)$
- $I \subseteq I_1 \times I_2$ s.t. $I \subseteq S$
- $R(\langle s_1, s_2 \rangle, \langle t_1, t_2 \rangle)$ iff $(R_1(s_1, t_1) \text{ and } s_2 = t_2)$ or $(s_1 = t_1 \text{ and } R_2(s_2, t_2))$
- $\bullet \ AP = AP_1 \cup AP_2$
- $L: S \longmapsto 2^{AP}$ s.t. $L(\langle s_1, s_2 \rangle) \stackrel{\text{def}}{=} L_1(s_1) \cup L_2(s_2)$.

Note: combined states must agree on the values of Boolean variables.

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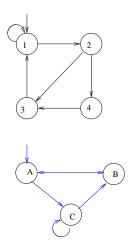
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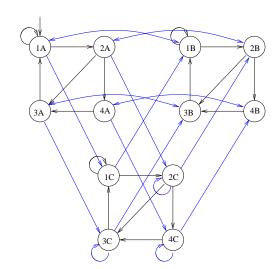
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Asynchronous composition is associative:

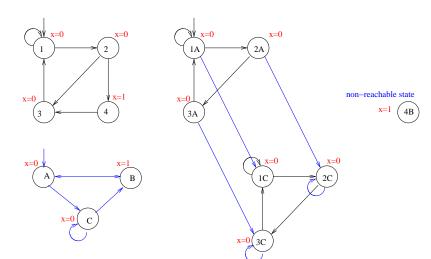
$$(...(M_1||M_2)||...)||M_n) = (M1||(M_2||(...||M_n)...) = M_1||M_2||...||M_n$$

Asynchronous Composition: Example 1

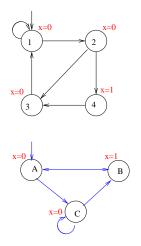


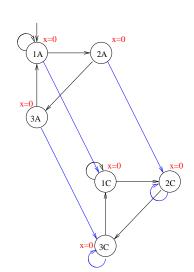


Asynchronous Composition: Example 2



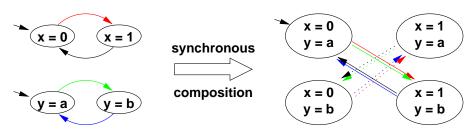
Asynchronous Composition: Example 2





Synchronous Composition

- Components evolve in parallel.
- At each time instant, every component performs a transition.



• Typical example: sequential hardware circuits.

Synchronous Composition/Product: formal definition

Synchronous product of Kripke models

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- $\begin{array}{l} \bullet \;\; \mathcal{S} \subseteq \mathcal{S}_1 \times \mathcal{S}_2 \; \text{s.t.}, \\ \forall \langle s_1, s_2 \rangle \in \mathcal{S}, \; \forall \mathit{I} \in \mathit{AP}_1 \cap \mathit{AP}_2, \mathit{I} \in \mathit{L}_1(s_1) \; \mathit{iff} \; \mathit{I} \in \mathit{L}_2(s_2) \end{array}$
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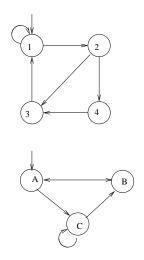
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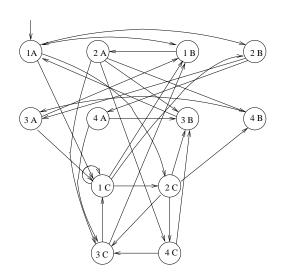
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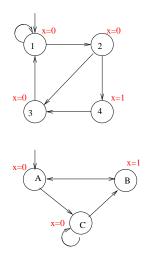
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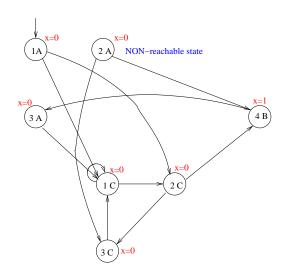
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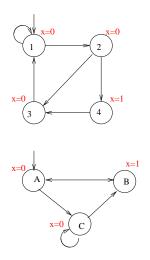


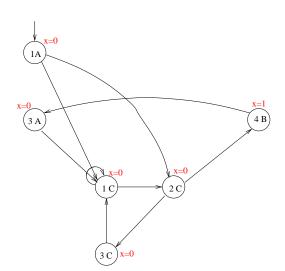
Synchronous Composition: Example 2





Synchronous Composition: Example 2 (cont.)





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- Languages for Transition Systems
- Properties of Transition Systems

Description languages for Kripke Model

 Tipically a Kripke model is not given explicitly, rather it is usually presented in a structured language (e.g., SMV, SDL, PROMELA, StateCharts, VHDL, ...)

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 - state variables: determine the set of atomic propositions *AP*, the state space *S* and the labeling *L*.
 - initial values for state variables: determine the set of initial states 1.
 - instructions: determine the transition relation R.

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Remark

tipically these description are much more compact (and intuitive) than the explicit representation of the Kripke model.

The SMV language

- The input language of the SMV M.C. (and N∪SMV)
- Booleans, enumerative and bounded integers as data types
- now enriched with other constructs, e.g. in NuXMV language
- An SMV program consists of:
 - Declarations of the state variables (e.g., b0);
 - Assignments that define the valid initial states (e.g., init (b0) := 0).
 - Assignments that define the transition relation (e.g., next (b0) := !b0).
- Allows for both synchronous and asyncronous composition of modules (though synchronous interaction more natural)

The SMV language: example

Example: The modulo 4 counter with reset

```
MODULE main
VAR
   b0 : boolean;
  b1 : boolean;
   reset : boolean;
   out : 0..3;
ASSIGN
   init(b0) := 0;
   next(b0) := case
                 reset = 1 : 0;
                 reset = 0 : !b0;
               esac:
   init(b1) := 0;
   next(b1) := case
                 reset = 1 : 0;
                 reset = 0 : (b0 xor b1);
               esac;
```

The PROMELA language

- PROMELA (Process Meta Language) is the modeling language of the M.C. SPIN
- The syntax is C-like
- A system in PROMELA consists of a set of processes that interact by means of:
 - shared variables
 - communication channels
 - rendez-vous communications
 - buffered communications
- Processes can be created dynamically
- Allows for both synchronous and asyncronous composition of processes (though asynchronous interaction more natural)

The PROMELA language: example

Example: A Mutual Exclusion Algorithm

```
bool turn;
bool flag[2];
proctype User(bool pid) {
  flag[pid] = 1;
  turn = 1-pid;
  (flag[1-pid] == 0 \mid \mid turn == pid);
  /* Begin of critical section */
  /* End of critical section
  flaq[pid] = 0;
init { run User(0); run User(1) }
```

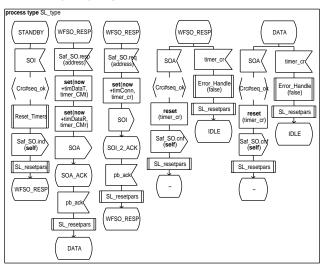
```
process 0
                   process 1
   0 0
                  0.1
    turn = 1
                    turn = 1
  1 0
                                CRITICAL
CRITICAL
                                SECTION
SECTION
```

The SDL language

- An ITU standard
- Allows for booleans, enumerative and bounded integers as data types
- Allows for representing TIME (time elapse, clocks, ...)
- represents states, message I/O, conditions, clock operations, subroutines
- Allows for both synchronous and asyncronous composition of processes (though asynchronous interaction more natural)

The SDL Language: example

Example: the Safety Layer protocol



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Safety properties

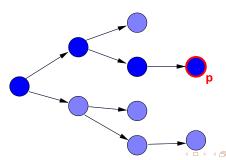
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 - deadlock: two processes waiting for input from each other, the system is unable to perform a transition.
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- Ex.: it is never the case that p.

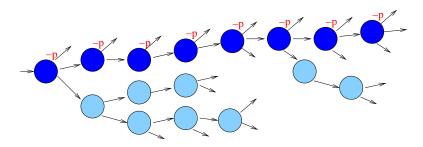


Liveness properties

- Something desirable will eventually happen
 - sooner or later this will happen

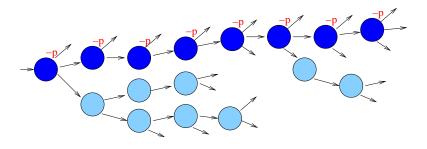
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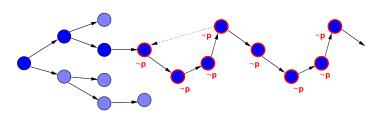
an infinite behaviour can be typically presented as a loop

Fairness properties

- Something desirable will happen infinitely often
 - important subcase of liveness
 - whenever a subroutine takes control, it will always return it (sooner or later)

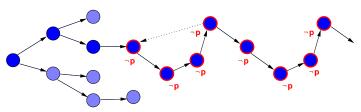
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