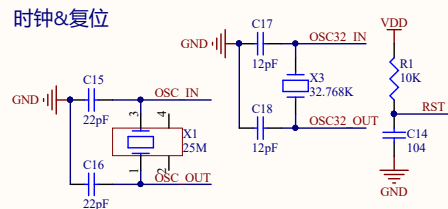
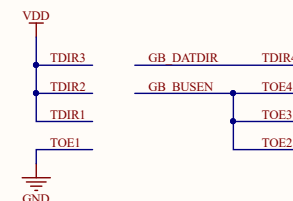
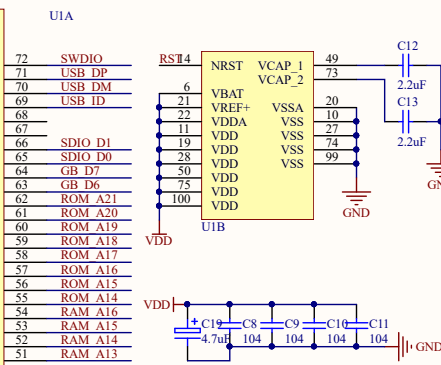
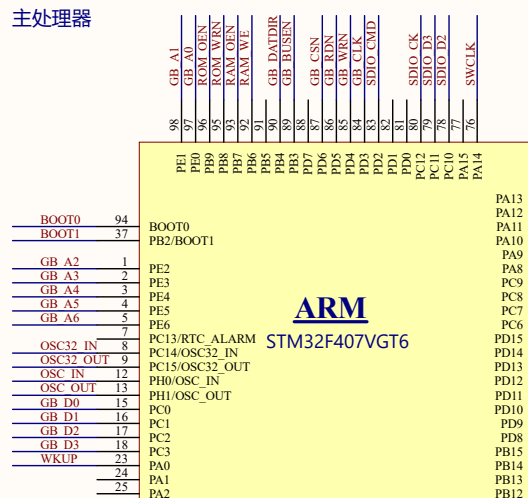


时钟&复位

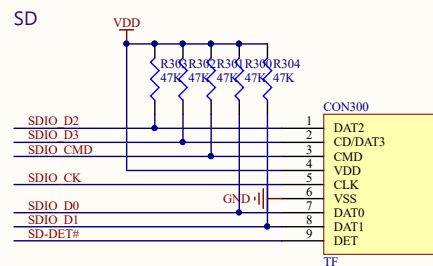


主处理器

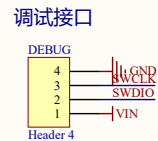


Note:
DIR = L, B to A
DIR = H, A to B
OE = H, Isolation

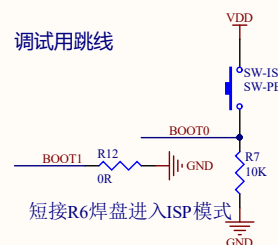
SD



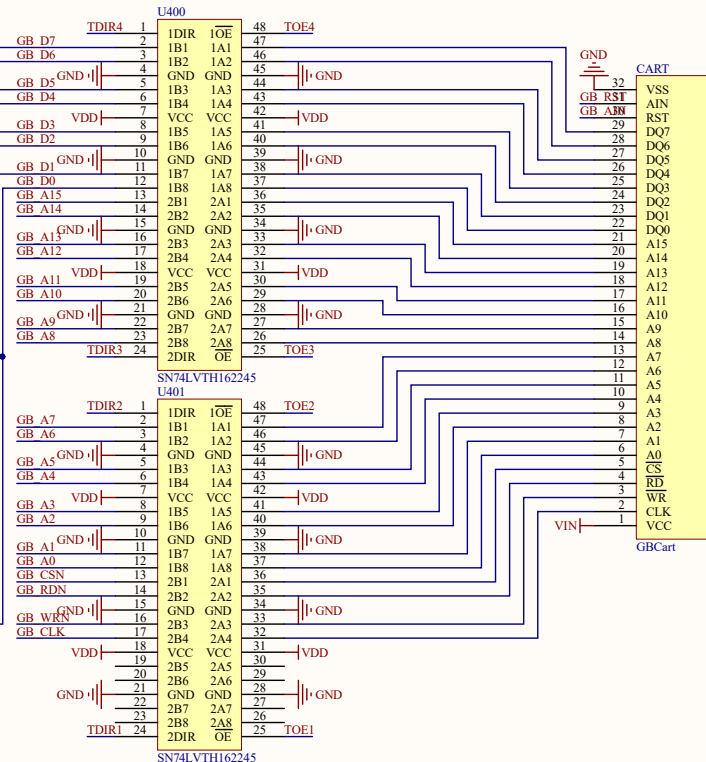
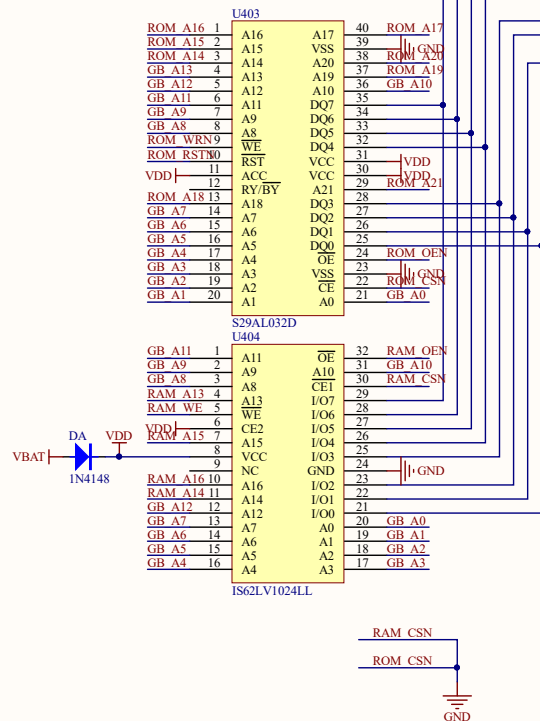
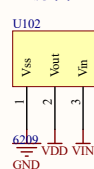
调试接口



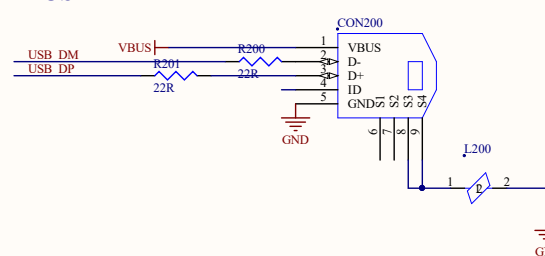
调试用跳线



电源管理



USB



Board S