

# FRANCISCO CASADO

ENGINEER, MAKER & EDUCATOR

## SUMMARY

I love putting technology at peoples service and strive for enabling meaningful experiences through it.

Learning is one of my passions. I love learning new things and I am profoundly interested in how teams learn.

I have a dream: *I want Chile to have a world-class industry of technological products and systems.*

## REACH ME AT

 franciscocasado.cl

 FranciscoCasado

 hola@franciscocasado.cl

 fcasadoc

## STUDIES

Electrical Engineer  
University of Chile, 2018

## SKILLS

Languages:  
Native Spanish, Fluent English

Electronics  
CADs: Altium, Eagle, KiCAD;  
Simulation: LTSpice, HFSS, AWR  
Embedded: Arduino, PIC, Xilinx,  
TI Launchpad.

Mechanical Design:  
Fusion 360, Blender

Programming & Software:  
Python, C, UNIX, Git, MATLAB, SQL.

## WORK EXPERIENCE

### Beauchef Proyecta - UChile

Coordinator

JUL 2019 - PRESENT

- Assesment of interdisciplinary project-based courses and student mentor in technology related topics
- Lead projects that aim to enhance learning experiences in robotics and automation.

*See below: Sh\*tty Robots and My Little Factory*

### Papinotas

Hardware Engineer

DEC 2017 - MAY 2019

- Hardware and software development for an automated and free-flow attendance platforms for public schools. Worked with UHF RFID technologies, Raspberry Pi, Flask framework and company's own web application (Ruby on Rails). We managed to deploy more than 40 units all over Santiago, with very little maintenance needed.

### Department of Electrical Engineering - UChile

Part-Time Lecturer

MAR 2020 - PRESENT

- Teacher of innovation related courses:

EL3105 - Seminar on Electrical Engineering & Technology Innovation

EL5002 - Introduction to design Workshop

EL5004 - Design Workshop

## HIGHLIGHTED PROJECTS

- **My Little Factory:** Scaled version of a factory facility, including robotic arms, conveyor belts, webcams and micro-computers for each arm. The aim of the project is to provide a space for learning practical robotics and automation in an inexpensive and friendly way at our Engineering School.  
JAN 2021 - PRESENT

- **Sh\*tty Robots:** Lectures and mechatronics kit, delivered by post mail, for enabling richer learning experiences for 2nd year student, while staying safe at home during the pandemic.  
SPRING 2020

- **B.AMBÚ:** UChile's Emergency Ventilator (COVID-19).  
WINTER 2020

- **Thesis Project:** An FPGA-based digital multiple beamformer for radio-astronomy and telecommunication applications. Proof of concept of RF frontend phased array (4x4 patch antennas @ 5.8 GHz), integrated with digital signal processing implemented on ROACH-2 (Xilinx Virtex-5), for spacial filtering.  
*Developed at Millimeter and Submillimeter-wave Lab, Santiago Chile*  
MAR 2017 - DEC 2017