sw_up_en INPUT VCC

db_down[7..0]

db_down[7..0] BIDIR VCC

TRI tri1

db_up[7..0]

db_up[7..0] BIDIR VCC

db_up[7..0]

db_down[7..6]

inst4 TRI

AND2 inst3

db_up[7..6]

db_down[5..3]

tri2 TRI

db_up[5..3]

db_down[2..0]

inst5 TRI

AND2 inst6

db_up[2..0]

sw_down_en INPUT VCC

sw_mask543_en INPUT VCC

NOT inst

PROJECT	**A-Z80**	MODULE	**data_switch_mask**	
DESIGNER	Goran Devic			
DATE	August 31, 2014		REV	1.0

data_switch_mask.bdf