ctl_alu_oe — INPUT VCC — ALU internal bus to db output enable — OUTPUT — alu_oe

ctl_alu_shift_oe — INPUT VCC — Shifter to internal bus — OUTPUT — alu_shift_oe

ctl_alu_op2_oe — INPUT VCC — OP2 to internal bus — OUTPUT — alu_op2_oe

ctl_alu_res_oe — INPUT VCC — Result to internal bus — OUTPUT — alu_res_oe

ctl_alu_op1_oe — INPUT VCC — OP1 to internal bus — OUTPUT — alu_op1_oe

ctl_alu_bs_oe — INPUT VCC — Bit selector to internal bus — OUTPUT — alu_bs_oe

OP1 latch selector mux

ctl_alu_op1_sel_bus — INPUT VCC — OUTPUT — alu_op1_sel_bus

ctl_alu_op1_sel_low — INPUT VCC — OUTPUT — alu_op1_sel_low

ctl_alu_op1_sel_zero — INPUT VCC — OUTPUT — alu_op1_sel_zero

OP2 latch selector mux

ctl_alu_op2_sel_zero — INPUT VCC — OUTPUT — alu_op2_sel_zero

ctl_alu_op2_sel_bus — INPUT VCC — OUTPUT — alu_op2_sel_bus

ctl_alu_op2_sel_lq — INPUT VCC — OUTPUT — alu_op2_sel_lq

ctl_alu_sel_op2_neg — INPUT VCC — OUTPUT — alu_sel_op2_neg

ctl_alu_sel_op2_high — INPUT VCC — OUTPUT — alu_sel_op2_high

ALU core operation

ctl_alu_core_R — INPUT VCC — OUTPUT — alu_core_R

ctl_alu_core_V — INPUT VCC — OUTPUT — alu_core_V

ctl_alu_core_S — INPUT VCC — OUTPUT — alu_core_S

| PROJECT | A-Z80 | MODULE | alu_select |
|---|---|---|---|
| DESIGNER | Goran Devic | | |
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