d1_in INPUT
VCC

dec0_in INPUT
VCC

carry_borrow_in INPUT
VCC

dec1_in INPUT
VCC

d0_in INPUT
VCC

AND2
and2

AND3
and3

XOR
xor2

XOR
xor3

OUTPUT d1_out

OUTPUT carry_borrow_out

OUTPUT d0_out

PROJECT	A-Z80	MODULE	inc_dec_2bit
DESIGNER	Goran Devic		
DATE	May 3, 2014	REV	1.0

inc_dec_2bit.bdf