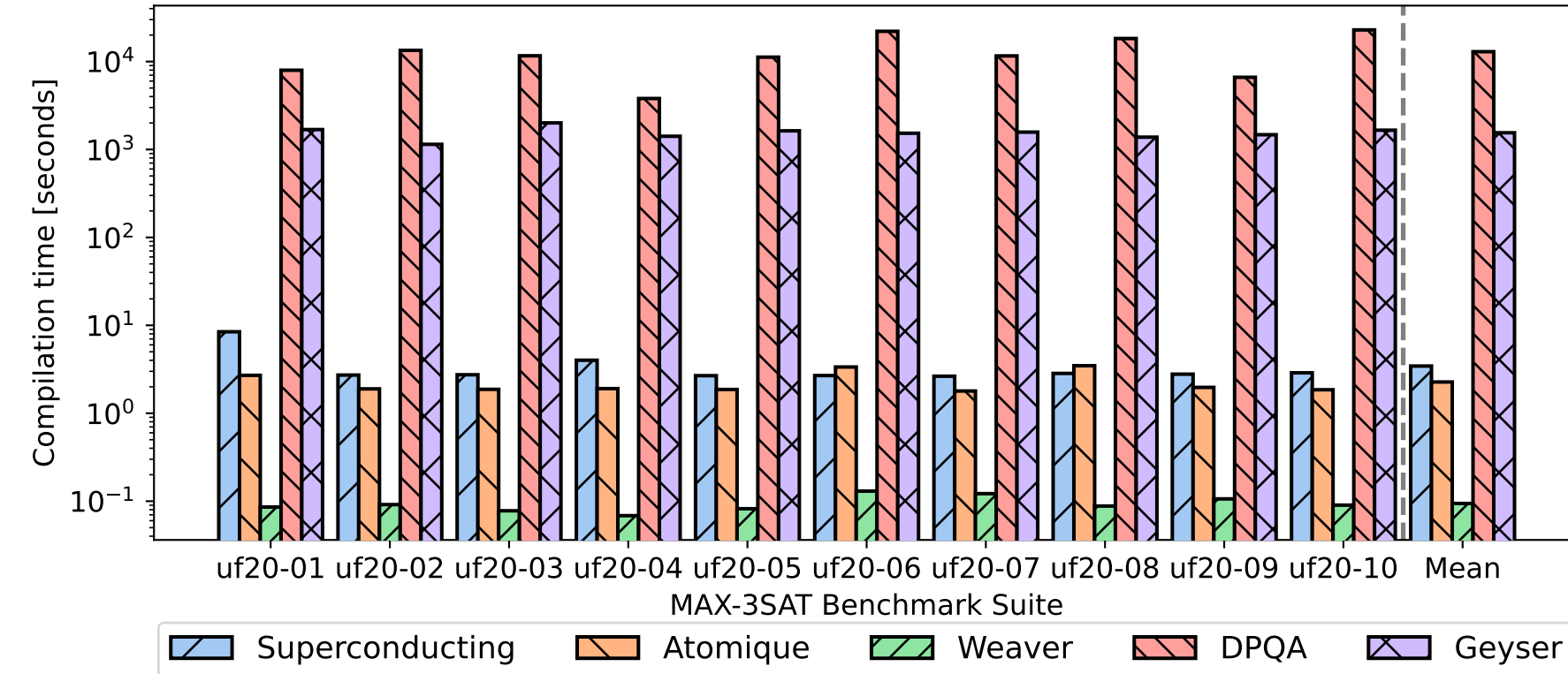


**(a) Compilation time - Fixed size circuit****Lower is better ↓****(b) Compilation time - Variable size circuit****Lower is better ↓**