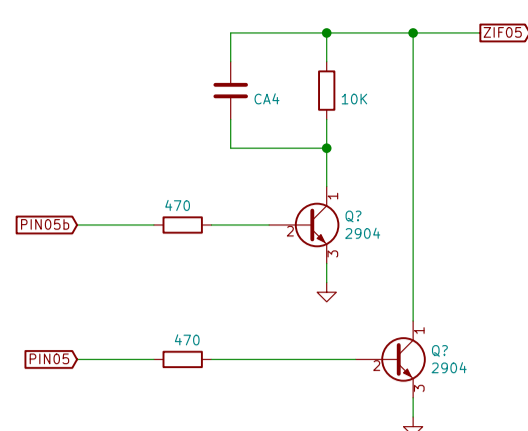
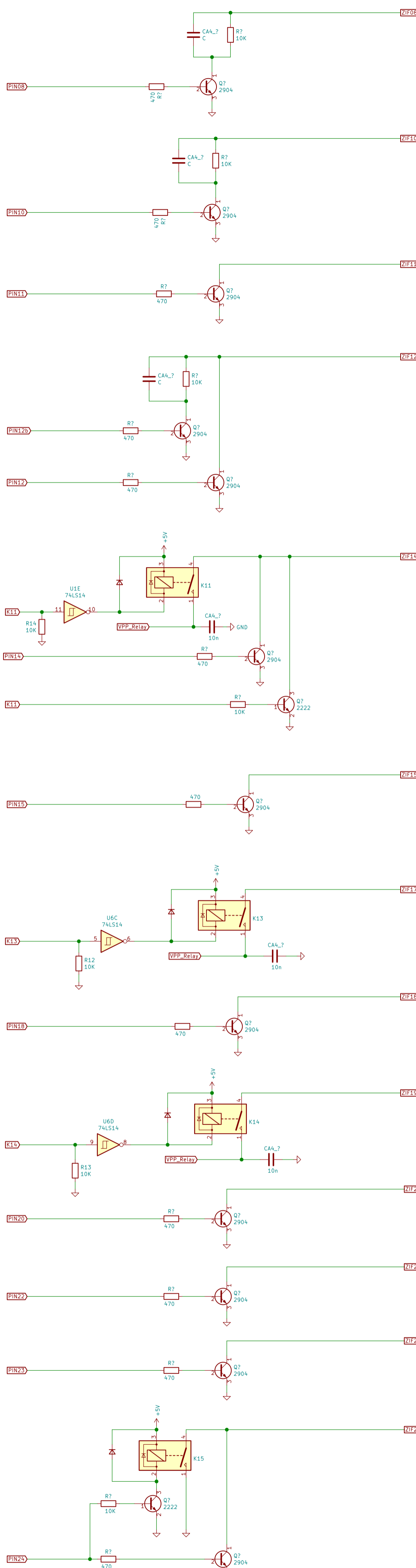
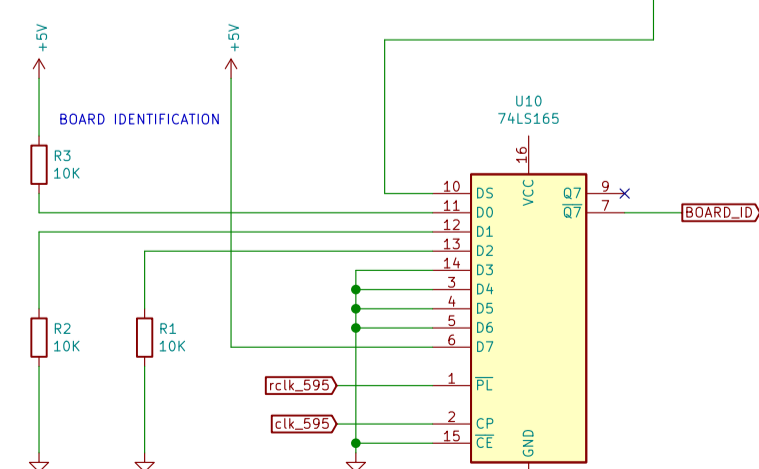
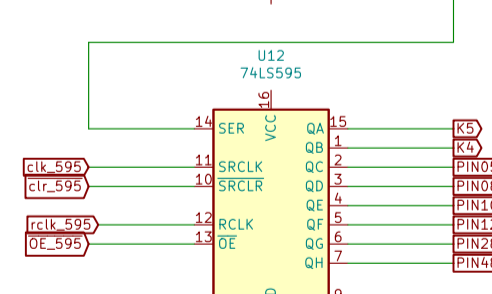
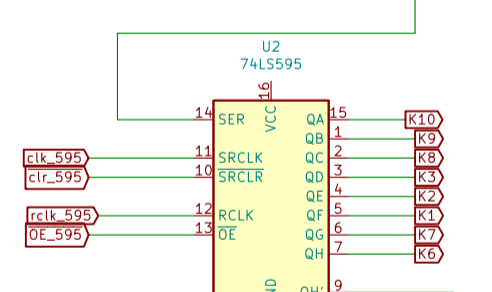
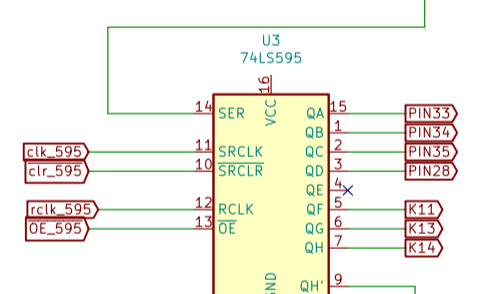
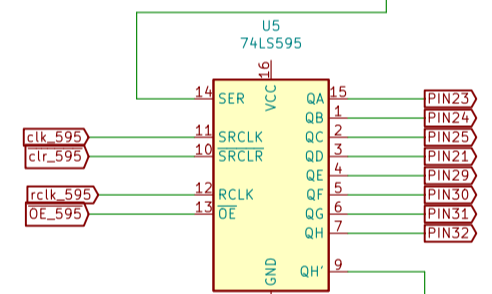
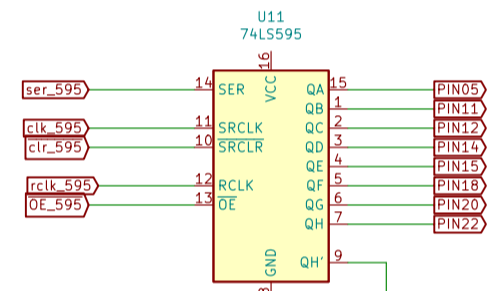
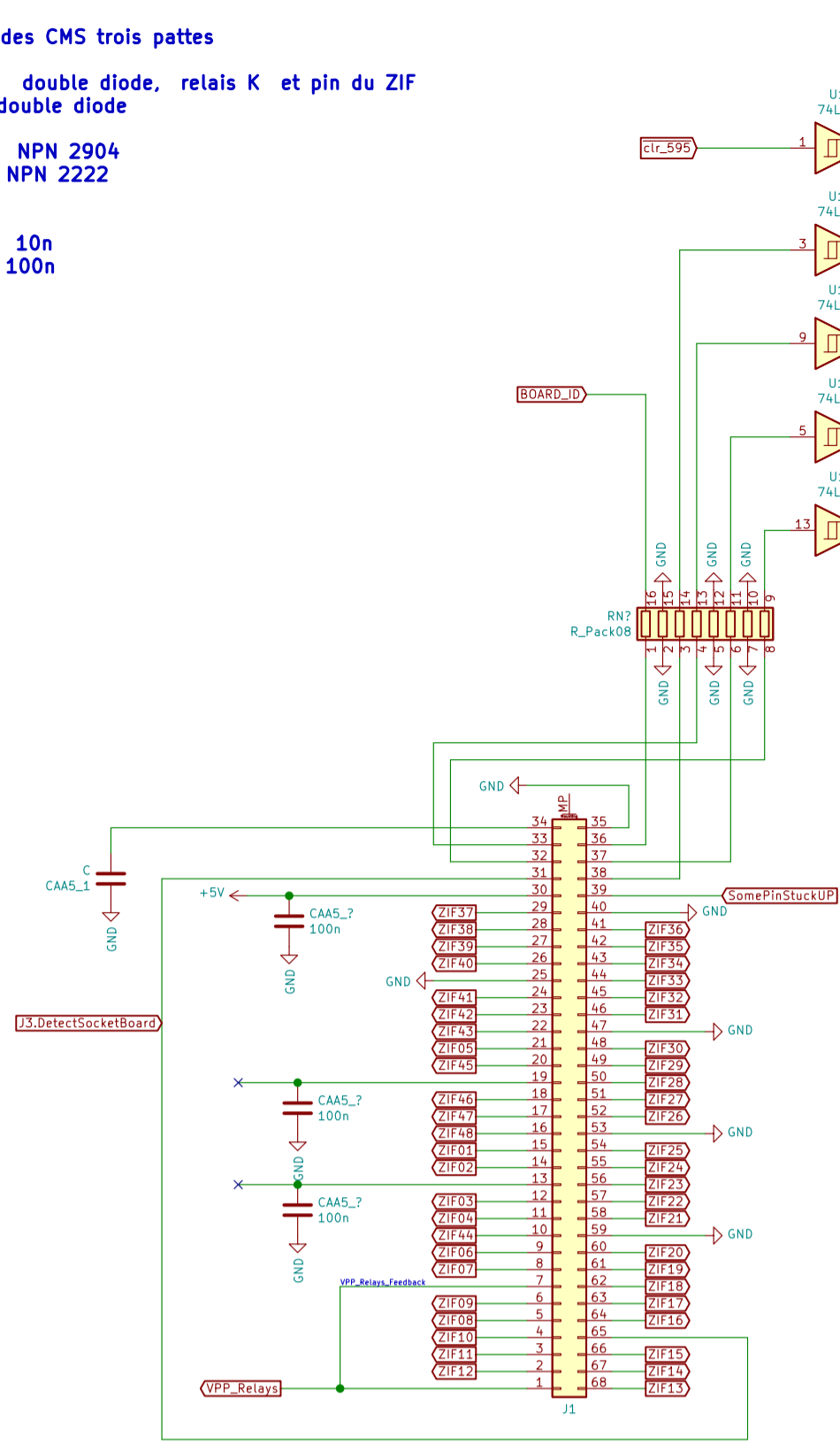


	1	2	3	4	5	6
A	<div>Sheet: socket</div> <div>File: socket.sch</div>		<div>SOCKET BOARD</div>		<div><div>This is a reverse schematic of the DATAIO ChipLabProgrammer with DIP48–1 socket</div><div>I have some transistors unconnected, checked every corner or the board !</div><div>Todo</div><div>Describe the serie of comparators on the supply board ! Q6 to confirm purpose</div><div>Identify U12, probably a μController, to put precise names on unkown nets.</div><div>Find a datasheet with pin names for U34 (TC17G032AT)</div><div>Find some info on NCR drivers U35, U38, U39</div><div>The real PCB doesn't have any silkscreen on it. Pretty annoying to find a component. I wrote numbers on the PCB for Q and U only.</div><div>What can be done is complete the KiCad annotation, fixe all errors and then use the PCB to place components and get a 3D view.</div></div>	
B	<div>Sheet: waveform</div> <div>File: waveform.sch</div>		<div>WAVEFORM BOARD</div>			
C	<div>Sheet: power</div> <div>File: power.sch</div>		<div>POWER BOARD</div>			
D	<div>Reverse engineering of a DATAIO Chiplab</div> <div><div><div><div>https://buymeacoffee.com/franck78 if usefull to you</div><div>DATAIO</div><div>Sheet: /</div><div>File: chiplab.sch</div></div><div><div>Title: Chiplab Programmer</div><div><div>Size: A4</div><div>Date: 2025–12–07</div></div><div><div>KiCad E.D.A. kicad 5.1.12</div><div>Rev: v0</div><div>Id: 1/4</div></div></div></div></div>					
	1	2	3	4	5	6

JA: double diode, relais K et pin du ZIF
A1E: double diode

41A: NPN 2904
1P: NPN 2222

CAPA
AA4 : 10m
AA5: 100m

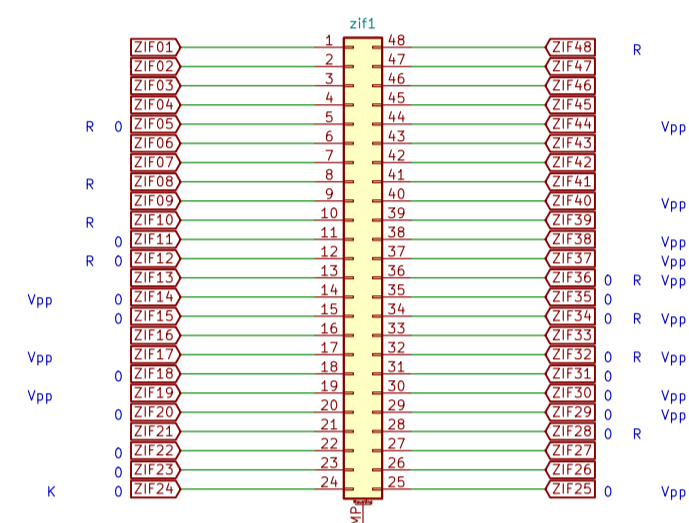


This part of the schematic is only responsible to apply '0' in various ways to the ZIF socket pins.

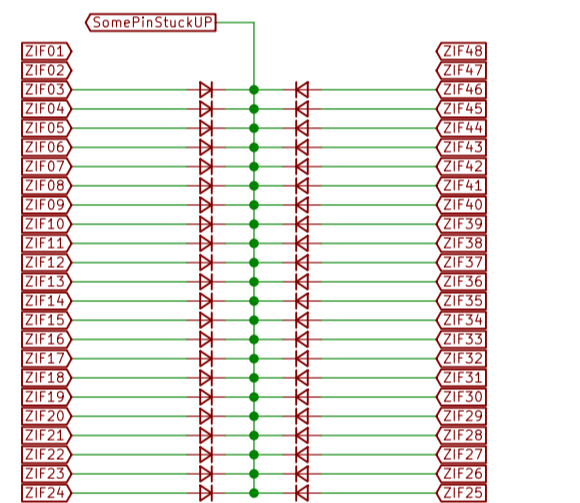
- simple transistor
- transistor + RC

Others pins receive the Vpp via a relay

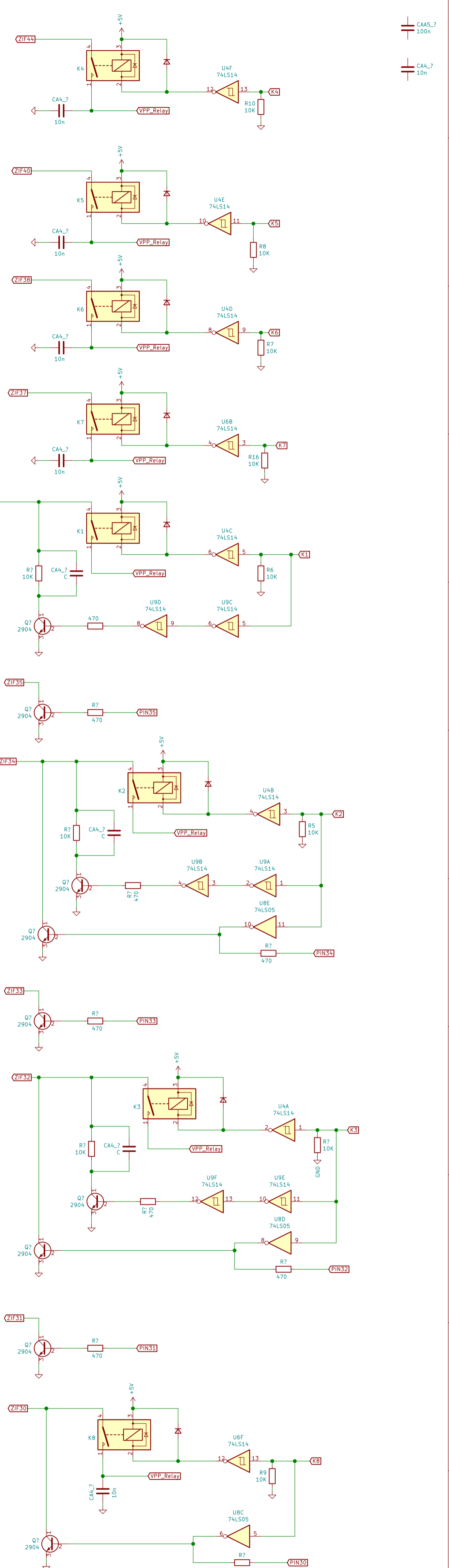
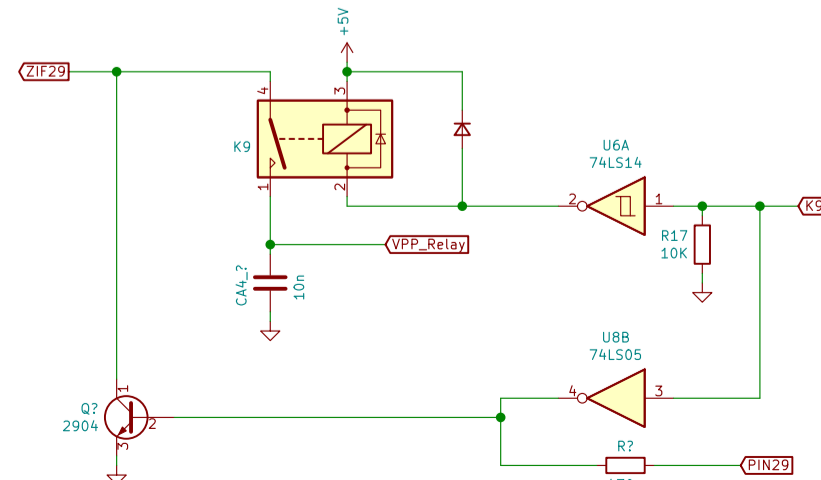
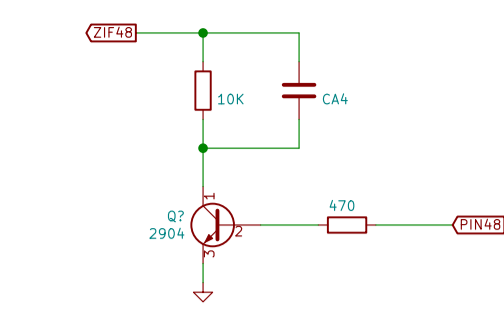
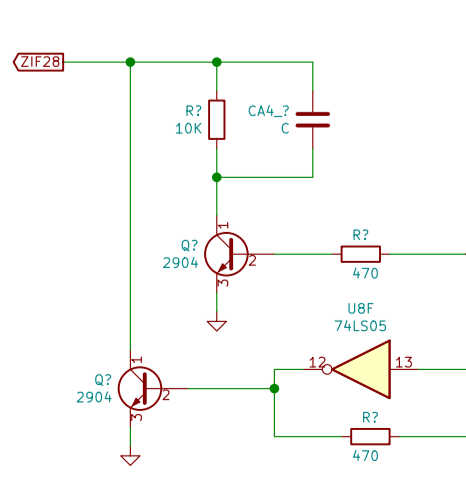
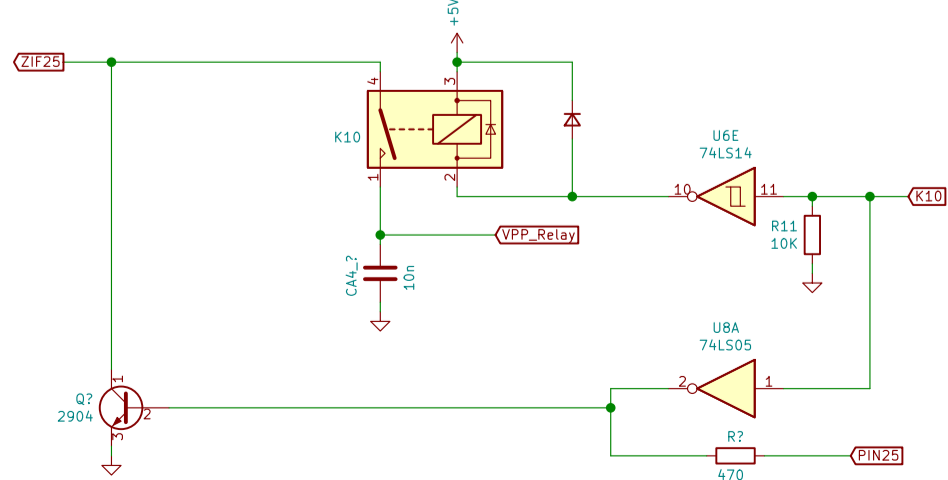
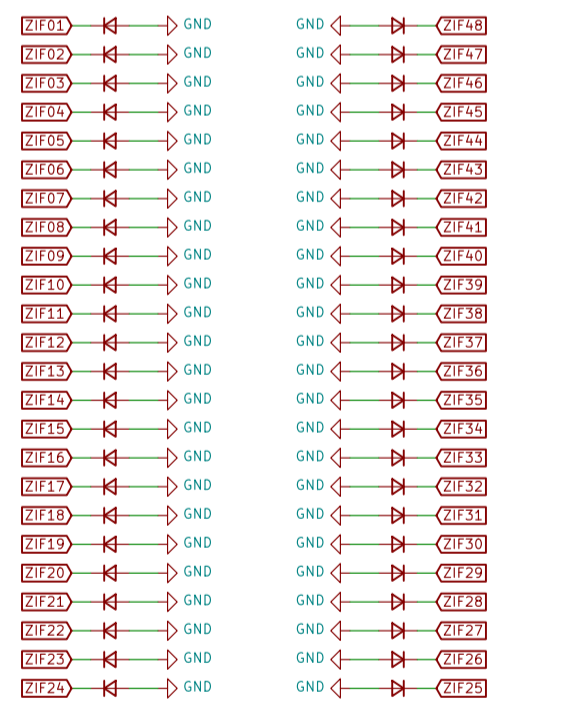
ZIF24 usually GND have a relay to 0v



Error detection
(not clamping)



Each pin of the socket have a PCB trace near the GND to simulate a surge protector. Very easy to short circuit.



Reverse engineering of a DATAIO Chiplab

<https://buymeacoffee.com/franck78> if usefull to you

Sheet: /socket/

File: socket.sch
Title: Chirolab - Programmer

Size: A1	Date: 2025-12-0
----------	-----------------

KiCad E.D.A. kicad 5.1.12	
15	1

Rev: draft

Id: 2/4

