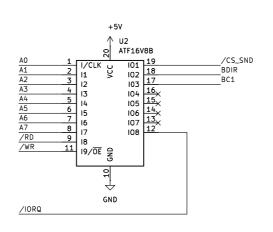
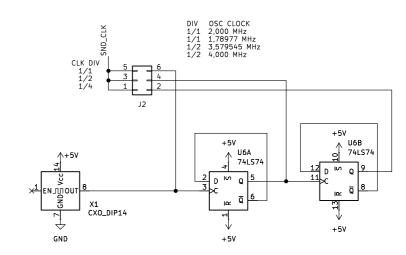


Function	BDIR	BC1	REG	/WR	/RD	/cs	ADR
Inactive	0	0	x	x	x	1	-
Read Data	0	1	1	1	0	0	D8h
Write Data	1	0	0	0	1	0	D0h
Select Register	1	1	1	0	1	0	D8h

Audio Out 22k R10 c12 + 1k R2 R3 R3 C14 100μ R4 10k U5 YM2149 R5 10k R6 10k C15 100μ 1 OUT2 2 J6 R7 10k IOAU IOA1 IOA2 IOA3 IOA4 R11 22k 10A6 10A7 29 BC1 28 BC2 GND 10B0 10B1 10B2 10B3 +5V ← SND_CLK /RESET 10B4 10B5 10B6 10B7 ×39 TEST1 YM2149F Clock OPEN <= 2 MHz CLOSE > 2 MHz P. SE (GND DIG 10





Recherche: ay38910.asm

AY_RCSND .EQU 0 ; 0 = EB MODULE, 1=MF MODULE

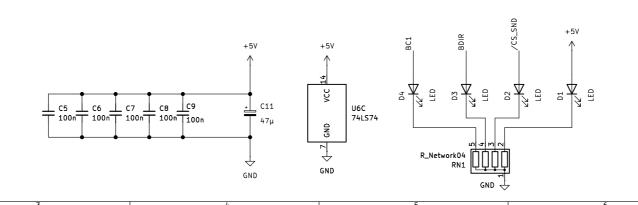
#IF (AYMODE == AYMODE RCZ80)

AY_RSEL .EQU \$D8 AY RDAT .EQU \$D0

AY_RIN .EQU AY_RSEL+AY_RCSND

SND Port address

register_port: 0xD8h 1101 1000 data_port : 0xD0h 1101 0000



History: VA: first Version VB: PLD Full address logic

Project MRC-ECB

F.Kapps

Sheet: /
File: MC-RC-ECB-SND-AY3-VB.kicad_sch

Title: sound modul for MRC-ECB Version B

 Size: A3
 Date: 2024–10–05
 Rev: 1

 KiCad E.D.A. 8.0.5
 Id: 1/1