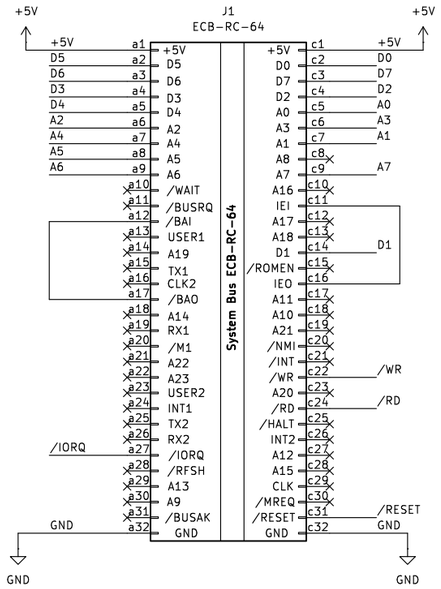
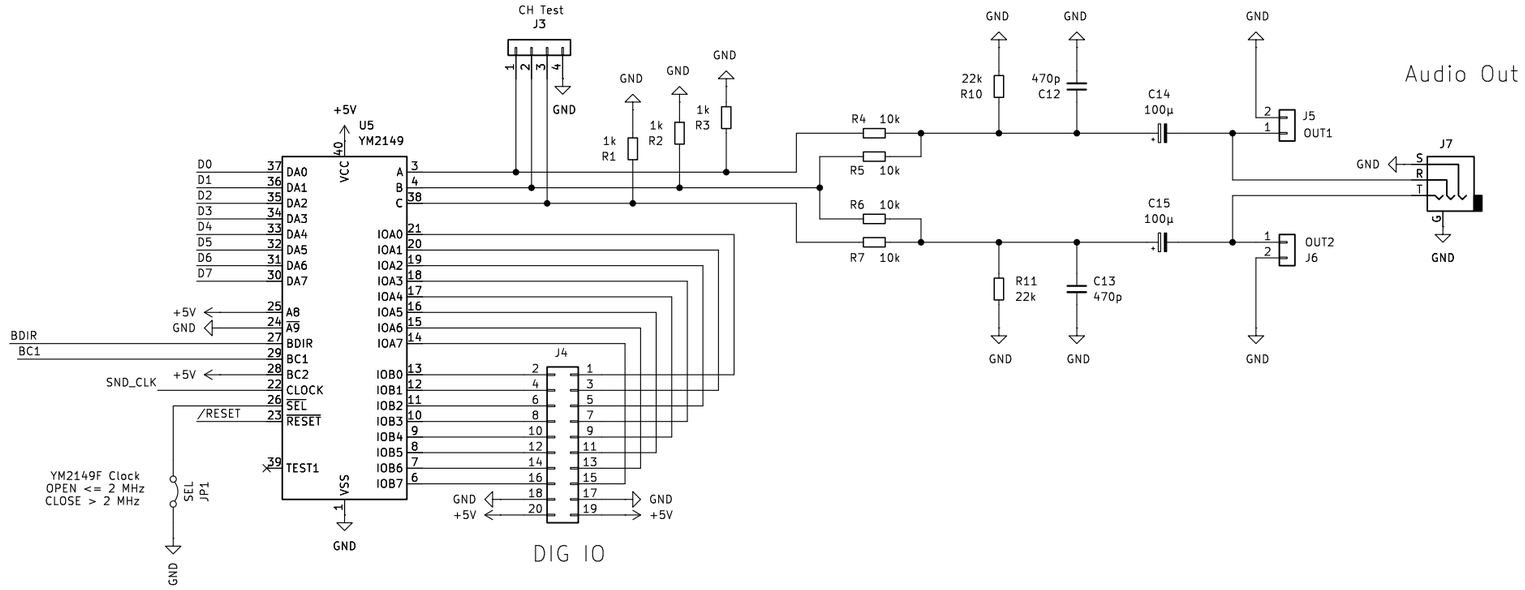
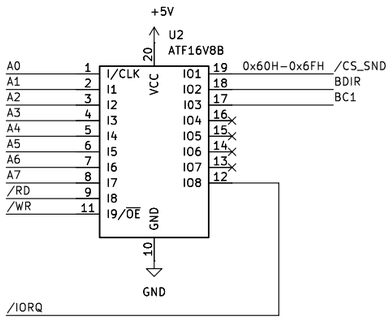
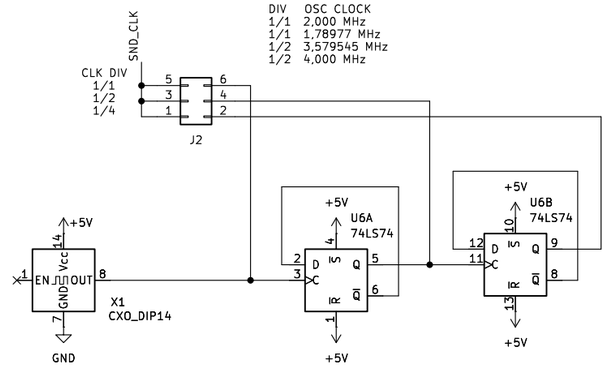




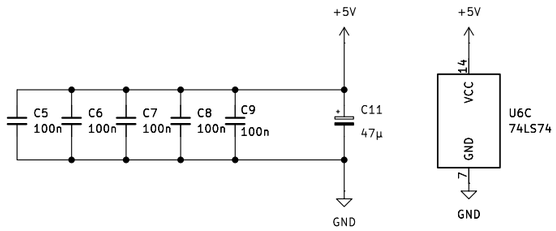




Function	BDIR	BC1	REG	/WR	/RD	/CS	ADR
Inactive	0	0	x	x	x	1	-
Read Data	0	1	1	1	0	0	D8h
Write Data	1	0	0	0	1	0	D0h
Select Register	1	1	1	0	1	0	D8h

```
Recherche: ay38910.asm
...
AY_RCSND .EQU  0     ; 0 = EB MODULE, 1=MF MODULE
...
#IF (AYMODE == AYMODE_RCZ80)
AY_RSEL     .EQU  $D8
AY_RDAT     .EQU  $D0
AY_RIN      .EQU  AY_RSEL+AY_RCSND
```

```
SND Port address
register_port: 0xD8h  1101 1000
data_port    : 0xD0h  1101 0000
```



History:
VA: first Version
VB: PLD Full address logic

Project RC-ECB-64

F.Kapps

Sheet: /
File: MC-RC-ECB-SND-AY3-VB.kicad_sch

Title: sound modul for RC-ECB-64 Version B

Size: A3	Date: 2024-10-04	**Rev: 1**
KiCad E.D.A. 8.0.5		Id: 1/1