
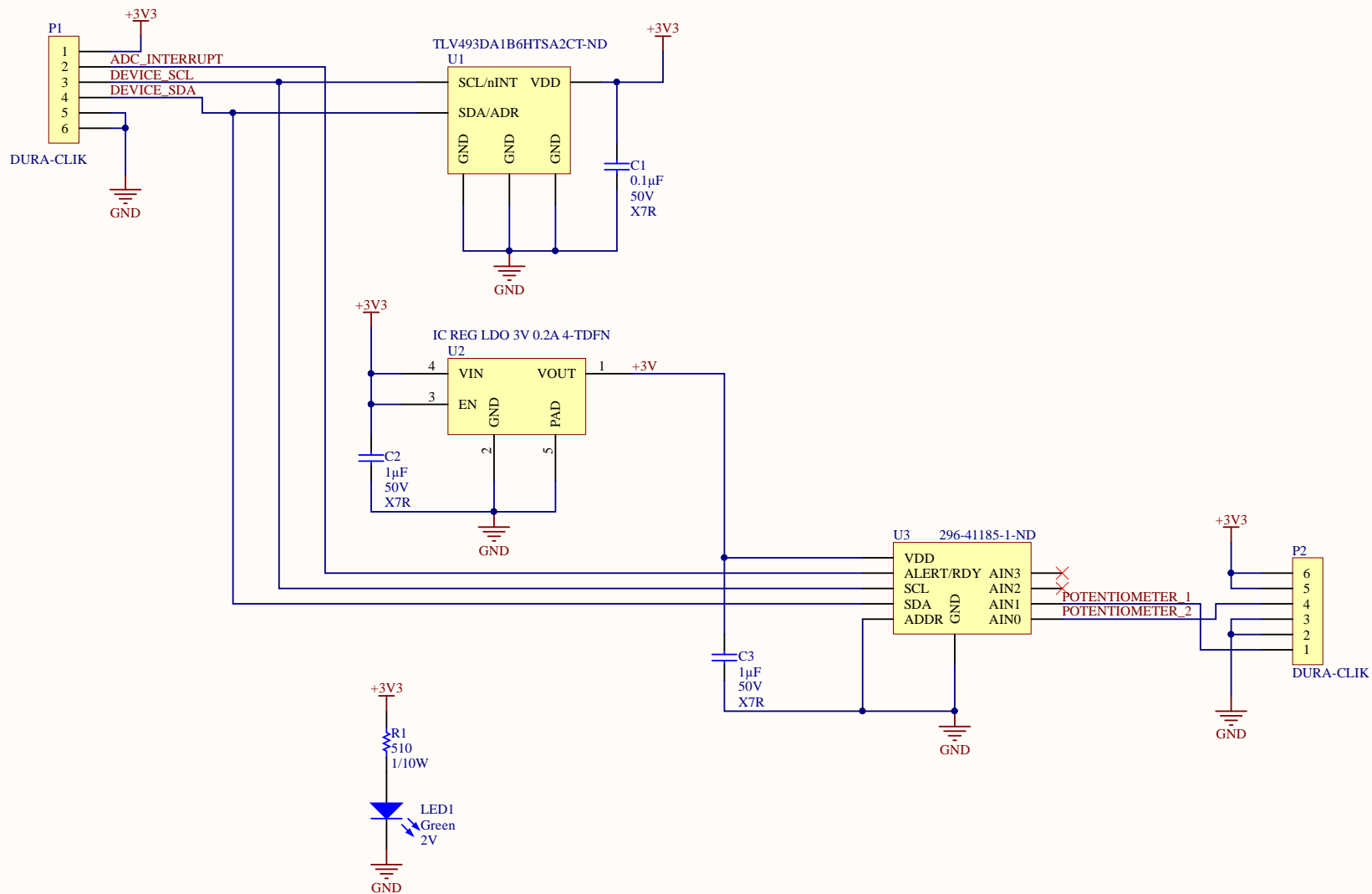



Project: <i>MSXII_DriverControls_Peripheral.PrjPcb</i>		
Title: <b>Block Diagram</b>		
Project Lead: *		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 1.23	
Date: 2017-07-19	Sheet1 of 2	Website: Website



Project: <i>MSXII_DriverControls_Peripheral.PrjPcb</i>		
Title: <b>Peripheral Board</b>		
Project Lead: *		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: <b>Letter</b>	Revision: <b>1.23</b>	
Date: <b>2017-07-19</b>	Sheet2 of 2	Website: <b>Website</b>

