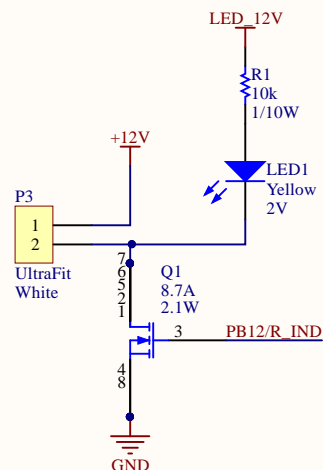
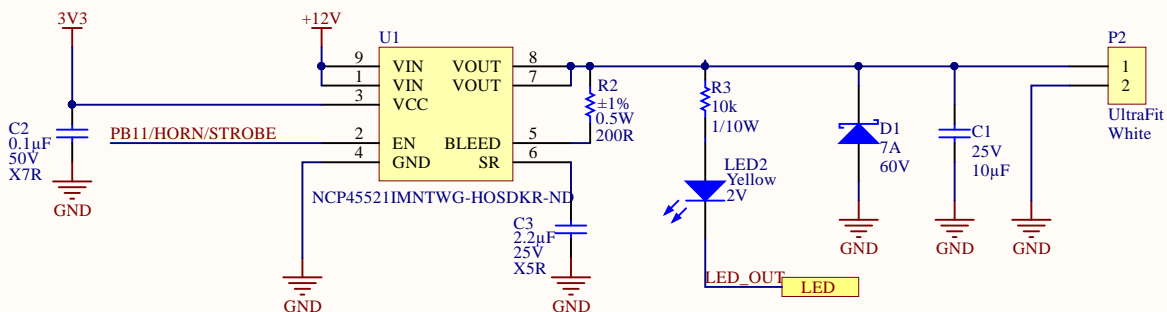


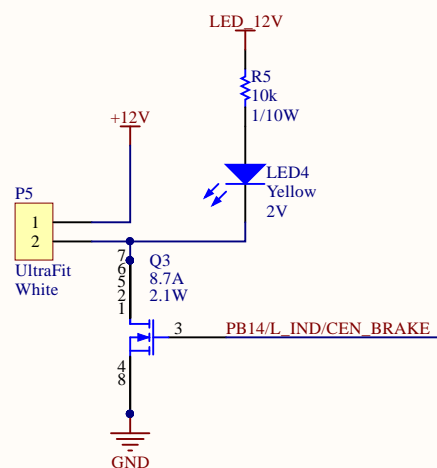
Side Right Indicator/NC



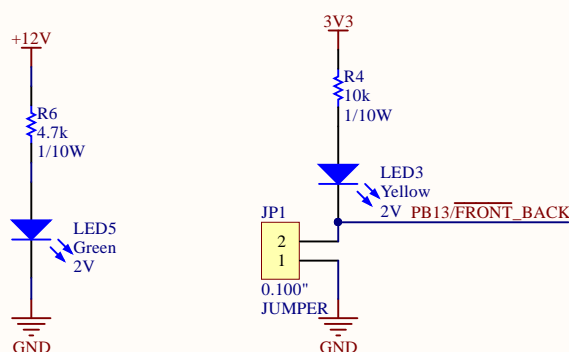
Horn/Strobe Light



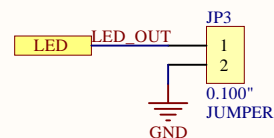
Side Left Indicator/Centre Brake Light



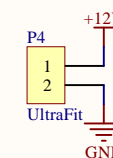
Distinguish Front vs Back



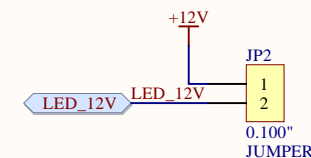
LED enable for HSD




Power Supply



LED enable for mosfets



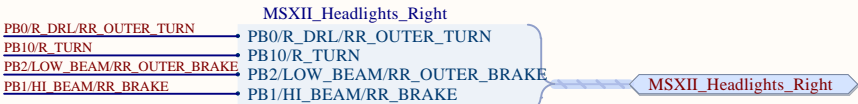
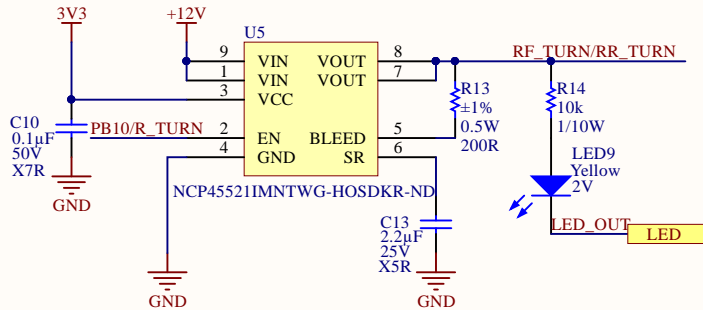
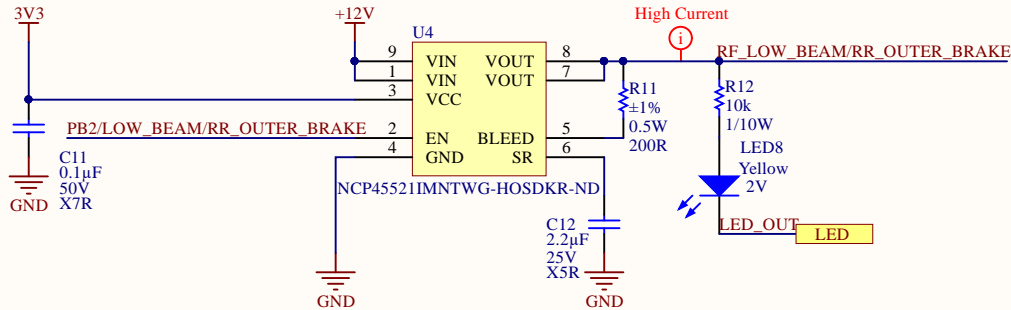
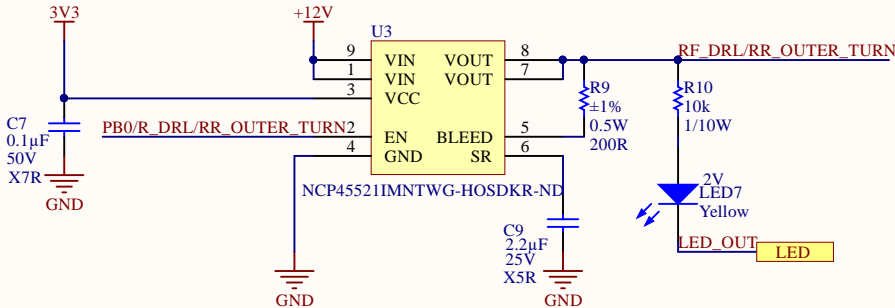
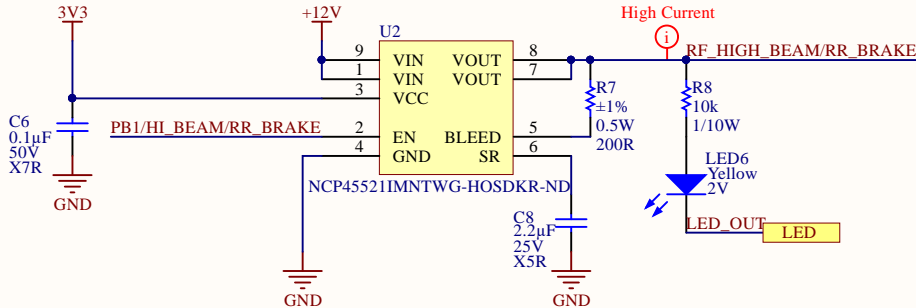
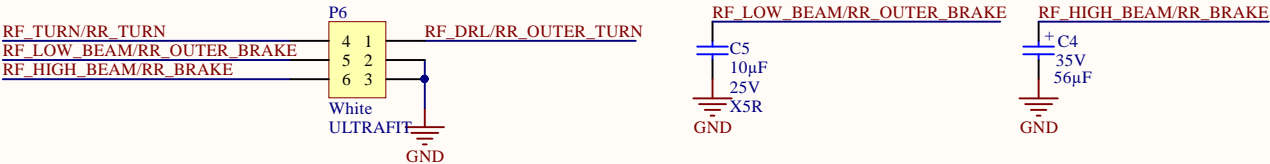
Note: LED ON when front and OFF when back


Project: <i>Lights_Board.PrjPcb</i>		
Title: All Other Lights		
Project Lead: Veronica Lee		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 1.18	
Date: 2018-02-27	Sheet 1 of 3	
		Website: www.uwmidsun.com

Right Headlight Assem/Rear Right Brake & Turn

Do not populate capacitor on SR

Note: High Beam will use Low Beam
Low Beam will use DRL
DRL will use parking light

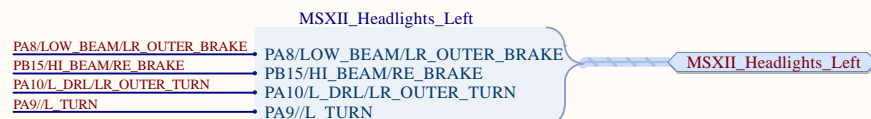
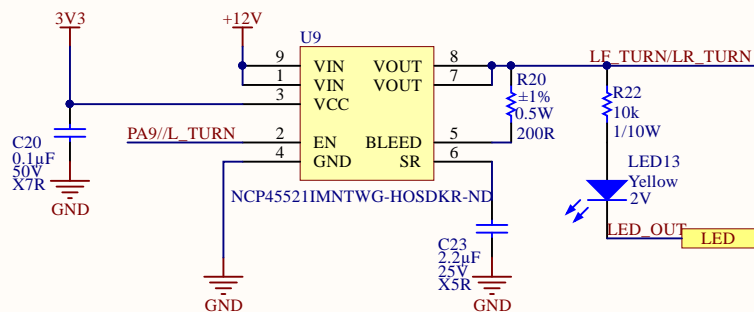
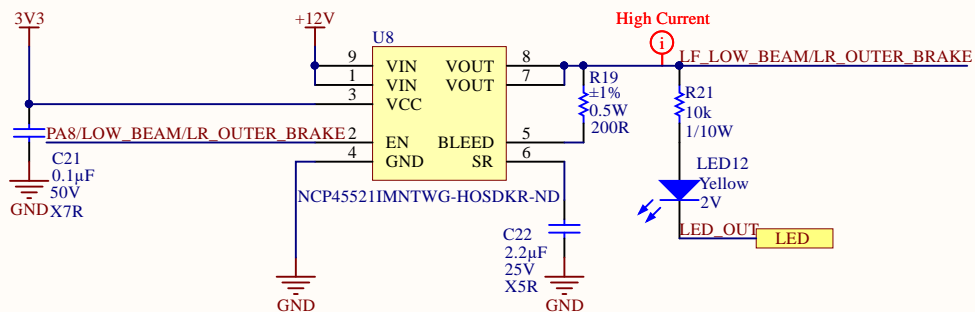
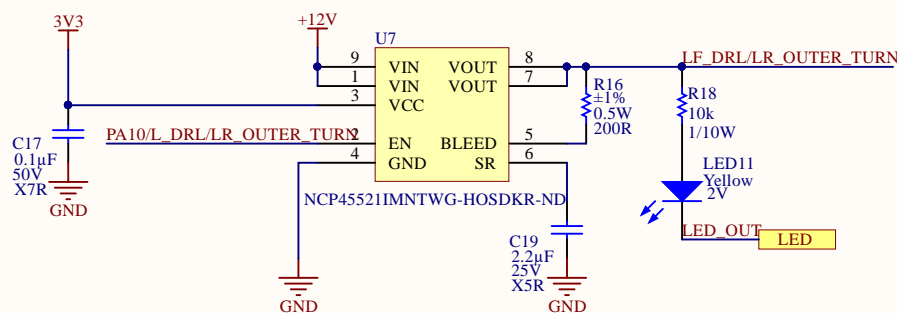
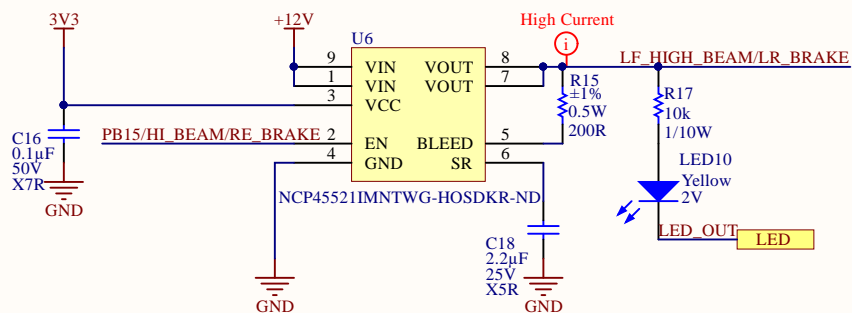
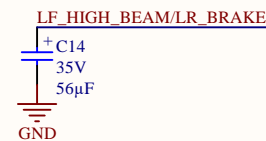
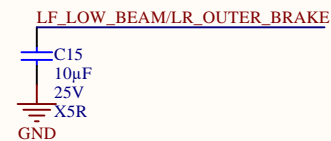
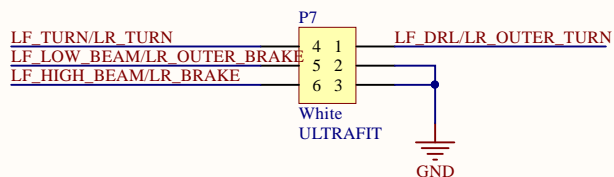


Project: <i>Lights_Board.PrjPcb</i>		
Title: Right Headlights		
Project Lead: Veronica Lee		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 1.18	
Date: 2018-02-27	Sheet 2 of 3	
		Website: www.uwmidsun.com

Left Headlight Assem/Rear left Brake & Turn

Do not populate capacitor on SR

Note: High Beam will use Low Beam
Low Beam will use DRL
DRL will use parking light



Project: **Lights_Board.PrjPcb**

Title: **Left Headlights**

Project Lead: Veronica Lee

Size: Letter

Date: 2018-02-27

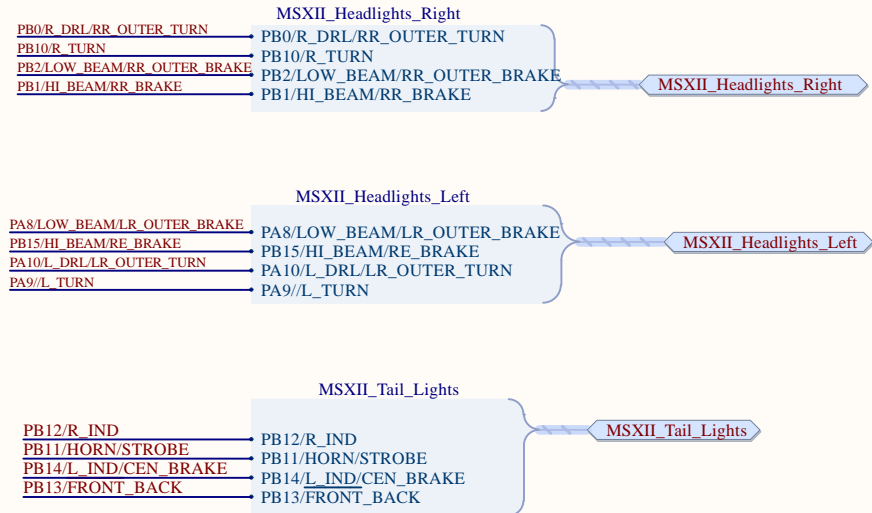
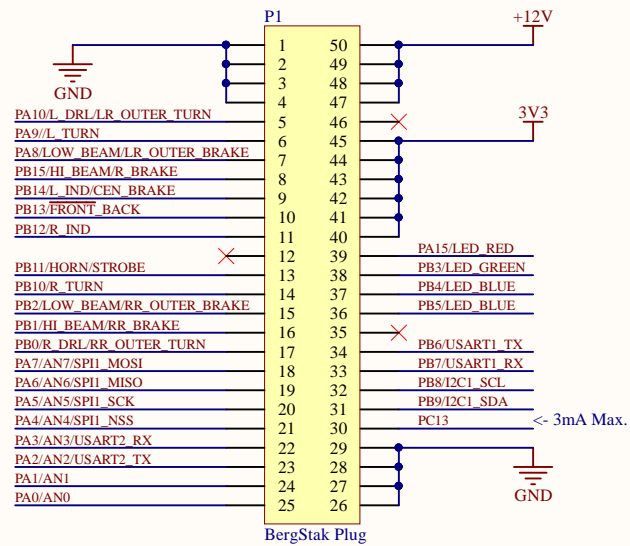
Revision: 1.18


Sheet3 of 3

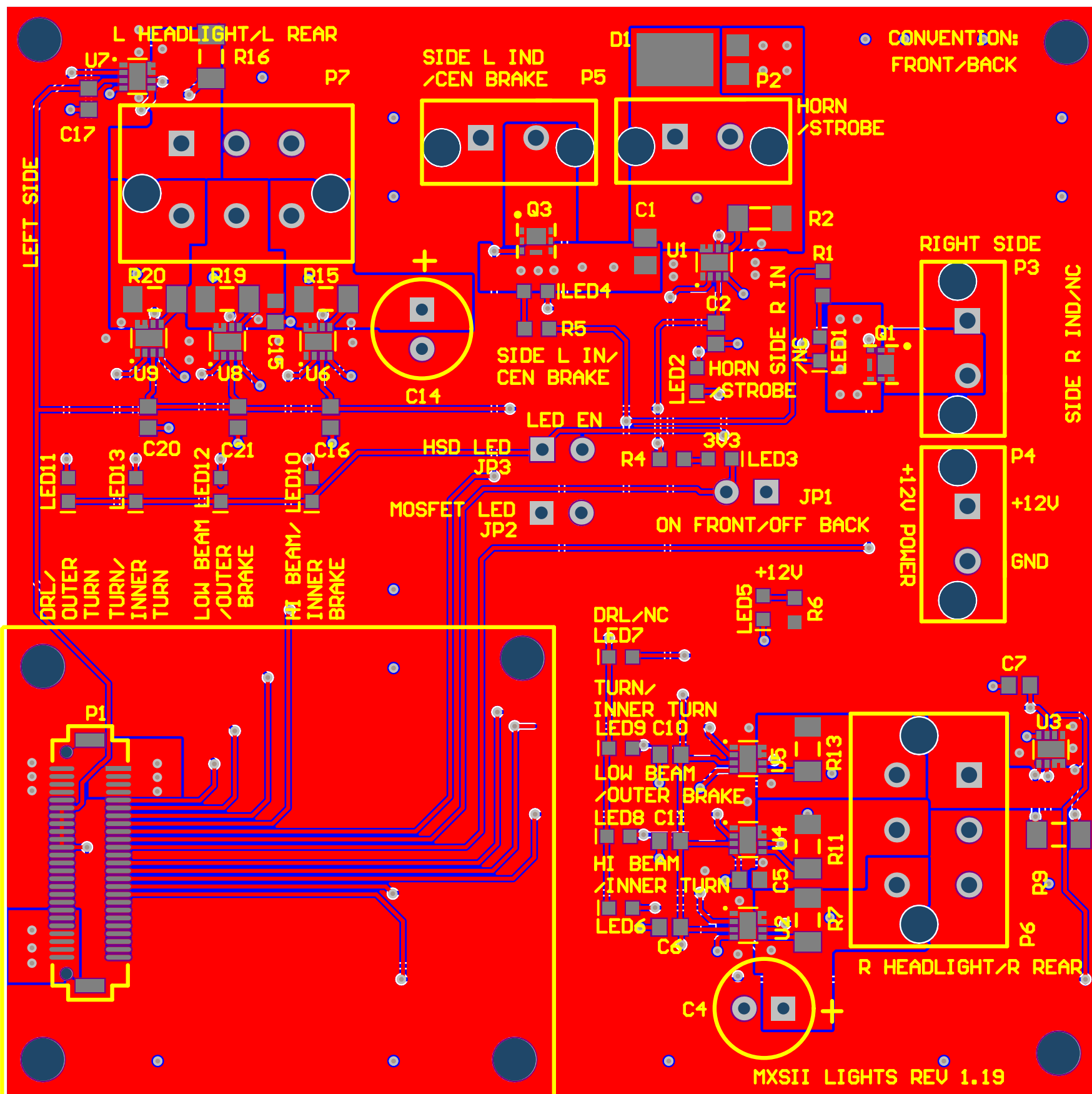
MIDNIGHT SUN

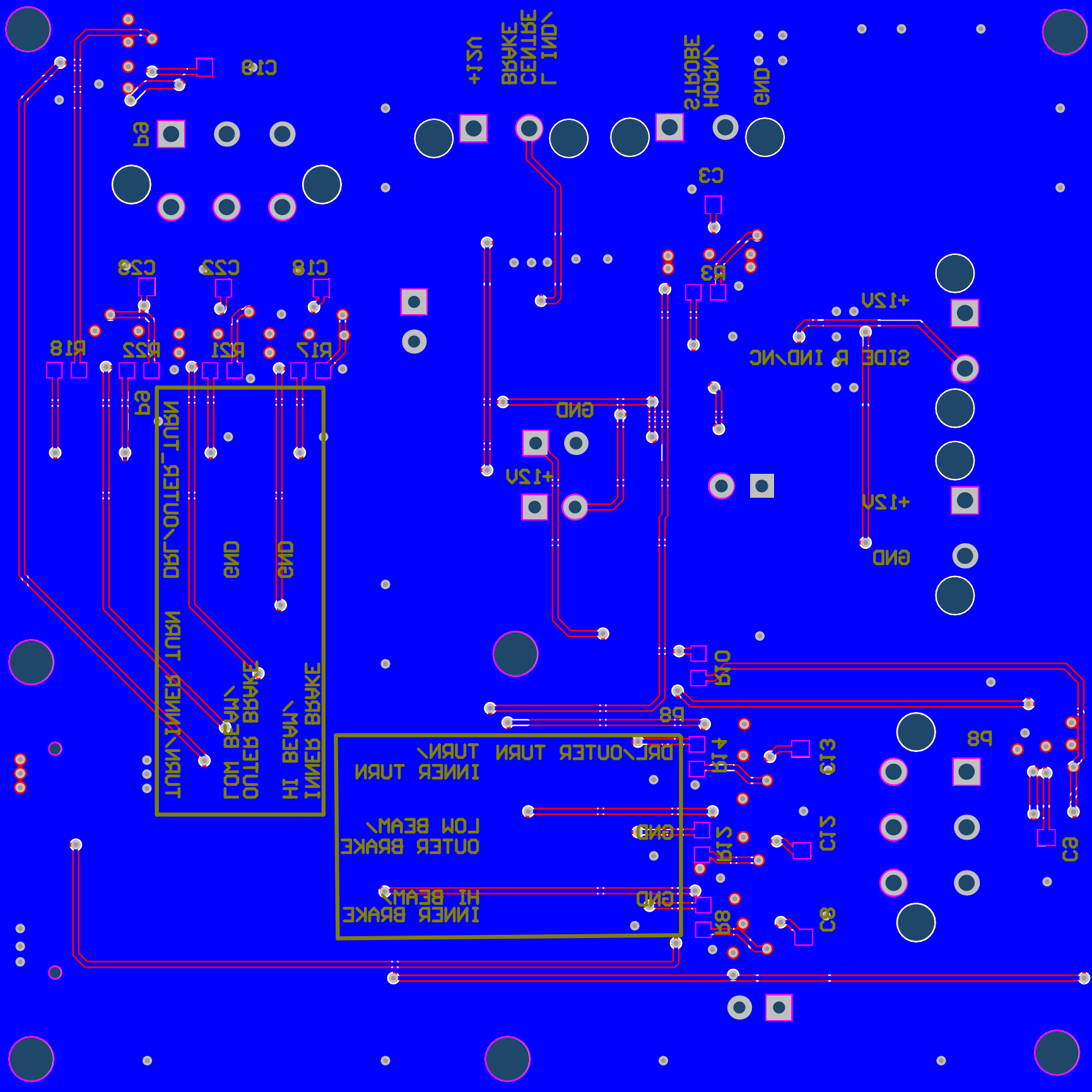
University of Waterloo
200 University Ave W
Waterloo, ON, Canada
N2L 3E9

Website: www.uwmidsun.com



Project: <i>Lights_Board.PrjPcb</i>		<div><div>MIDNIGHT</div><div></div><div>SUN</div></div>
Title: *		
Project Lead: Veronica Lee		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 1.18	
Date: 2018-02-27	Sheet* of *	
		Website: www.uwmidsun.com





Electrical Rules Check Report

Class	Document	Message
Error	Controller_Board_Interface.SchDoc	Net PA0/AN0 has only one pin (Pin P1-25)
Error	Controller_Board_Interface.SchDoc	Net PA1/AN1 has only one pin (Pin P1-24)
Error	Controller_Board_Interface.SchDoc	Net PA2/AN2/USART2_TX has only one pin (Pin P1-23)
Error	Controller_Board_Interface.SchDoc	Net PA3/AN3/USART2_RX has only one pin (Pin P1-22)
Error	Controller_Board_Interface.SchDoc	Net PA4/AN4/SPI1_NSS has only one pin (Pin P1-21)
Error	Controller_Board_Interface.SchDoc	Net PA5/AN5/SPI1_SCK has only one pin (Pin P1-20)
Error	Controller_Board_Interface.SchDoc	Net PA6/AN6/SPI1_MISO has only one pin (Pin P1-19)
Error	Controller_Board_Interface.SchDoc	Net PA7/AN7/SPI1_MOSI has only one pin (Pin P1-18)
Error	Controller_Board_Interface.SchDoc	Net PA15/LED_RED has only one pin (Pin P1-39)
Error	Controller_Board_Interface.SchDoc	Net PB3/LED_GREEN has only one pin (Pin P1-38)
Error	Controller_Board_Interface.SchDoc	Net PB4/LED_BLUE has only one pin (Pin P1-37)
Error	Controller_Board_Interface.SchDoc	Net PB5/LED_BLUE has only one pin (Pin P1-36)
Error	Controller_Board_Interface.SchDoc	Net PB6/USART1_TX has only one pin (Pin P1-34)
Error	Controller_Board_Interface.SchDoc	Net PB7/USART1_RX has only one pin (Pin P1-33)
Error	Controller_Board_Interface.SchDoc	Net PB8/I2C1_SCL has only one pin (Pin P1-32)
Error	Controller_Board_Interface.SchDoc	Net PB9/I2C1_SDA has only one pin (Pin P1-31)
Error	Controller_Board_Interface.SchDoc	Net PB15/HI_BEAM/R_BRAKE has only one pin (Pin P1-8)
Error	Controller_Board_Interface.SchDoc	Net PB15/HI_BEAM/RE_BRAKE has only one pin (Pin U6-2)
Error	Controller_Board_Interface.SchDoc	Net PC13 has only one pin (Pin P1-30)

Design Rules Verification Report

Filename : C:\Users\Taiping\Documents\MidnightSun\hardware\MSXII_Lights\MSXII_Lights.Pc Warnings 0
Rule Violations 4

Warnings	
Total	0

Rule Violations	
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Board Clearance Constraint (Gap=0mm) (All)	4
Total	4

Board Clearance Constraint (Gap=0mm) (All)	
Board Outline Clearance(Outline Edge): (Collision < 0.406mm) Between Board Edge And Track (0.102mm,0.001mm)(0.102mm,30.001mm) on Top Overlay	
Board Outline Clearance(Outline Edge): (Collision < 0.406mm) Between Board Edge And Track (0.202mm,0.101mm)(35.202mm,0.101mm) on Top Overlay	
Board Outline Clearance(Outline Edge): (0.075mm < 0.406mm) Between Board Edge And Track (0.202mm,30.101mm)(35.202mm,30.101mm) on Top	
Board Outline Clearance(Outline Edge): (Collision < 0.406mm) Between Board Edge And Track (35.202mm,0.101mm)(35.202mm,30.101mm) on Top	