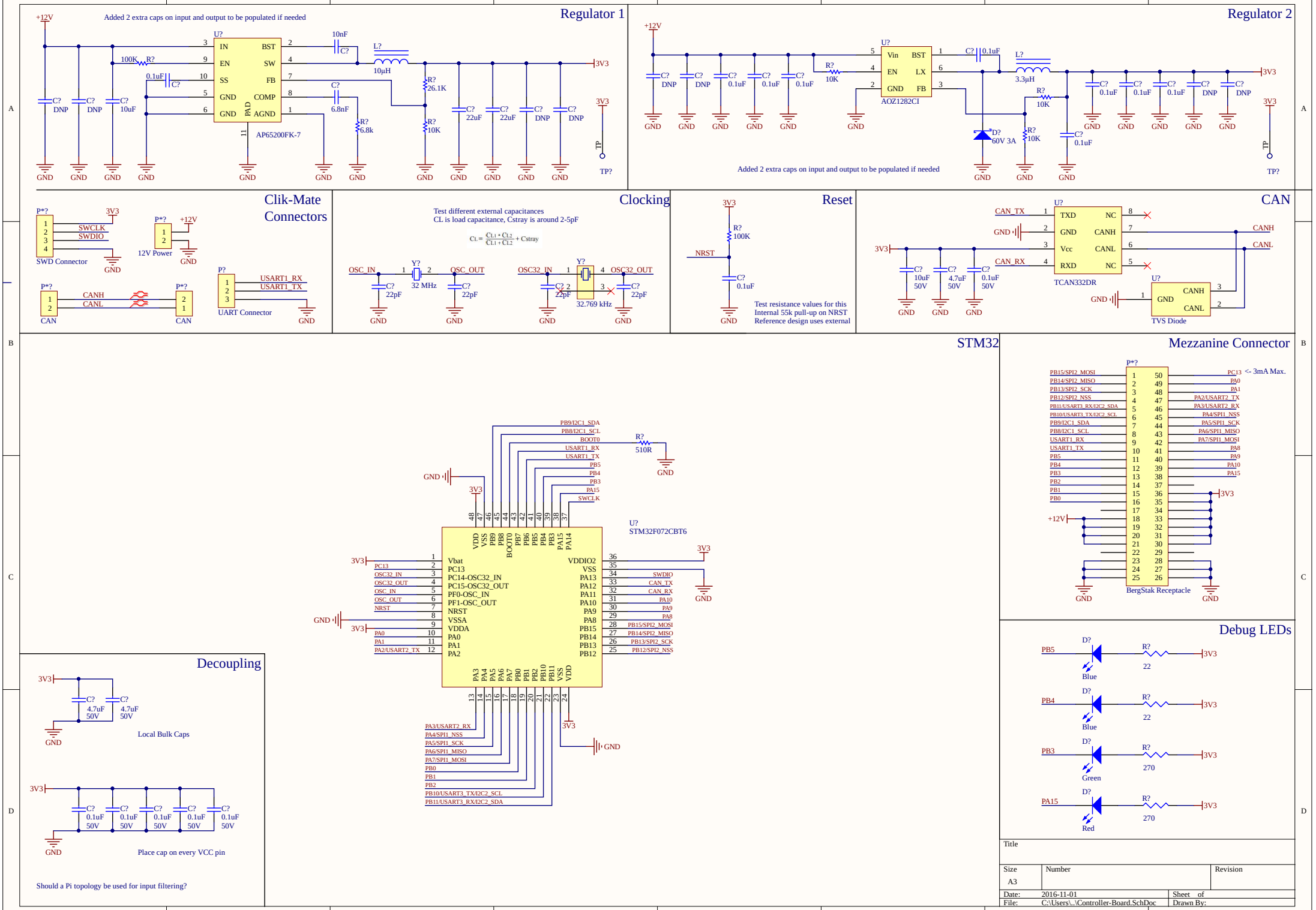
Regulator 1
Added 2 extra caps on input and output to be populated if needed
AP65200FK-7
Regulator 2
AOZ1282CI
Added 2 extra caps on input and output to be populated if needed
Clik-Mate Connectors
SWD Connector
12V Power
UART Connector
CAN
Clocking
Test different external capacitances
CL is load capacitance, Cstray is around 2-5pF
32 MHz
32.769 kHz
Reset
Test resistance values for this
Internal 55k pull-up on NRST
Reference design uses external
CAN
TCAN332DR
TVS Diode
STM32
U?
STM32F072CBT6
Mezzanine Connector
BergStak Receptacle
<- 3mA Max.
Debug LEDs
Blue
Blue
Green
Red
Decoupling
Local Bulk Caps
Place cap on every VCC pin
Should a Pi topology be used for input filtering?
Title
Size
A3
Number
Revision
Date: 2016-11-01
Sheet of
File: C:\Users\..\Controller-Board.SchDoc
Drawn By: