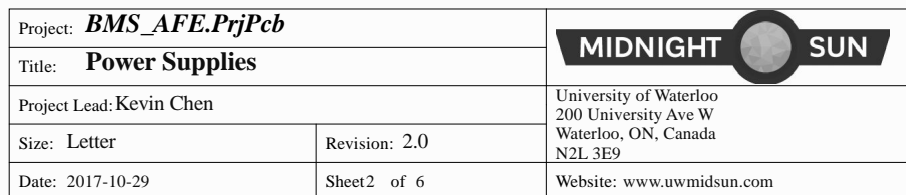
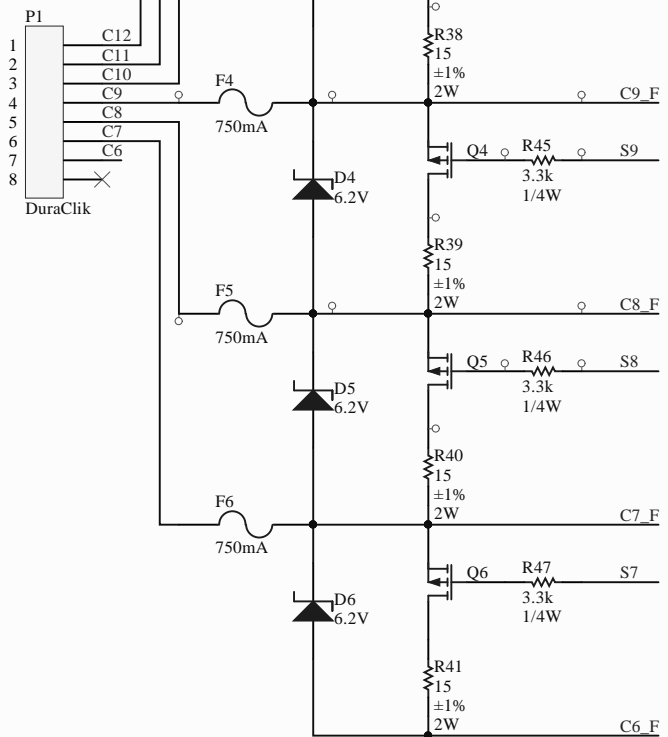


Project: <b>BMS_AFE.PrjPcb</b>		<div><div>MIDNIGHT</div><div>SUN</div></div>
Title: <b>Block Diagram</b>		
Project Lead: Kevin Chen		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9 Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a>
Size: Letter	Revision: 2.0	
Date: 2017-10-29	Sheet 1 of 6	

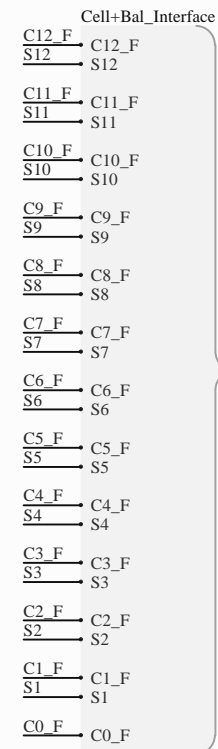
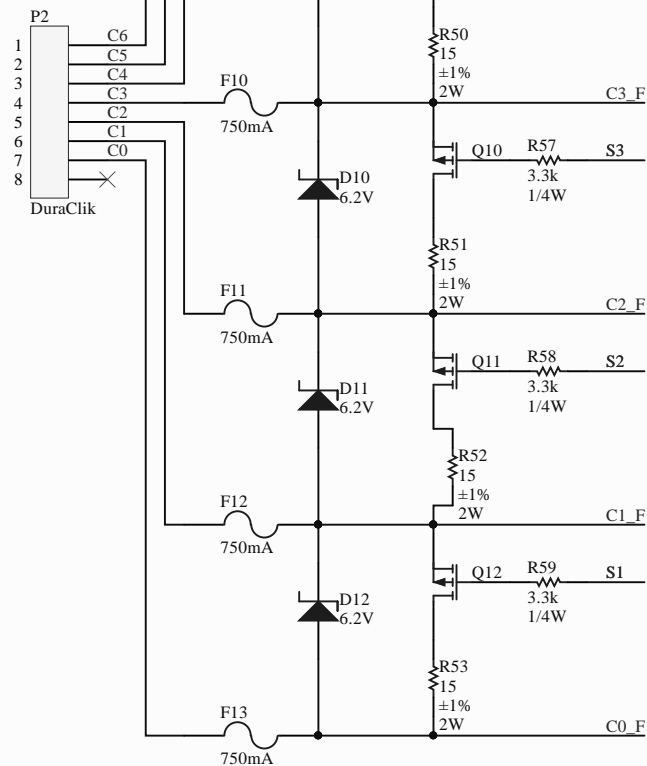





# Cell Inputs 7 - 12

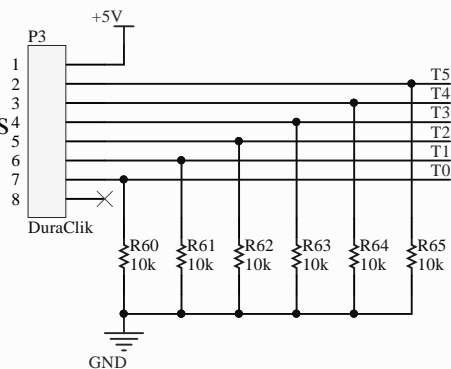


# Cell Inputs 0 - 6

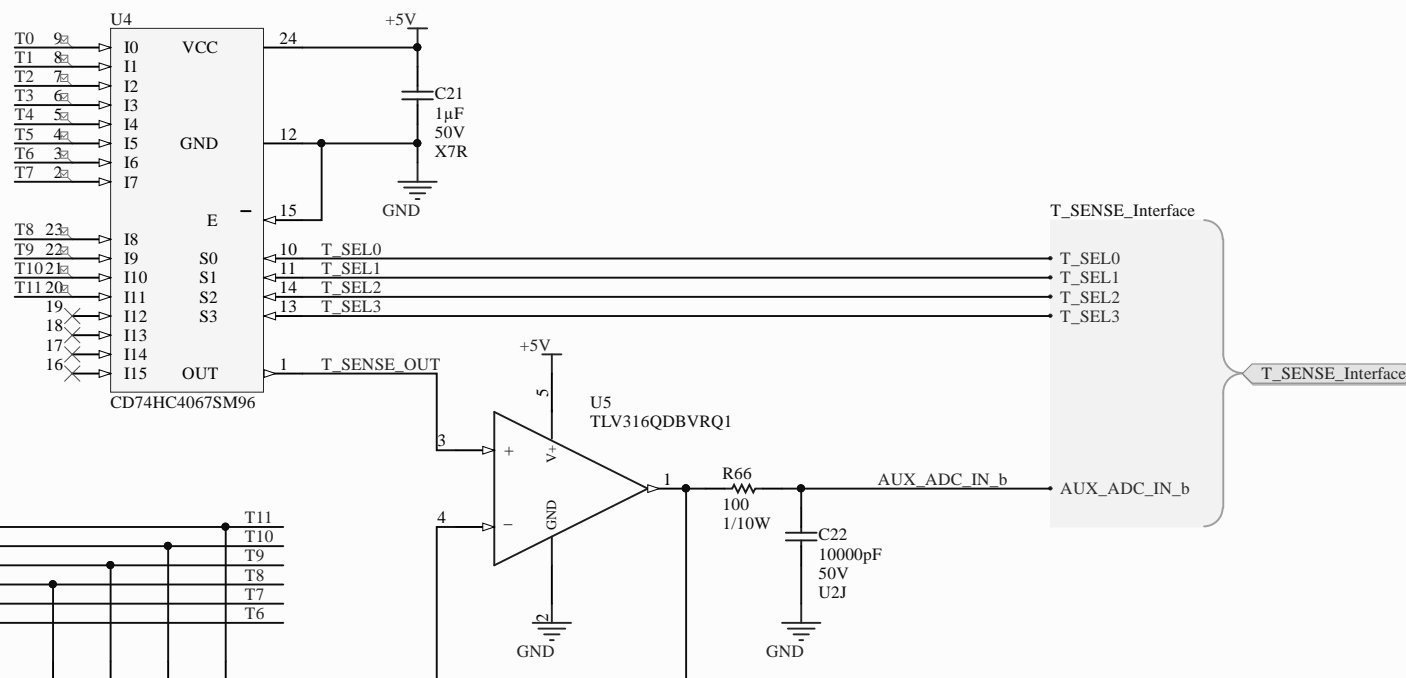
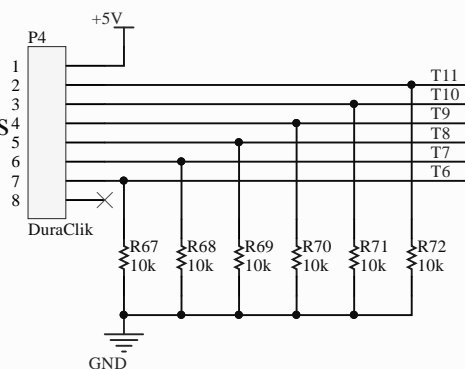



Project: <b>BMS_AFE.PrjPcb</b>		
Title: <b>Cell+Balancing Interface</b>		
Project Lead: Kevin Chen		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9  Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a>
Size: Letter	Revision: 2.0	
Date: 2017-10-29	Sheet4 of 6	

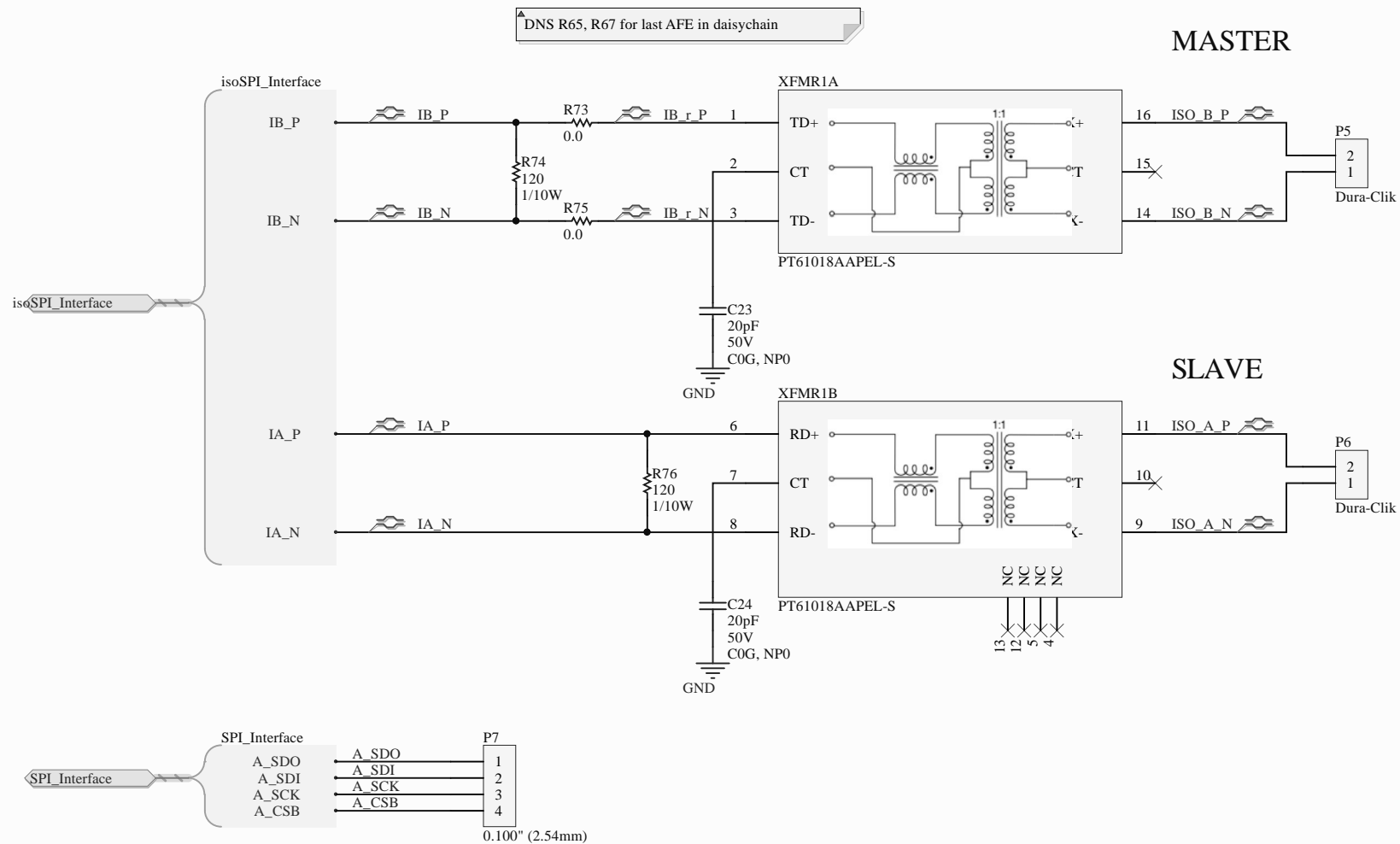
## Thermistor Inputs 0 - 5



## Thermistor Inputs 6 - 11



Project: <b>BMS_AFE.PrjPcb</b>		
Title: <b>Thermistor Interface</b>		
Project Lead: Kevin Chen		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9  Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a>
Size: Letter	Revision: 2.0	
Date: 2017-10-29	Sheet5 of 6	

Project: **BMS\_AFE.PrjPcb**Title: **isoSPI Interface**

Project Lead: Kevin Chen

Size: Letter

Revision: 2.0

Date: 2017-10-29

Sheet 6 of 6

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