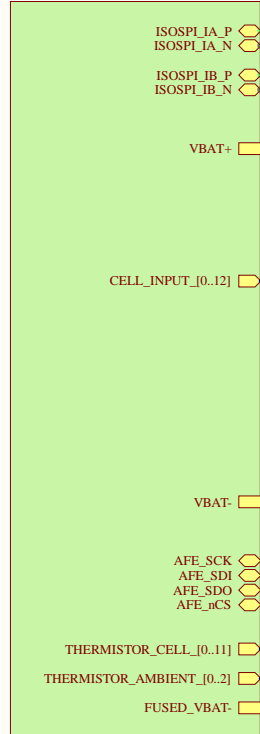
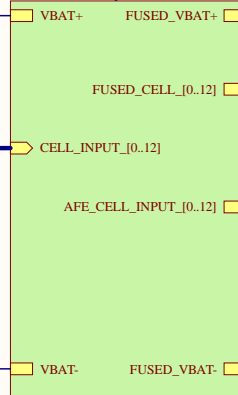


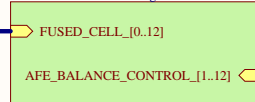
BMS AFE - Connectors.SchDoc



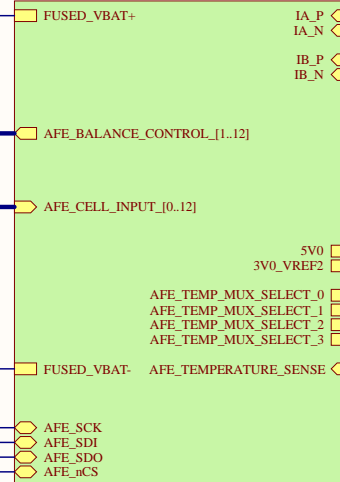
BMS AFE - Cell Inputs.SchDoc



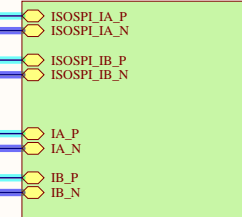
BMS AFE - Cell Balancing.SchDoc



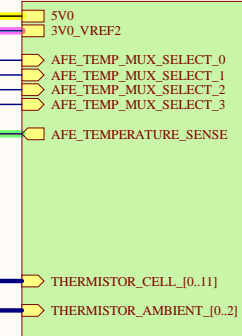
BMS AFE - AFE.SchDoc




BMS AFE - Communications.SchDoc

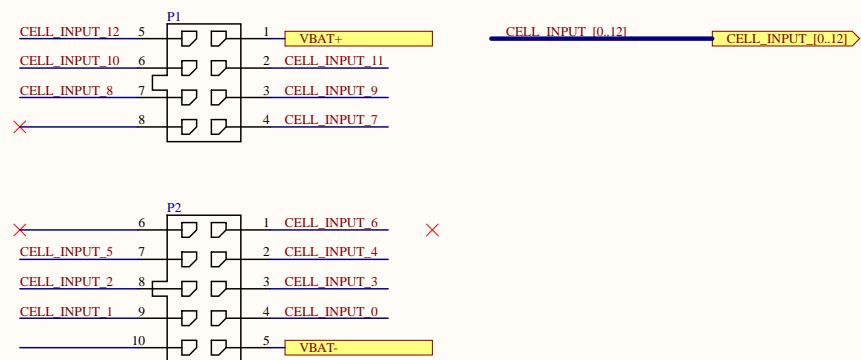


BMS AFE - Thermistors.SchDoc

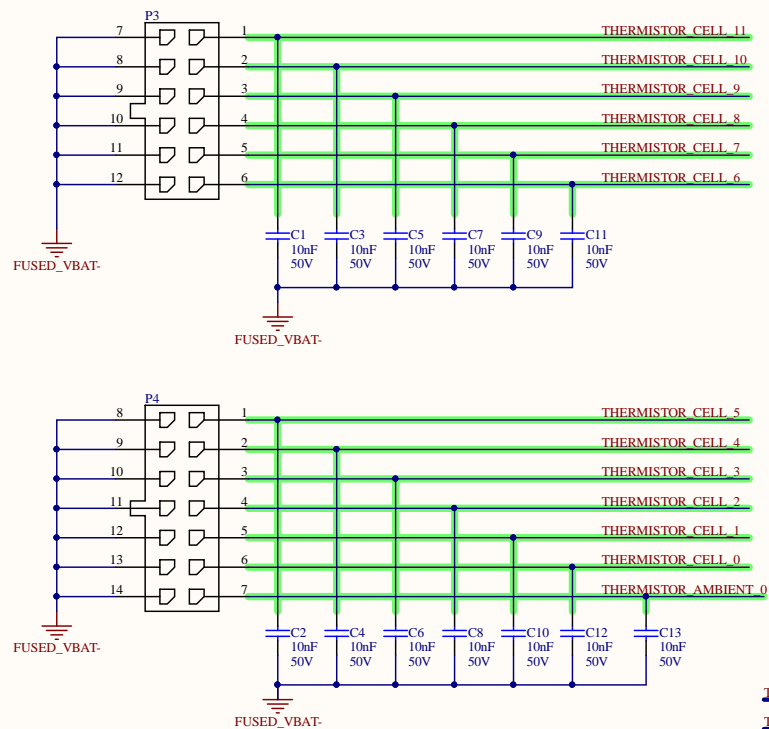


PROJECT		BMS_AFE.PrjPcb		 Engineering 5 - 1002 University of Waterloo (519) 888-4567 x32978 hardware@uwmidsun.com
DOCUMENT		BMS AFE - Top Sheet.SchDoc		
PART NUMBER	MS40005	VARIANT	01 - Standard	
DRAWN BY	Taiping Li	REVISION	4.0	
LAST MODIFIED	2018-11-16	SHEET	1 OF 7	

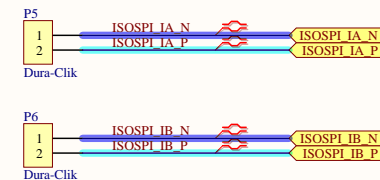
Cell Inputs



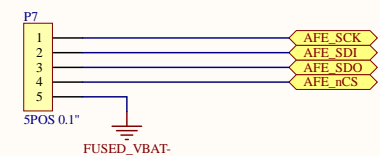
Cell Thermistors



isoSPI Connectors

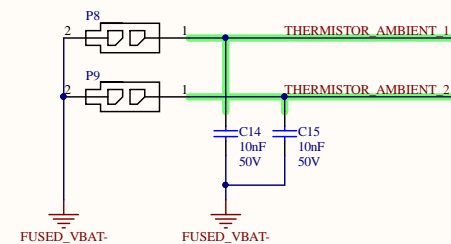



Optional 4-wire SPI

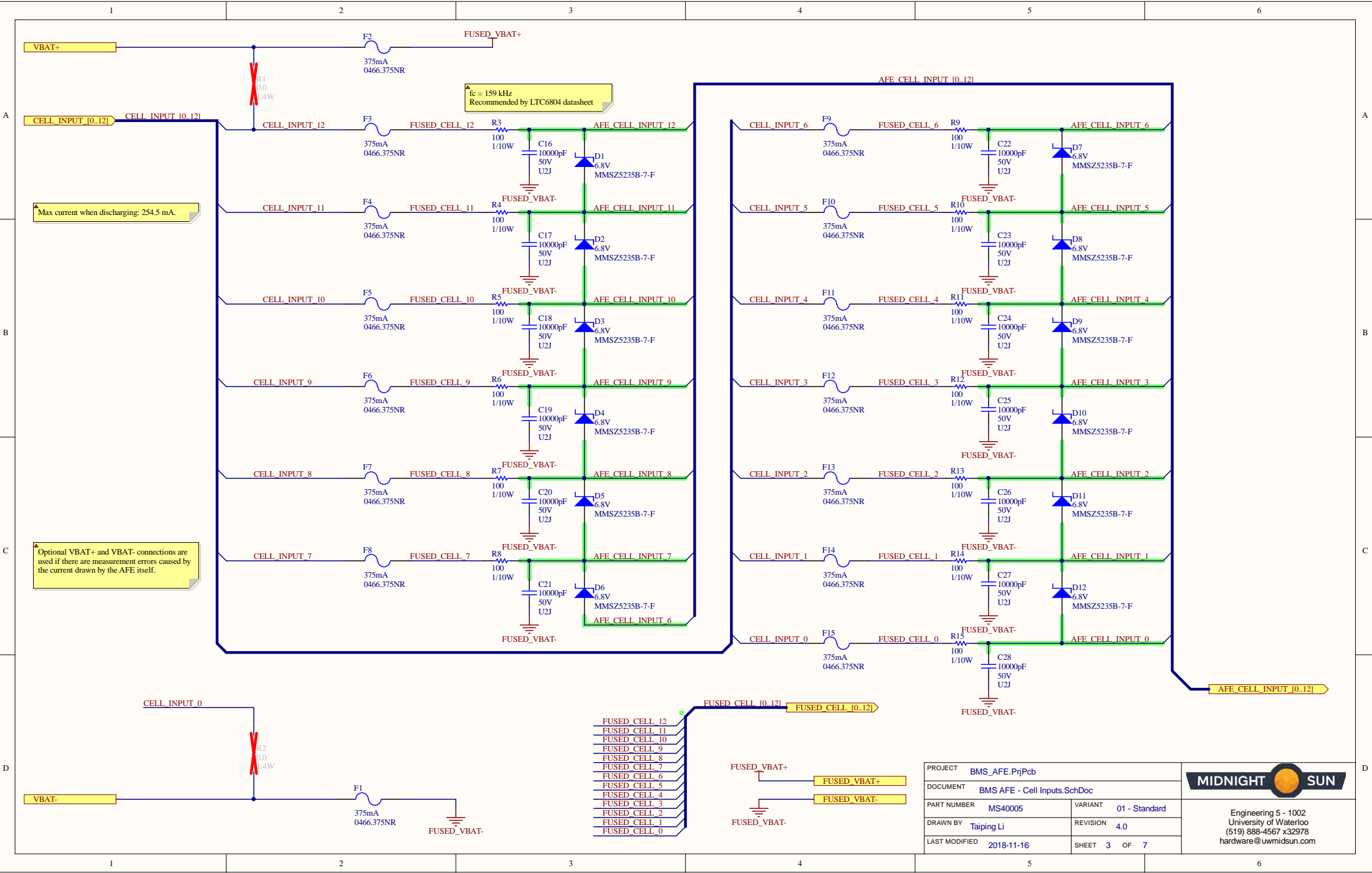


▲ Use variant Debug - SPI and solder on a 0.1" header to talk to the LTC6804 via SPI. Otherwise, communication should be isoSPI.

Ambient Thermistors



PROJECT		BMS_AFE_PrtPcb		
DOCUMENT		BMS AFE - Connectors.SchDoc		
PART NUMBER	MS40005	VARIANT	01 - Standard	
DRAWN BY Taiping Li		REVISION	4.0	
LAST MODIFIED		2018-11-16		Engineering 5 - 1002 University of Waterloo (519) 888-4567 x32978 hardware@uwmidsun.com
		SHEET	2 OF 7	

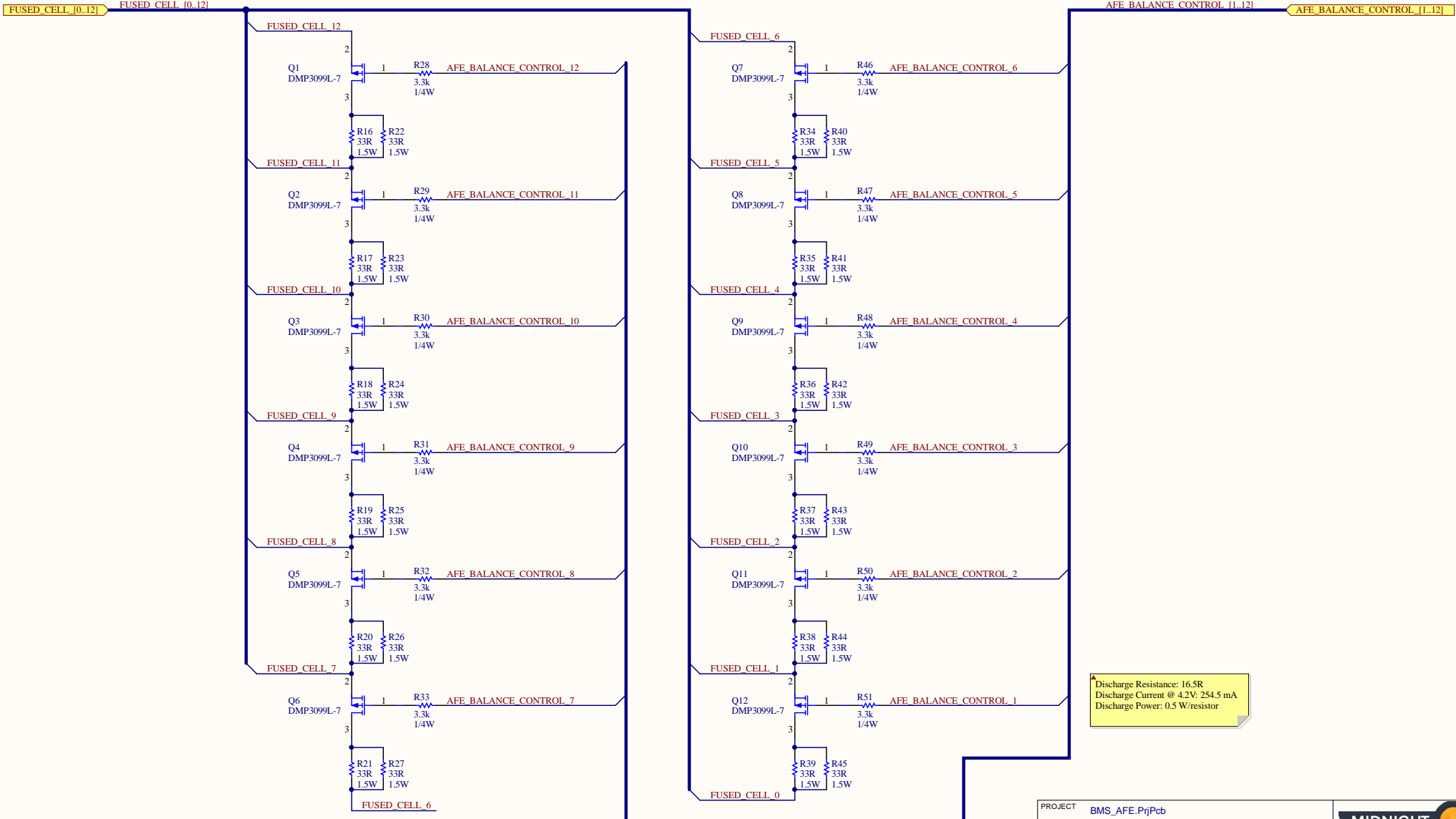


PROJECT		BMS_AFE.PrjPcb	
DOCUMENT		BMS AFE - Cell Inputs.SchDoc	
PART NUMBER	MS40005	VARIANT	01 - Standard
DRAWN BY	Taiping Li	REVISION	4.0
LAST MODIFIED	2018-11-16	SHEET	3 OF 7

MIDNIGHT

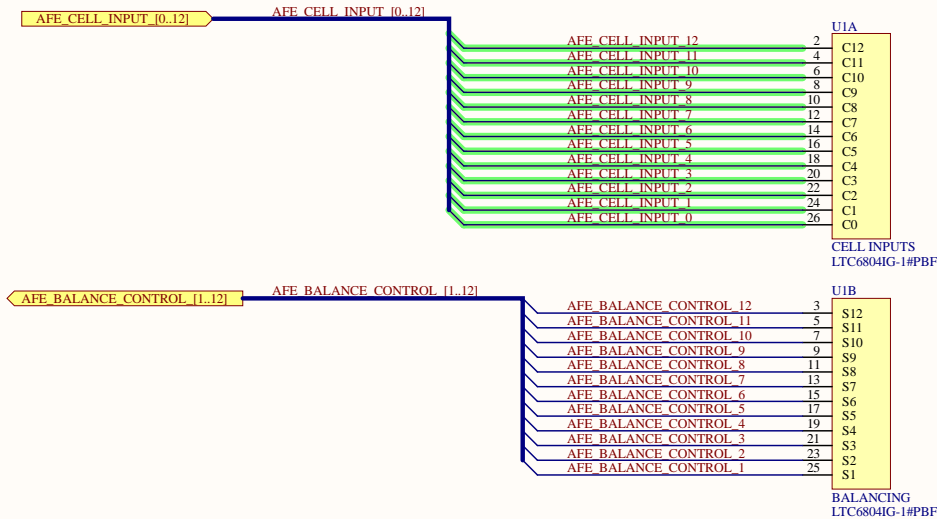
SUN

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hardware@uwmidsun.com

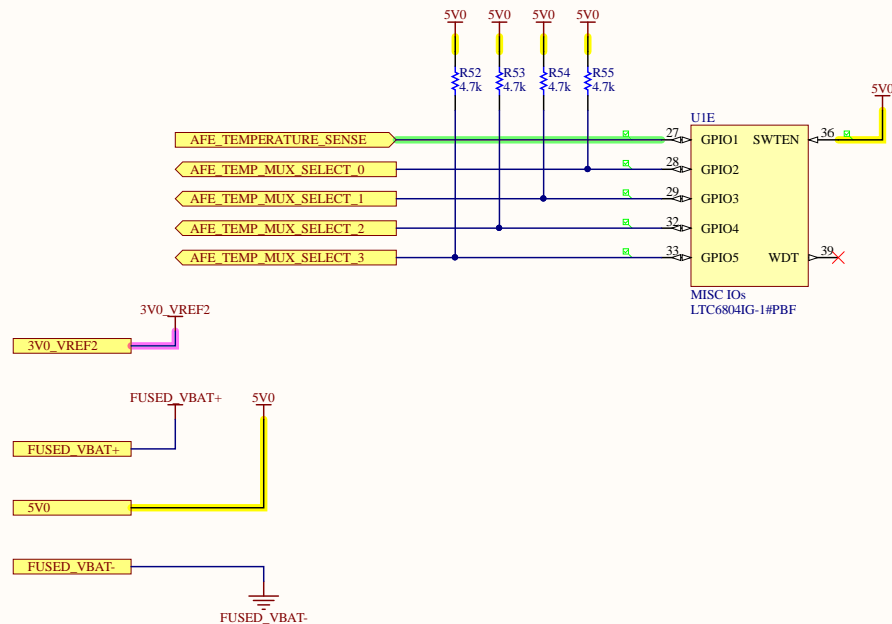


PROJECT		BMS_AFE.PrjPcb		<div><div>MIDNIGHT</div><div>SUN</div></div> <div>Engineering 5 - 1002 University of Waterloo (519) 888-4567 x32978 hardware@uwmidsun.com</div>
DOCUMENT		BMS AFE - Cell Balancing.SchDoc		
PART NUMBER	MS40005	VARIANT	01 - Standard	
DRAWN BY	Taiping Li	REVISION	4.0	
LAST MODIFIED	2018-11-16	SHEET	4 OF 7	

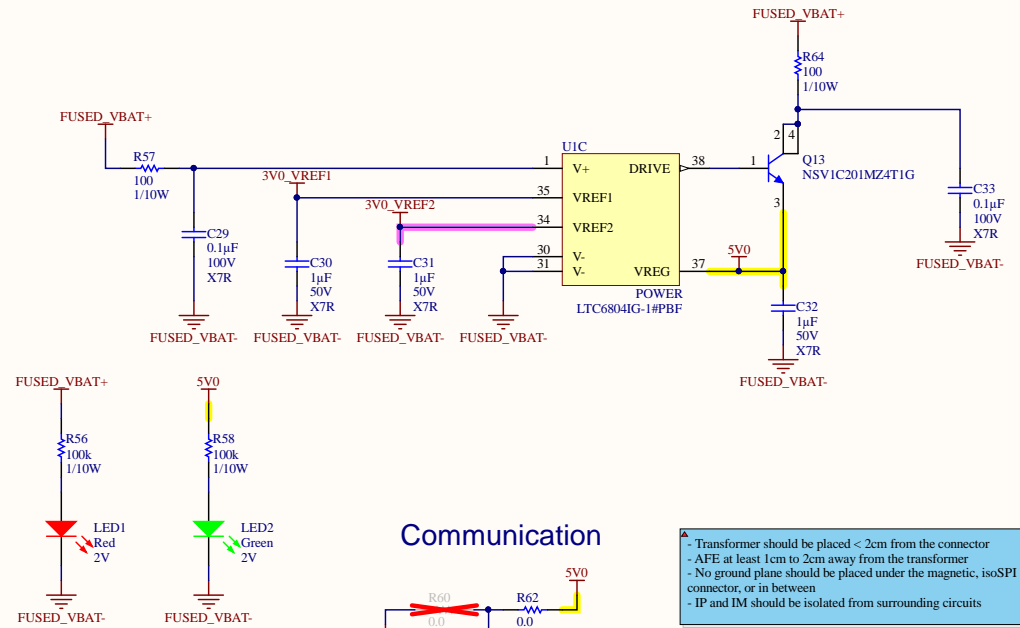
Cell & Balancing Inputs



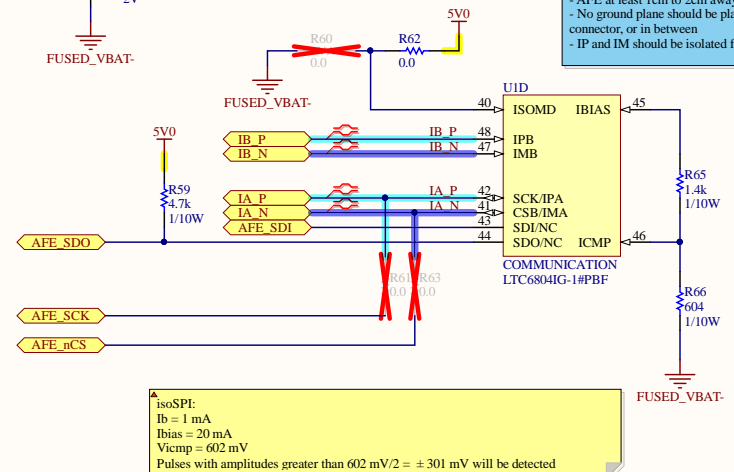
GPIOs



Power




Communication

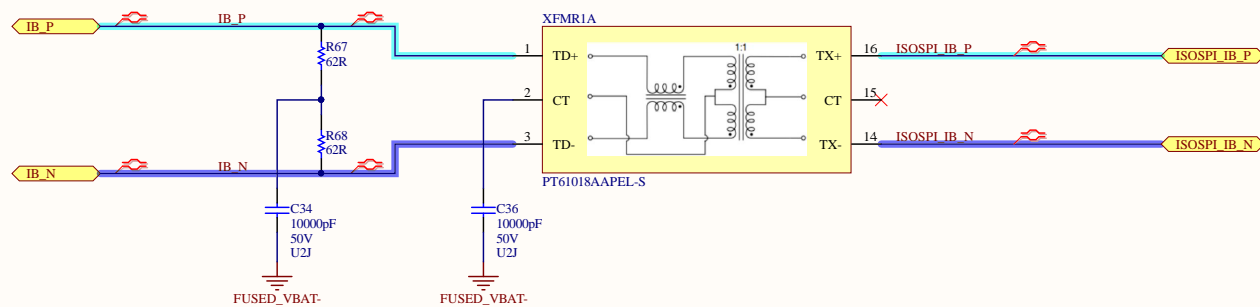


Transformer should be placed < 2cm from the connector
 - AFE at least 1cm to 2cm away from the transformer
 - No ground plane should be placed under the magnetic, isoSPI connector, or in between
 - IP and IM should be isolated from surrounding circuits

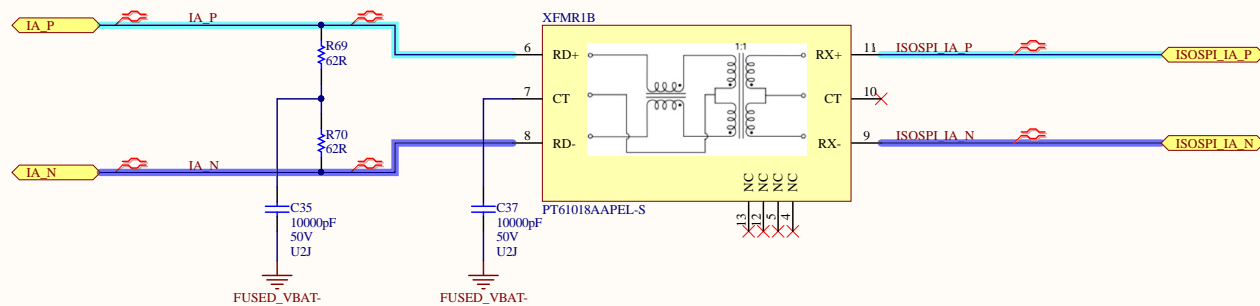
isoSPI:
 Ib = 1 mA
 Ibias = 20 mA
 Vicmp = 602 mV
 Pulses with amplitudes greater than 602 mV/2 = ± 301 mV will be detected

PROJECT		BMS_AFE.PrjPcb		 Engineering 5 - 1002 University of Waterloo (519) 888-4567 x32978 hardware@uwmidsun.com
DOCUMENT		BMS AFE - AFE.SchDoc		
PART NUMBER	MS40005	VARIANT	01 - Standard	
DRAWN BY	Taiping Li	REVISION	4.0	
LAST MODIFIED	2018-11-16	SHEET	5 OF 7	

isoSPI MASTER - TO NEXT AFE



SLAVE - FROM LTC6820



PROJECT		BMS_AFE.PrjPcb		<div><div>MIDNIGHT</div><div>SUN</div></div> <p>Engineering 5 - 1002 University of Waterloo (519) 888-4567 x32978 hardware@uwmidsun.com</p>
DOCUMENT		BMS AFE - Communications.SchDoc		
PART NUMBER	MS40005	VARIANT	01 - Standard	
DRAWN BY	Taiping Li	REVISION	4.0	
LAST MODIFIED	2018-11-16	SHEET	6 OF 7	

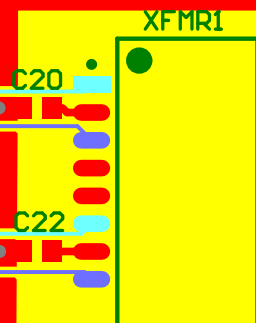
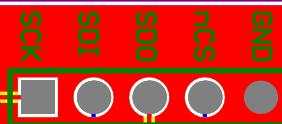
MIDNIGHT SUN XII

MSX12 BMS AFE
REV 4.0

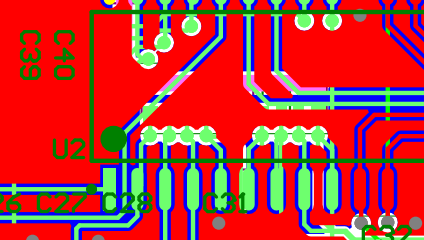
TO NEXT AFE

IB P
IB N

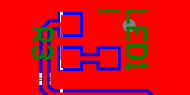
IA P
IA N
TO CARRIER



R73 R74



VBAT FUSED



P1
C12 VBAT+
C10 C11
C8 C9
NC C7

P4
NC C6
C5 C4
C2 C3
C1 C0
NC VBAT-