Digital Design & Computer Arch.

Problem Solving V

Prof. Onur Mutlu

ETH Zürich
Spring 2023
21 July 2023

Problem Solving V

- ## We solve 12 questions from Final Exam Spring 2022

  - ❑ Boolean Logic Circuits (Q1)

  - ❑ Finite State Machine (Q2)

  - ❑ ISA vs. Microarchitecture (Q3)

  - ❑ Verilog (Q4)

  - ❑ Memory Potpourri (Q5)

  - ❑ Performance Evaluation (Q6)

  - ❑ Pipelining (Q7)

  - ❑ Tomasulo's Algorithm (Q8)

  - ❑ GPUs and SIMD (Q9)

  - ❑ Branch Prediction (Q10)

  - ❑ Prefetching (Q11)

  - ❑ Caches (Q12)

Digital Design & Computer Arch.

Problem Solving V

Prof. Onur Mutlu

ETH Zürich
Spring 2023
21 July 2023