$$(0) (615,3)_{8} = (1000|101.011)_{2}$$

$$= (000|1000|101.0110)_{2}$$

$$= (180.6)_{16}$$

(b)
$$0.18/5 \times 2 = 0.3/5$$

 $0.3/5 \times 2 = 0.5$
 $0.95 \times 2 = 1.5$
 $0.5 \times 2 = 1$
 $(0.18/5)_{10} = (0.0011)_2$
 $(25.18/5)_{10} = (11001.0011)_2$
 $= (011 001.0011)_2$
 $= (31.14)_0$

$$13 = (1101)_{2} 49 = (110001)_{2} 28 = (11100)_{2}$$

$$-13 = (1110011)_{2} -49 = (1001111)_{2}$$

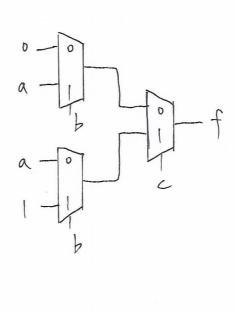
Overflow detection in hardware:

lompare the carry into the last bit with

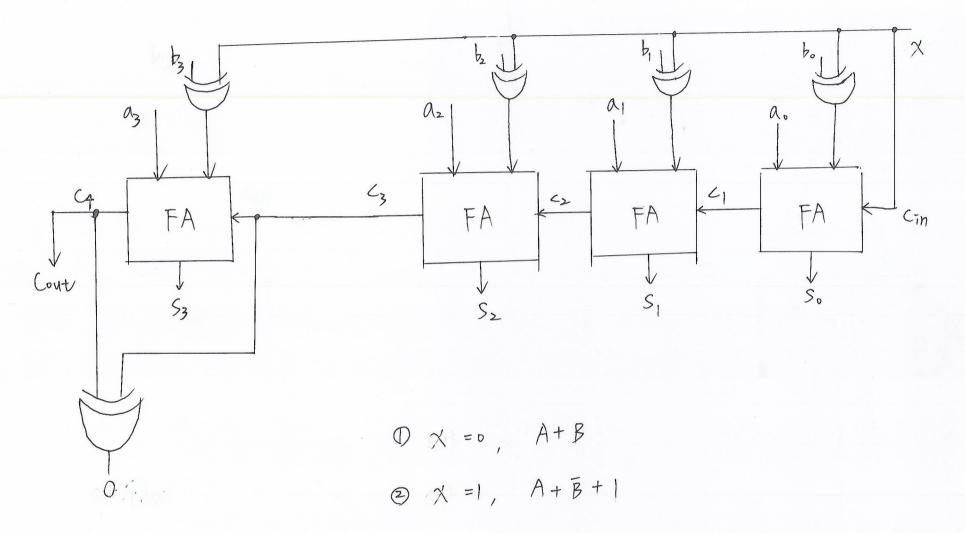
the carry out of the last bit,

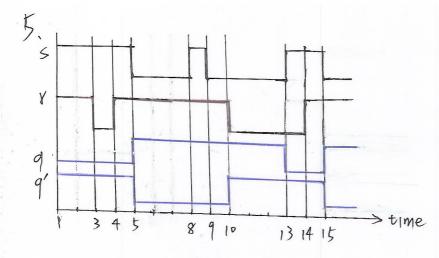
If they are different, there is overflow.

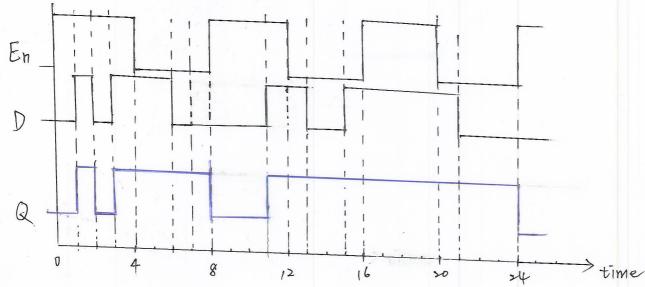
i (a) (b) both have no overflow.



4.





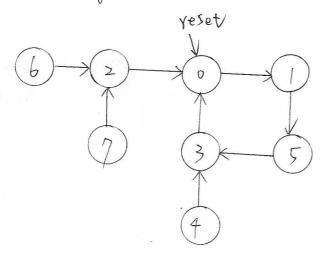


1	PS			n5			5,50		nS_2		
6.	52	51	5.	n52	ns,	ns.	S2/2-		/ 9	0	
	0	0	D	0	0	1		0		0	χ̈́
	O	0	1	1	0	1	52	X	D	» X	×
	ð	1	0	×	X	X	5,50		ns	5	,)
	D	1	1	0	0	0	Sz	0	0	0	X
	1	0	0	X	X	X		X	1	X	X
	1	0	1	0	1	1	5,50		ns	· •	
	1	1	0	X	X	X	Sz	1	Develop	0	X
	(1	1	X	X	X		X	,	° X	×
			1				-				
		sta		tab	le				0	-0	_

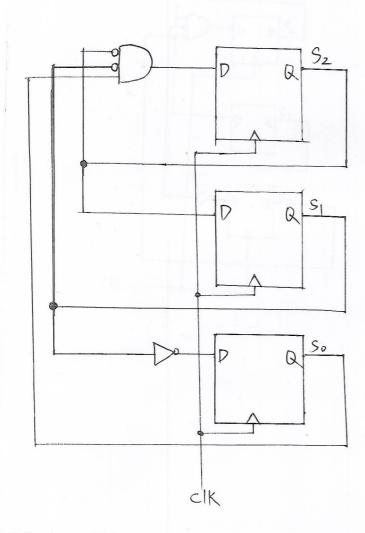
					L
nsz	=	Sol	$\sqrt{S_i}$	٨	5/2
n 91					
n5	Ξ	G.			

	7		
525150	nsz	nS,	ns.
0 0 0	0	0	1
00	1	0	1
0 0	0	0	0
0	0	O	O
00	0	1	1
(0)	Ø	1	1
[[0	0	1	O
1 1 1	0	1	ō
optim	1 pos	(+a+a	+-11

6. State diagram =



logic diagram:



7. (a) Mealy Machine

