Report Format Example

I.   Project Name(Ex : lab1_1, BCD to excess3 decoder…etc)

Design Specification

- ✓ Write down the specification of your design: inputs, outputs and related bit widths. The name should be the same as what you use in the Verilog design files.

   Example : For a 4-bit absolute value calculator:
   Input: a[3:0].
   Output: da[3:0].

- ✓ Draw the block diagram of the design.

Design Implementation

- ✓ Explain how you design the function and how the function works for every block in your block diagram.

- ✓ List the I/O pin assignment for your design. (we don't have this in lab1)

- ✓ 不要只在報告內貼上你的 code 而不做任何詳細的說明，code 只是實現你的想法的工具，報告內容要看到的是你如何設計、為什麼這麼設計、功能如何實現等等的設計流程。

II.   Project Name(Ex : lab1_2…etc)

Design Specification

<p align="center">…</p>

Design Implementation

<p align="center">…</p>

III.   Project Name(Ex : lab1_3…etc)

Design Specification

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Design Implementation

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Discussion

- ✓ Record the thoughts you have throughout this experiment.
- ✓ Explains why the results came out as you expected, or the things went wrong if they **did** go wrong.
- ✓ Analyze and explain the experiment results, including any thoughts, comments, or suggestions related to this experiment.

Conclusion

- ✓ Summarize what you have learned after finishing this experiment.

References

- ✓ Write down what you have found for this lab. Do not just write the URL or title of the references but write down something you have learned from the references.