

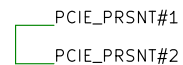
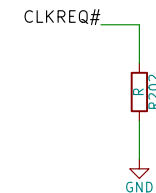
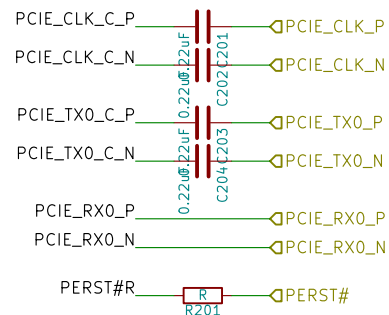
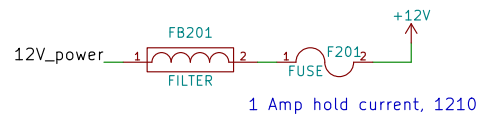
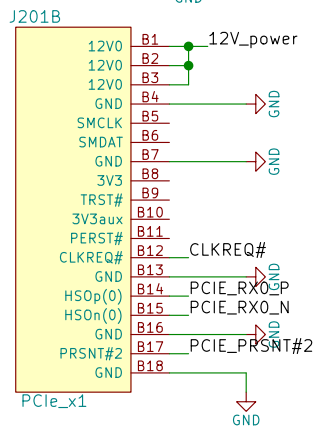
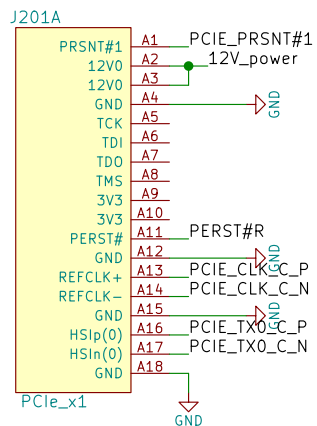
Sheet: /
File: artix7_pcie_pcb.sch

Title:

Size: A4
KiCad E.D.A. kicad 4.0.1—stable

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Jumper for presence detect

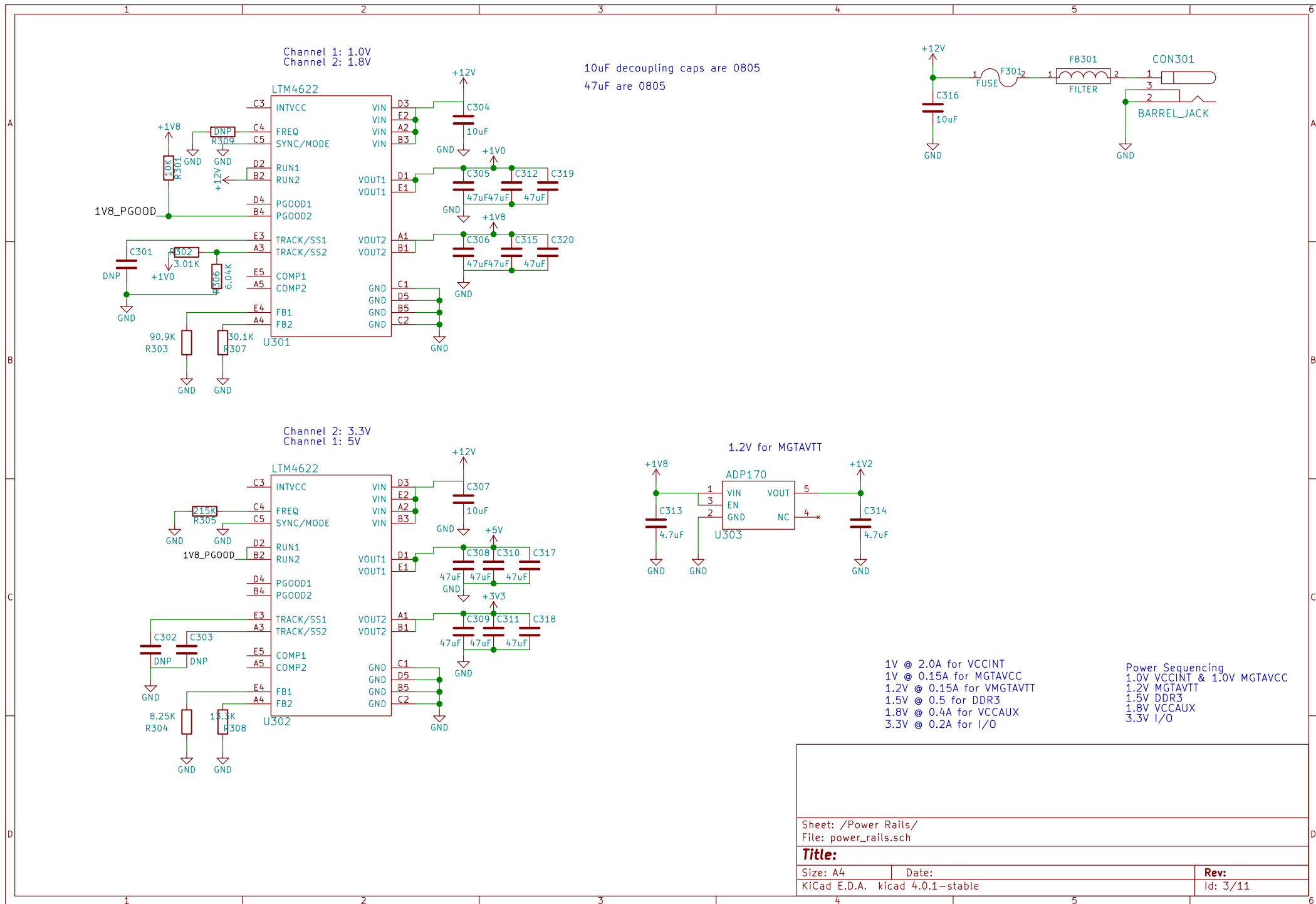
Sheet: /PCIe Connector/
File: pcie_conn.sch

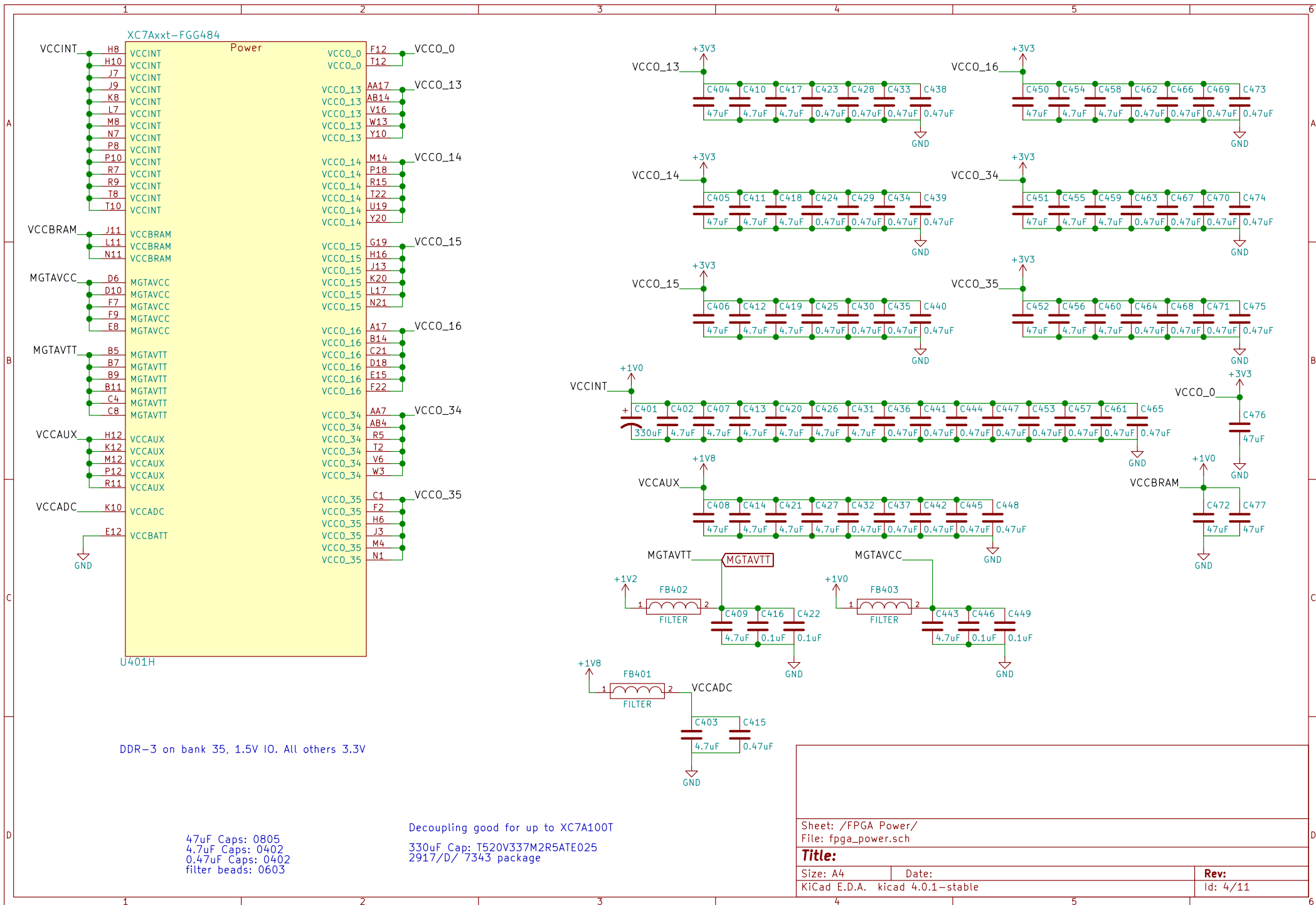
Title:

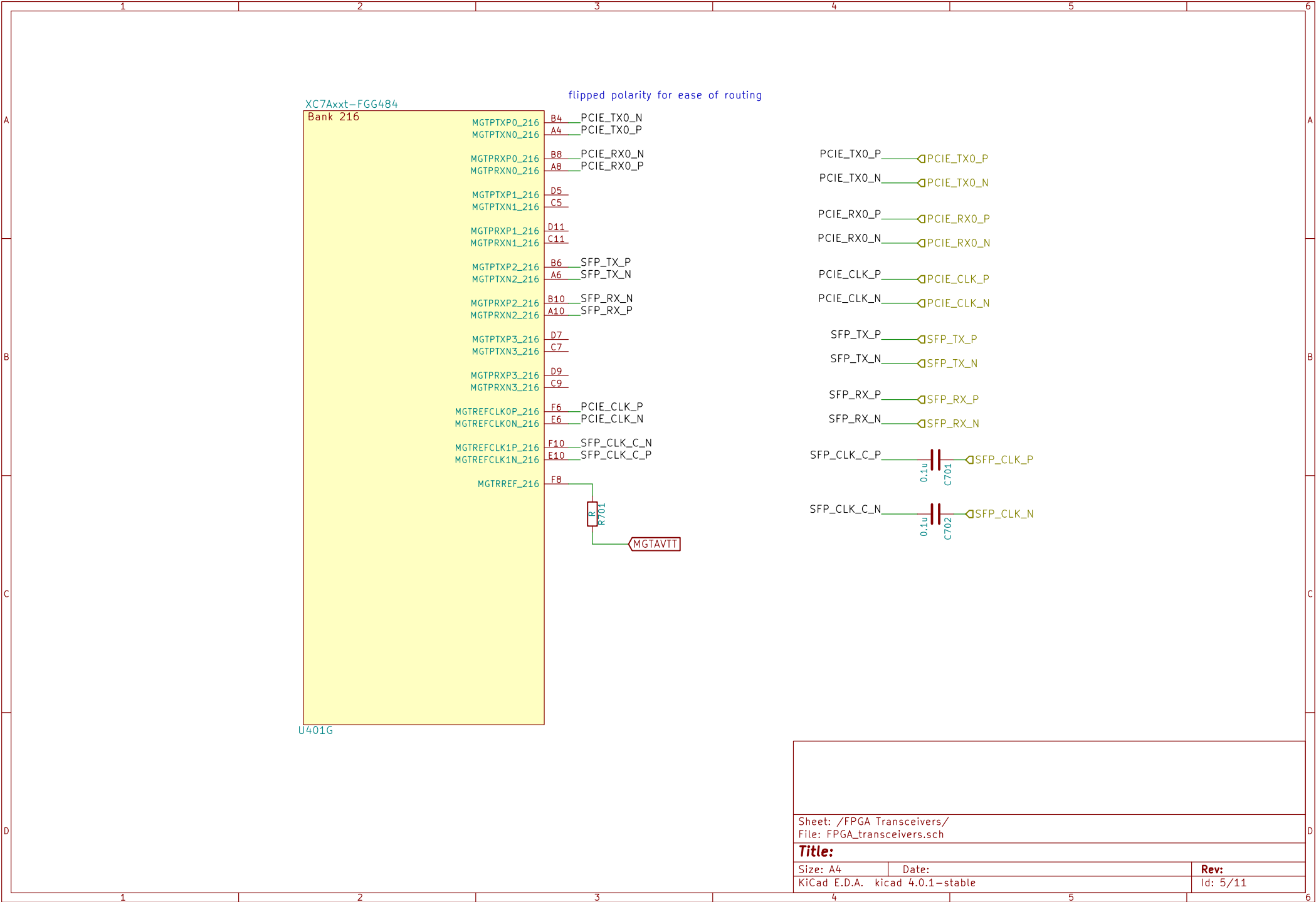
Size: A4
KiCad E.D.A. kicad 4.0.1-stable

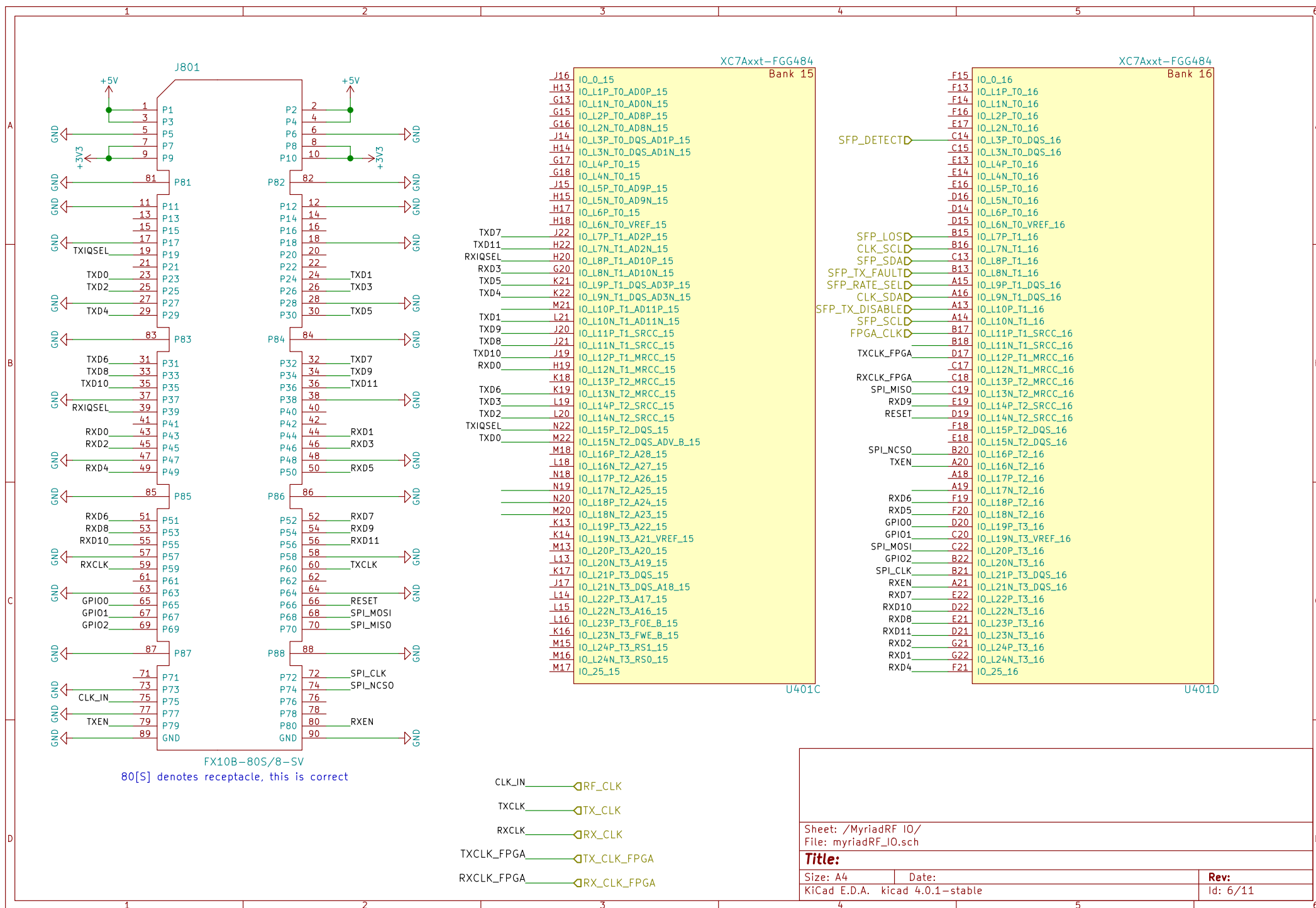
Date:

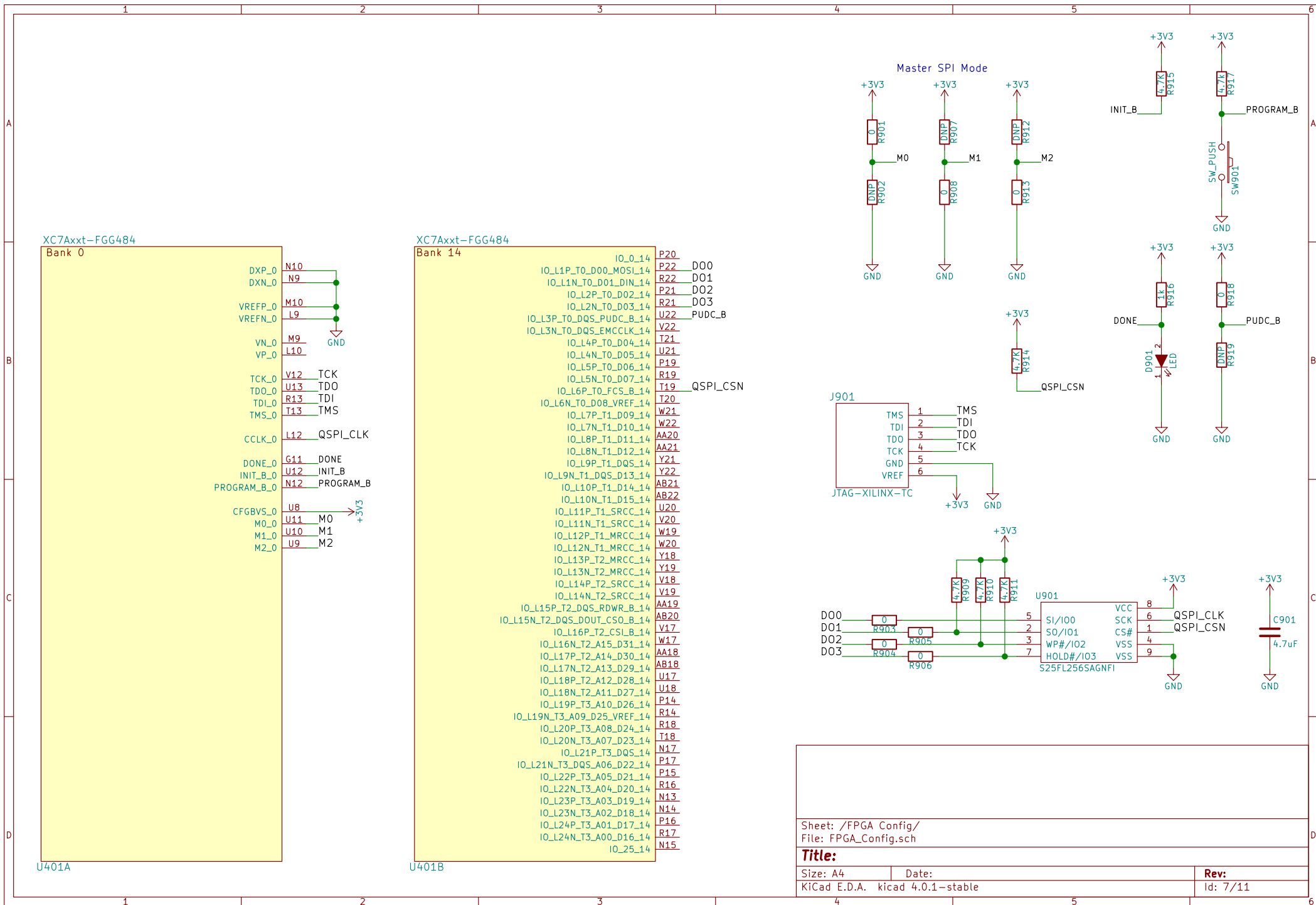
Rev:
Id: 2/11



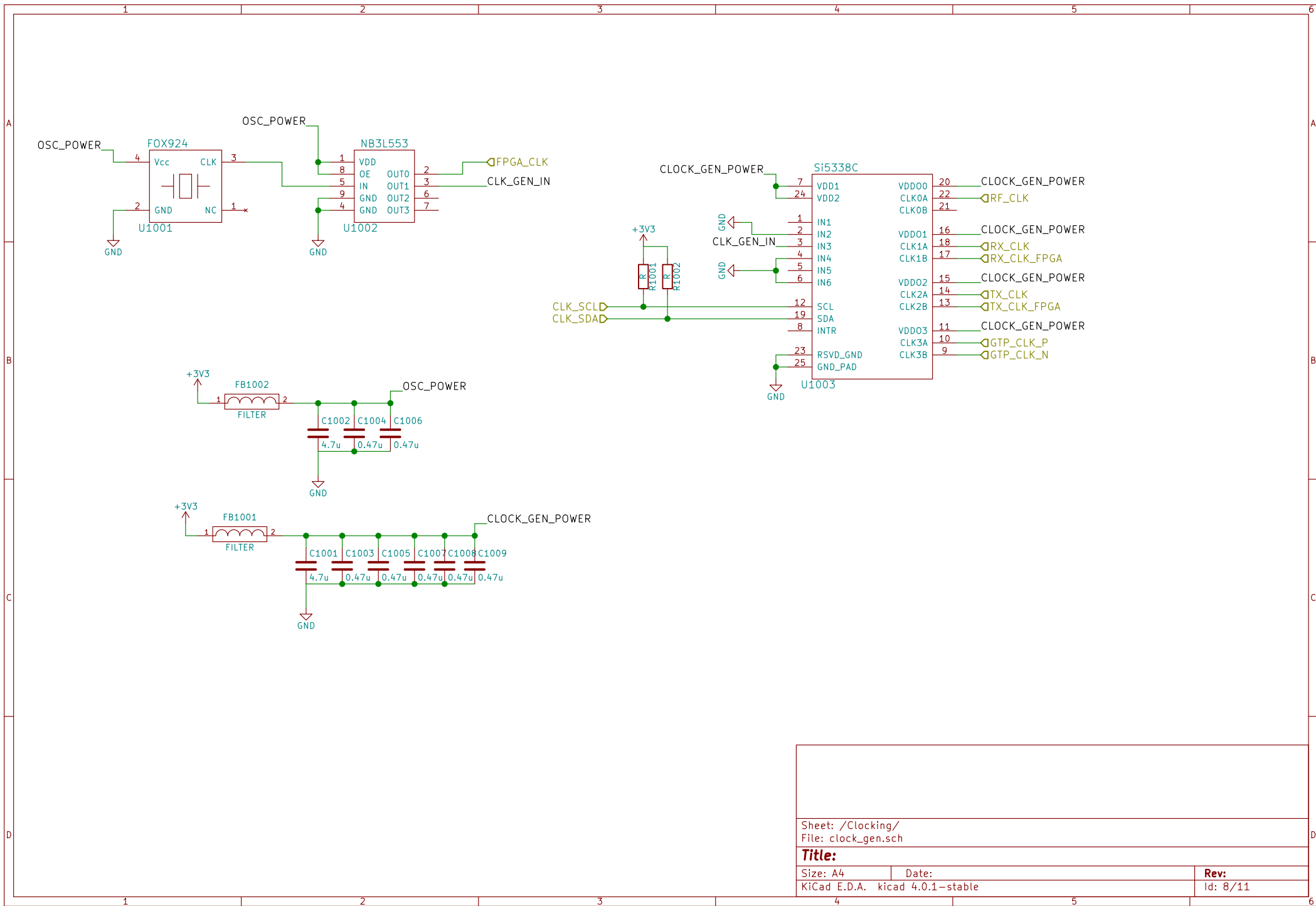








Sheet: /FPGA Config/ File: FPGA_Config.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 4.0.1--stable		Id: 7/11



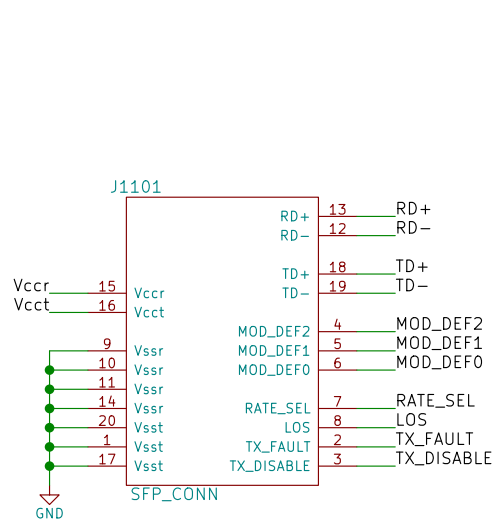
Sheet: /Clocking/
File: clock_gen.sch

Title:

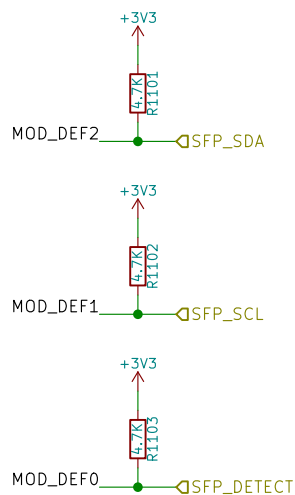
Size: A4
KiCad E.D.A. kicad 4.0.1-stable

Date:

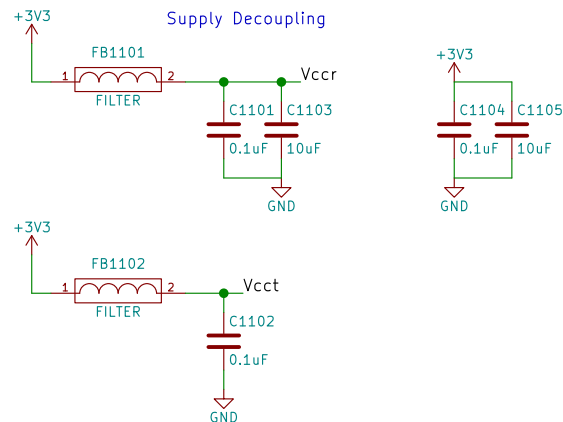
Rev:
Id: 8/11



SFP has internal AC coupling caps

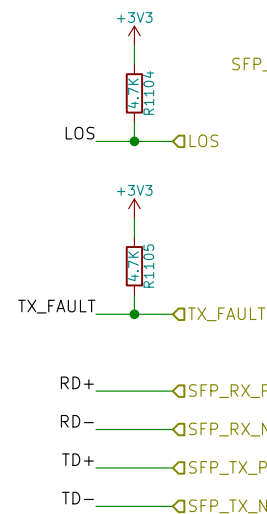


Supply Decoupling



SFP_RATE_SEL — RATE_SEL

SFP_TX_DISABLE — TX_DISABLE



RD+ — SFP_RX_P
RD- — SFP_RX_N
TD+ — SFP_TX_P
TD- — SFP_TX_N

Sheet: /SFP/
File: SFP.sch

Title:

Size: A4
KiCad E.D.A. kicad 4.0.1—stable

Date:

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Id: 9/11

