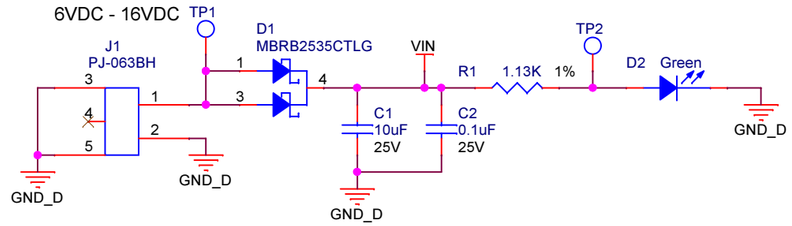
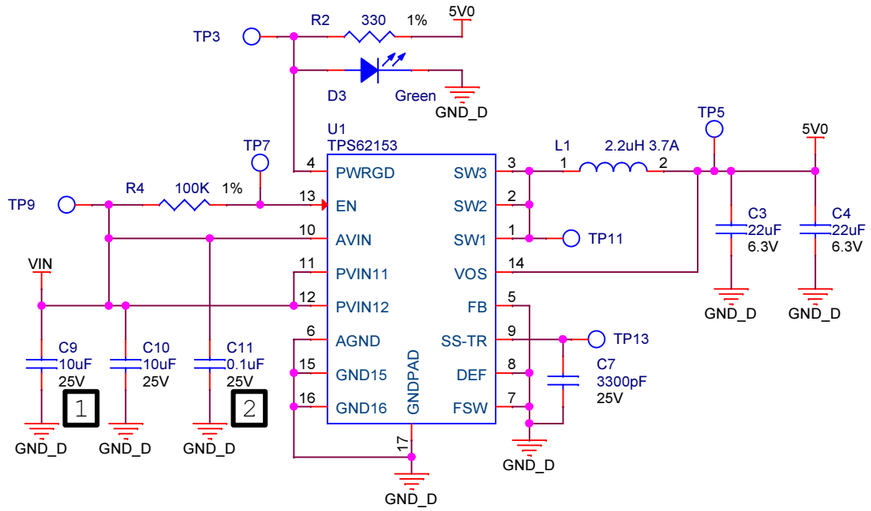
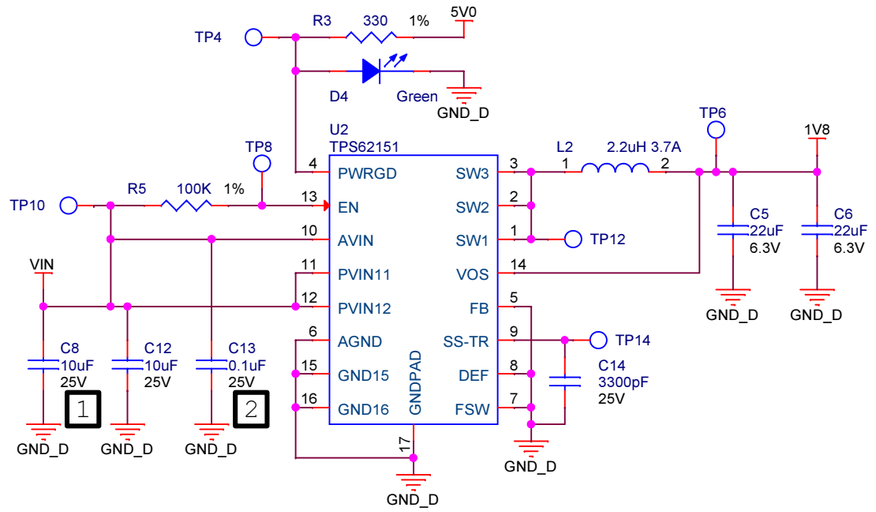
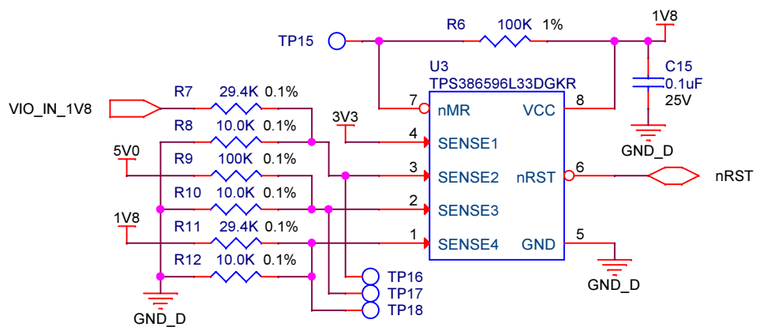
Rev.	Description	CO Number	Date	Approved By	Checked By
1.2	Refer to Change Order 8	CO8	10/05/2015	BEH	JJW









NOTES:

1. Place decoupling capacitors as close to IC pins as possible.

krtkl inc.
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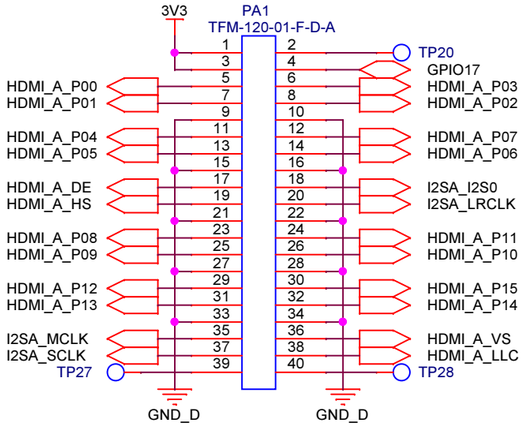
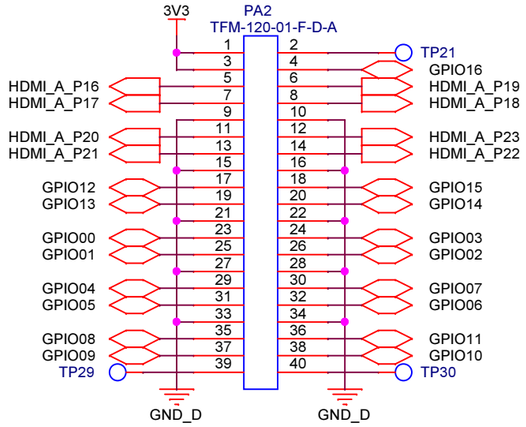
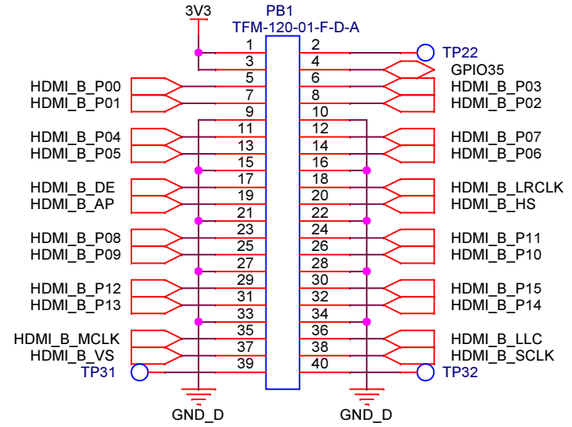
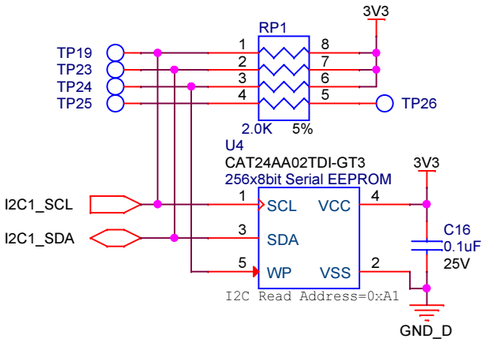
	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
CHECKED BY	J. Weatherbee	10/05/2015
APPROVED BY	R. Cousins	10/05/2015

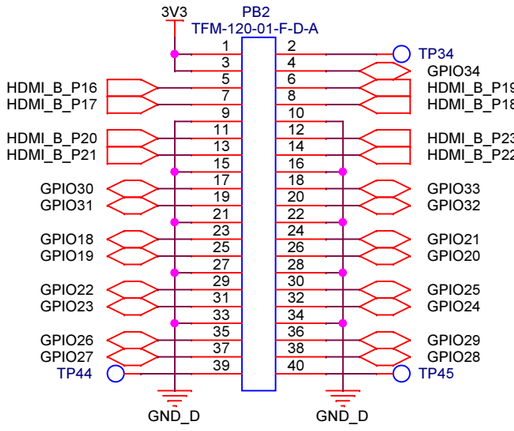
TITLE: piSmasher SBC
PATH: /
DESCRIPTION: Power Supplies
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 1 OF 7

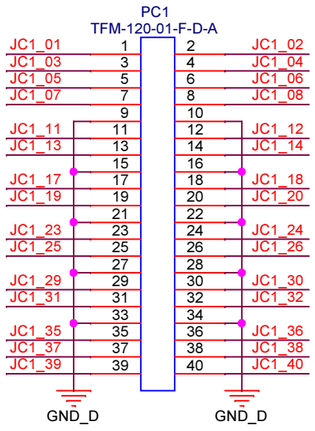


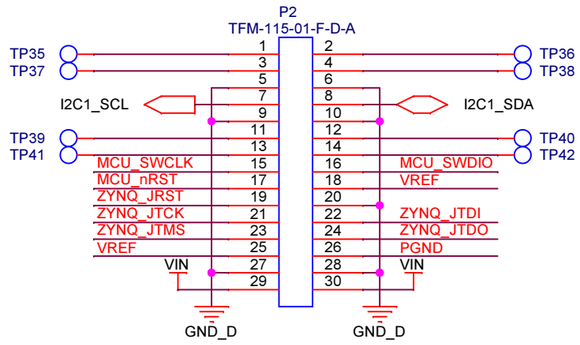


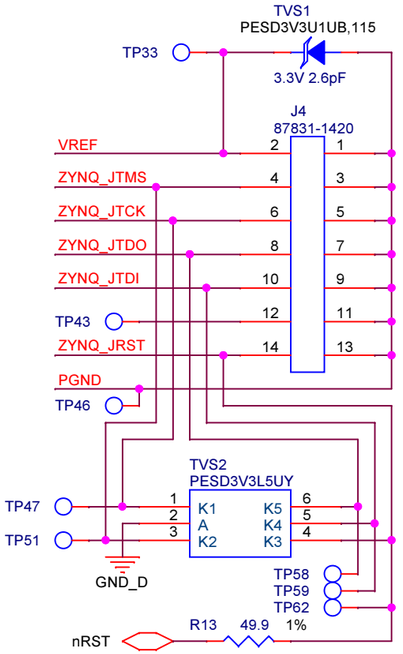


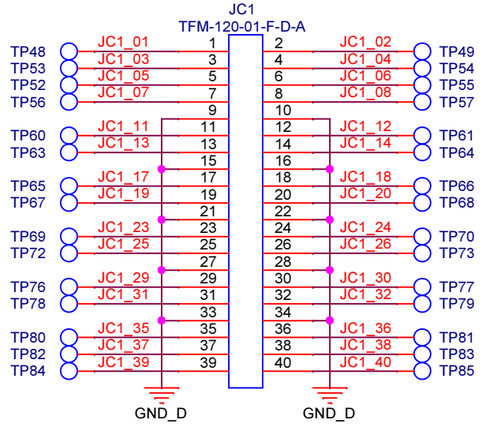


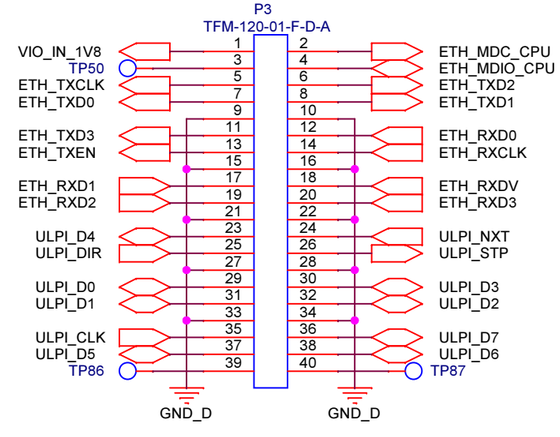


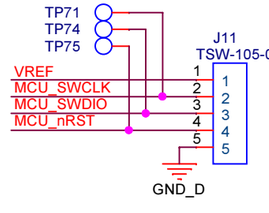












NOTES:

1. Place decoupling capacitors as close to IC pins as possible.

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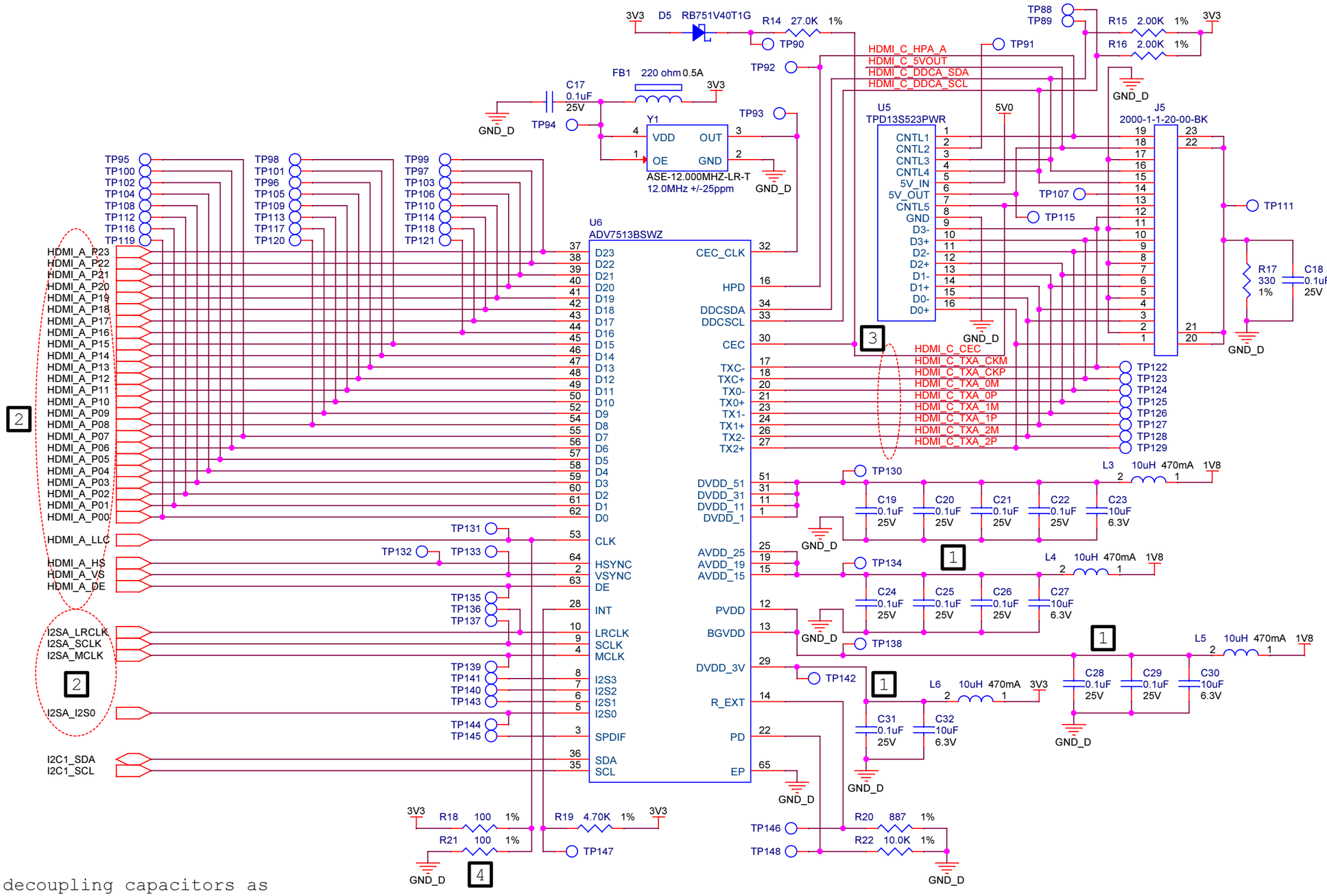
	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
CHECKED BY	J. Weatherbee	10/05/2015
APPROVED BY	R. Cousins	10/05/2015

TITLE: piSmasher SBC
PATH: /
DESCRIPTION: Main Interconnect
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 2 OF 7



NOTES:

1. Place decoupling capacitors as close to IC pins as possible.
2. High speed signal traces should be length matched to within 50mils for each set of grouped signals.
3. Route signals with 50ohm single, 100ohm differential impedance.
4. Place resistors as close to IC pin as possible with minimum trace stubs.

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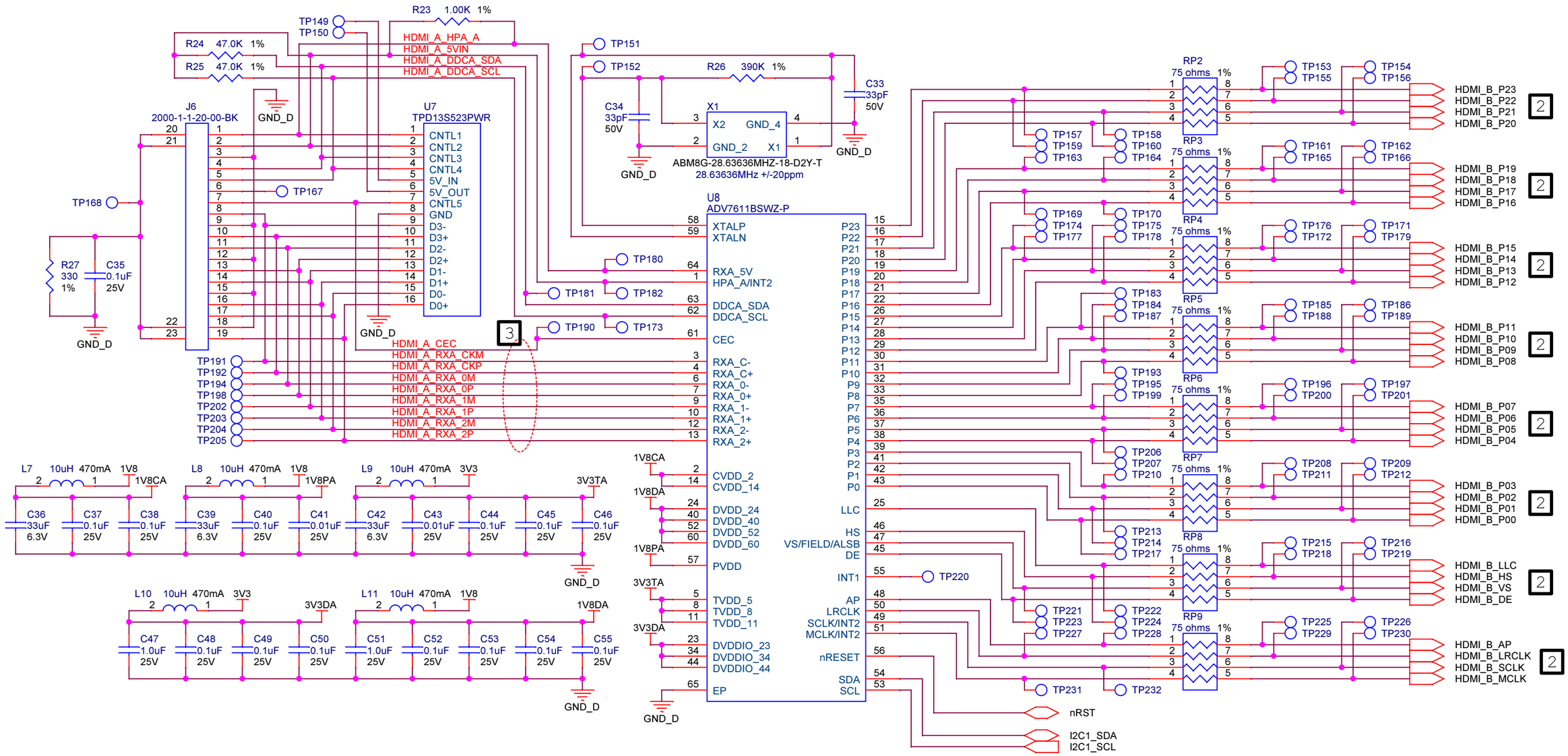
	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
CHECKED BY	J. Weatherbee	10/05/2015
APPROVED BY	R. Cousins	10/05/2015

TITLE: piSmasher SBC
PATH: /
DESCRIPTION: HDMI Transmitter
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 3 OF 7



NOTES:

1. Place decoupling capacitors as close to IC pins as possible.
2. High speed signal traces should be length matched to within 50mils for each set of grouped signals.
3. Route signals with 50ohm single, 100ohm differential impedance.

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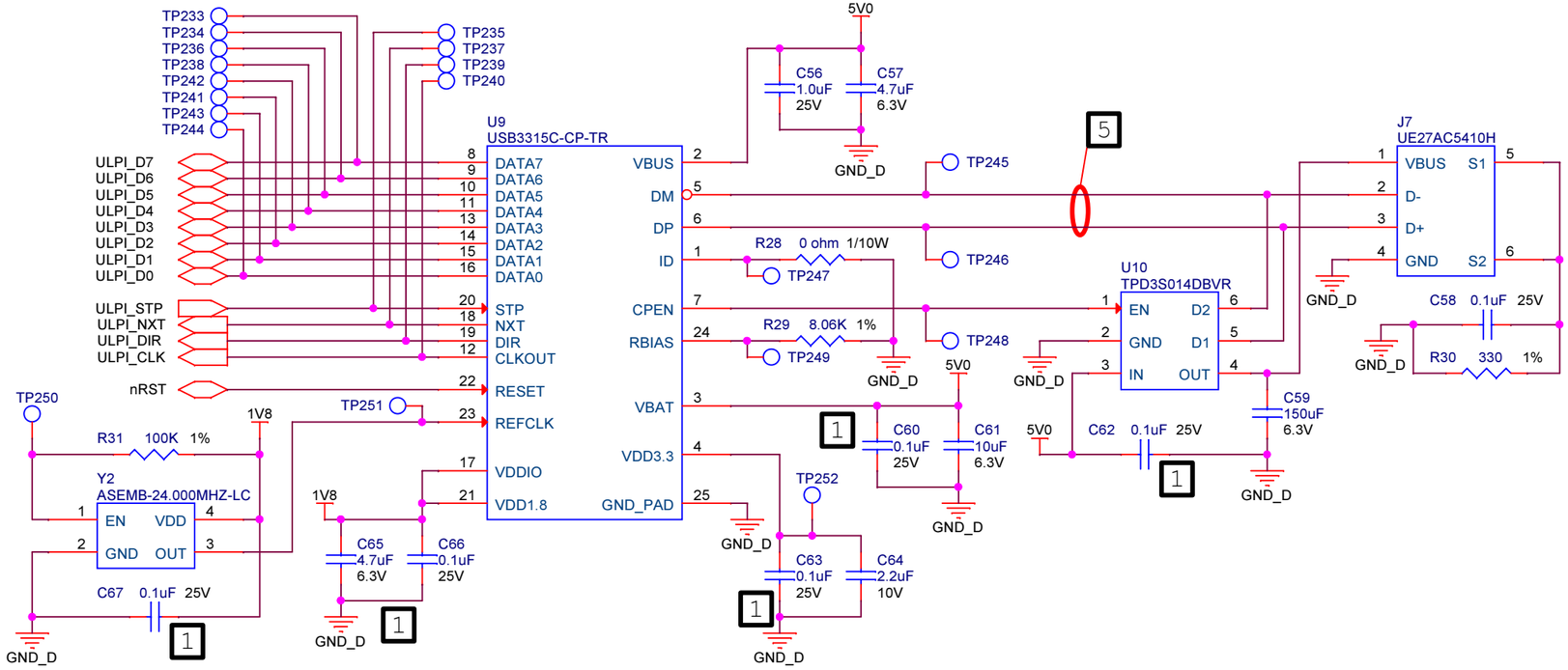
	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
CHECKED BY	J. Weatherbee	10/05/2015
APPROVED BY	R. Cousins	10/05/2015

TITLE: piSmasher SBC
PATH: /
DESCRIPTION: HDMI Receiver
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 4 OF 7



NOTES:

1. Place decoupling capacitors as close to IC pins as possible.
5. Route indicated signals as 90 ohm differential pairs.

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	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
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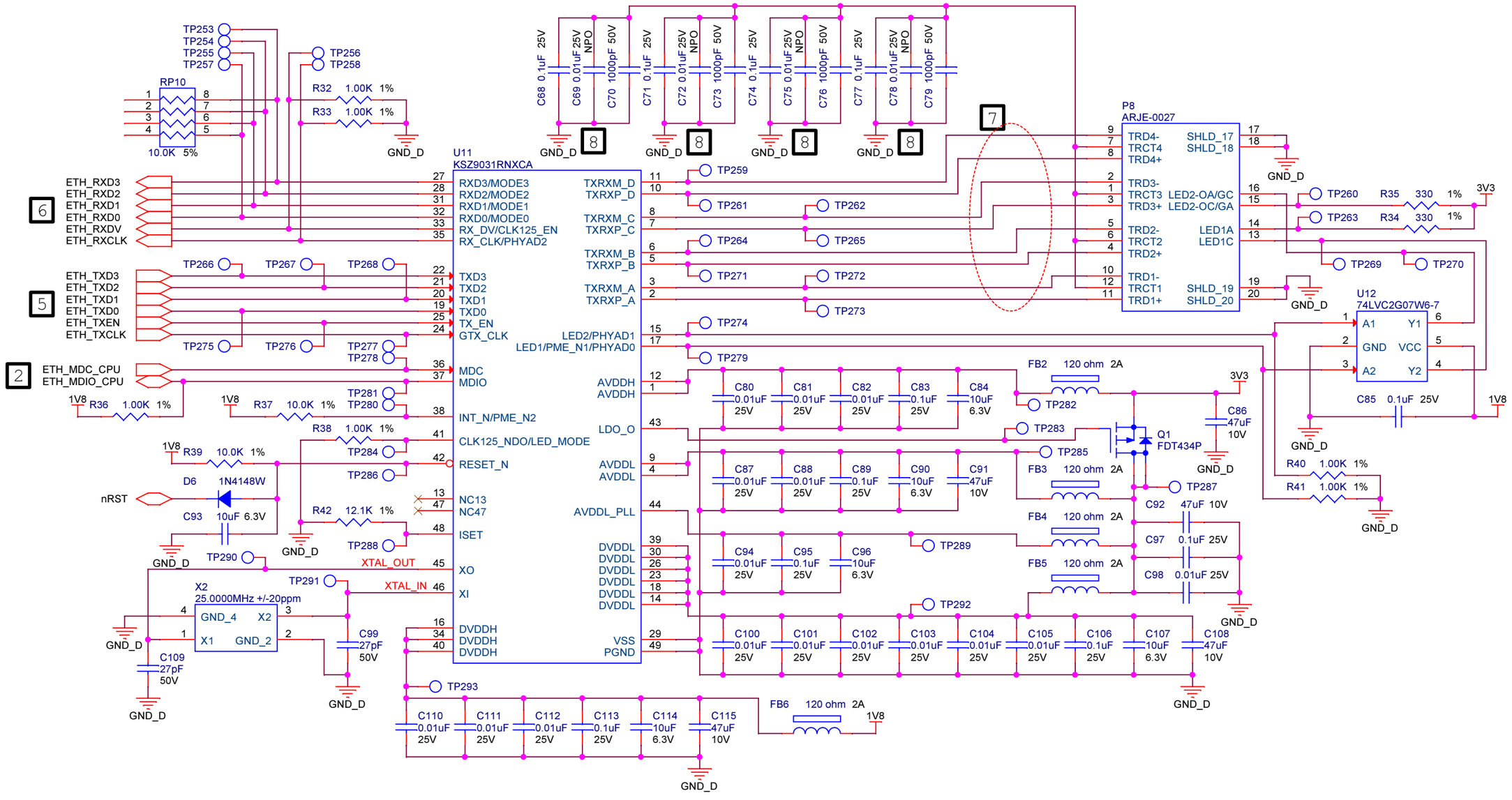
APPROVED BY	R. Cousins	10/05/2015

TITLE: piSmasher SBC
PATH: /
DESCRIPTION: ULPI USB PHY
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 5 OF 7



NOTES:

1. Place decoupling capacitors as close to IC pins as possible.
2. High speed signal traces should be length matched to within 50mils for each set of grouped signals.
5. U11 RGMII inputs ETH_TXxx: match trace lengths from connector P3 to U11 such that data bit to data bit skew is between -500ps and +500ps, and data bits to clock skew is between 1.0ns and 2.6ns.
6. U11 RGMII outputs ETH_RXxx: match trace lengths from connector P3 to U11 such that data bit to data bit skew and data bits to clock skew is between -500ps and +500ps.
7. Route signal pairs with 50ohm single, 100ohm differential impedance.
8. Place each set of three decoupling capacitors as close to Ethernet connector pins as possible.

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	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
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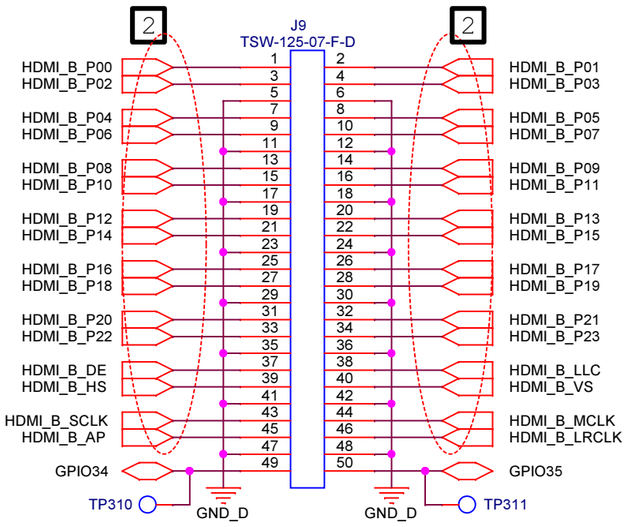
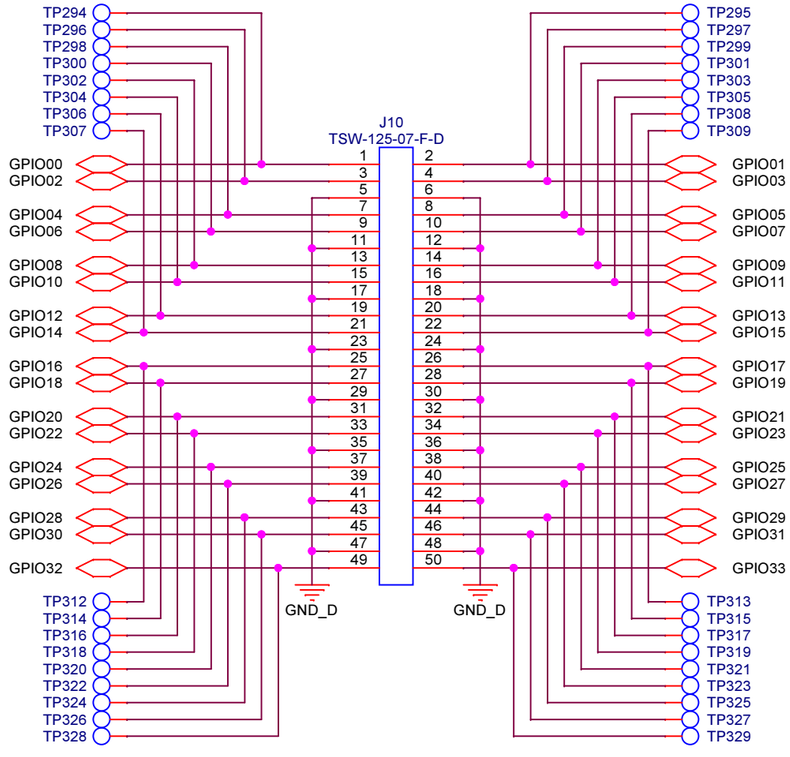
APPROVED BY	R. Cousins	10/05/2015

TITLE: piSmasher SBC
PATH: /
DESCRIPTION: Ethernet Transceiver
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 6 OF 7





NOTES:

2 High speed signal traces should be length matched to within 50mils for each set of grouped signals.

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	NAME	DATE
DRAWN BY	B. Hammond	10/05/2015
CHECKED BY	J. Weatherbee	10/05/2015
APPROVED BY	R. Cousins	10/05/2015

TITLE: piSmasher SBC
PATH: /
DESCRIPTION: User I/O Connectors
SIZE: B
DRAWING NO.: 15081802-01
REV: 1.2
SHEET 7 OF 7