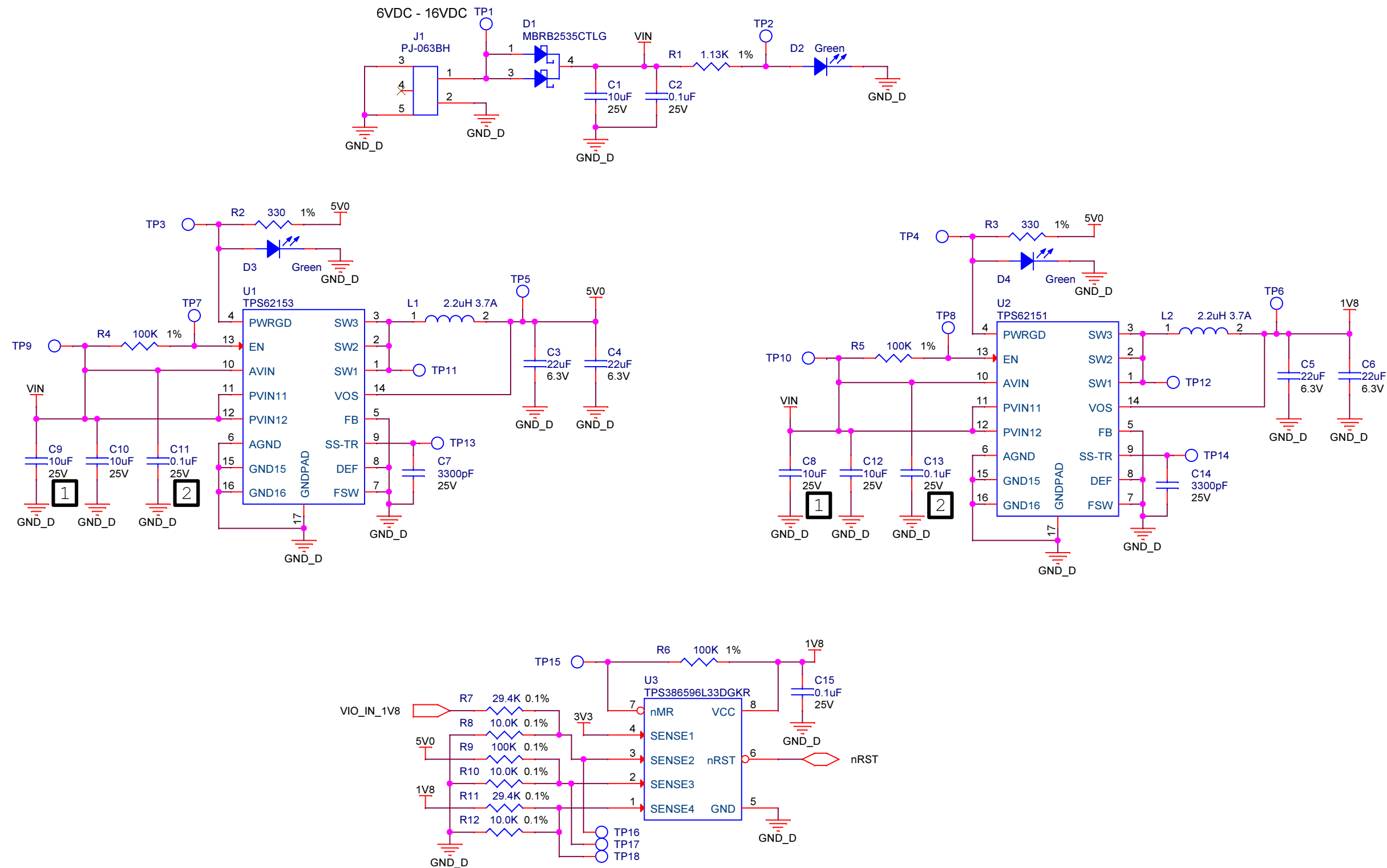


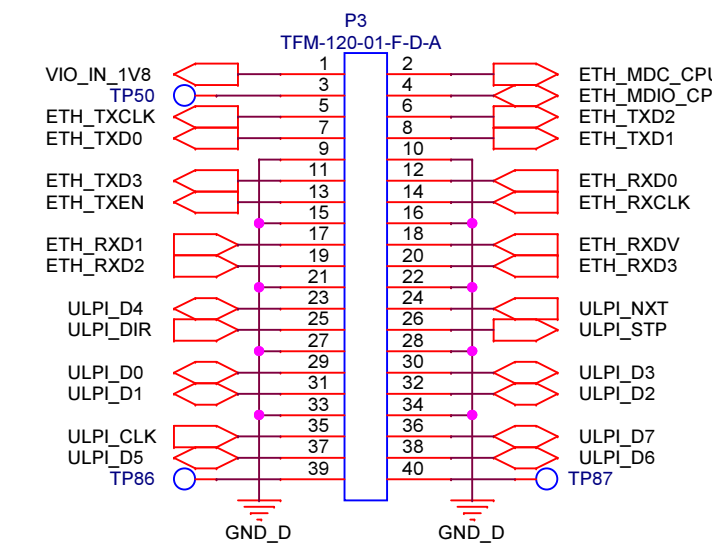
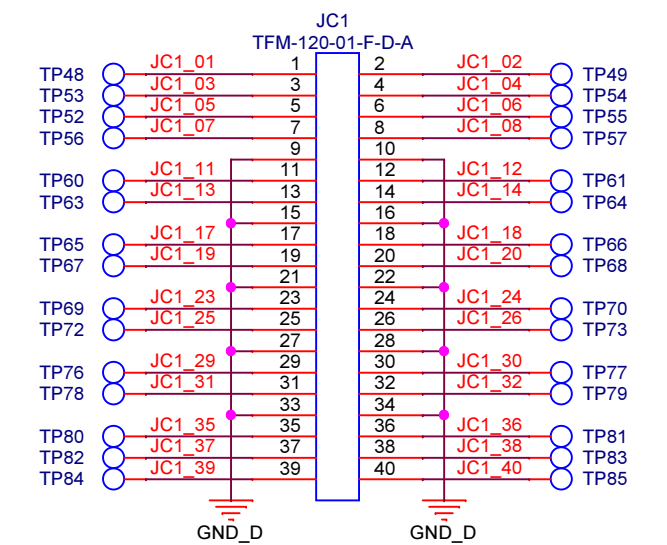
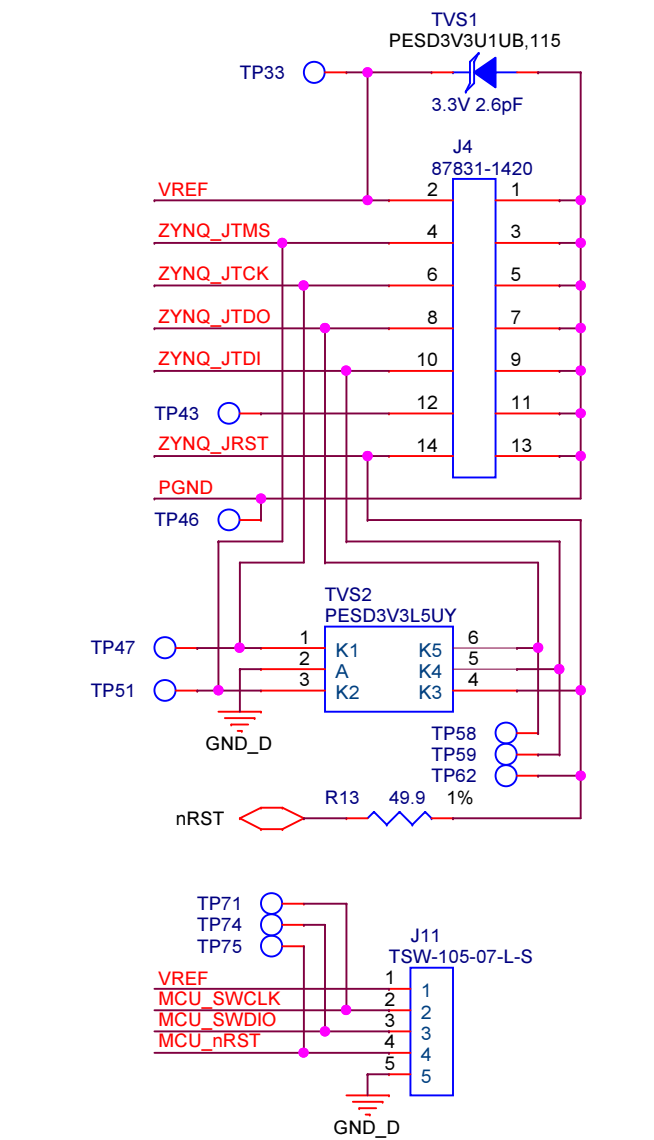
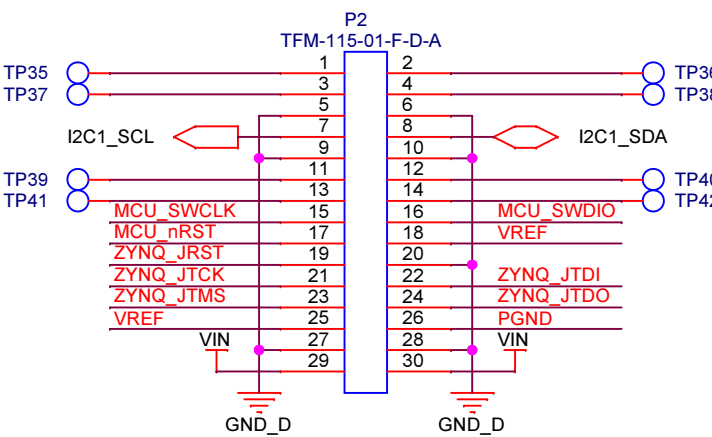
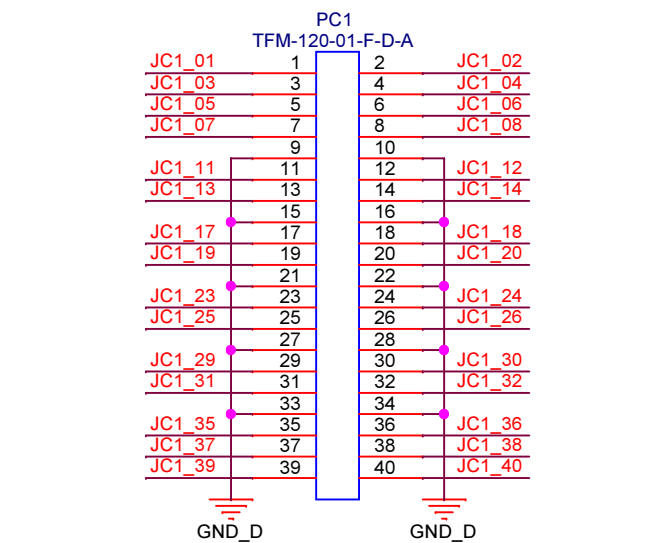
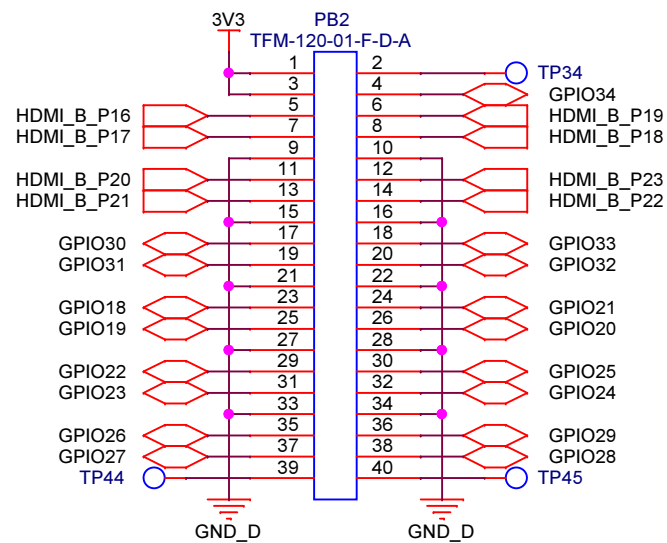
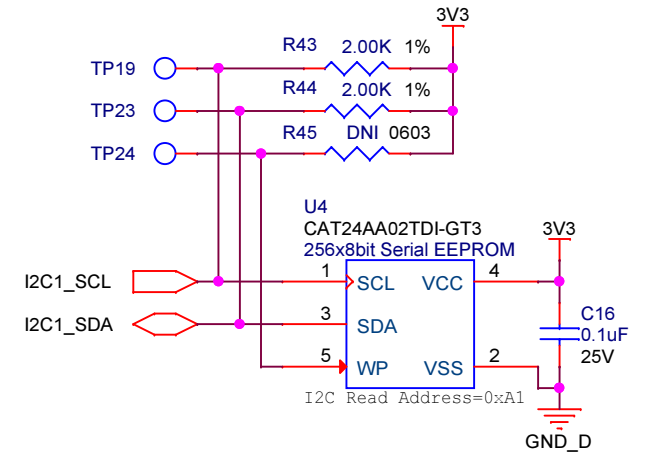
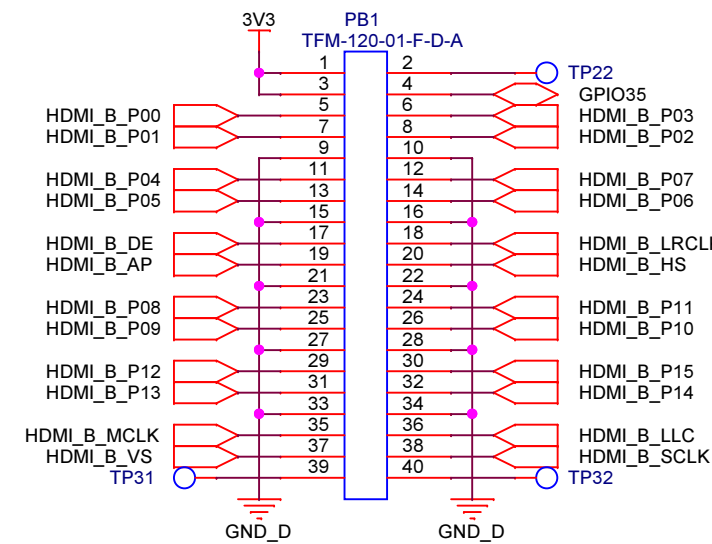
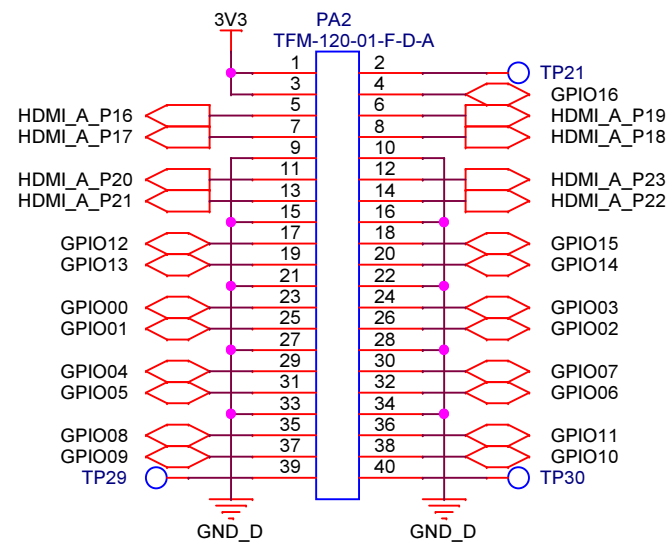
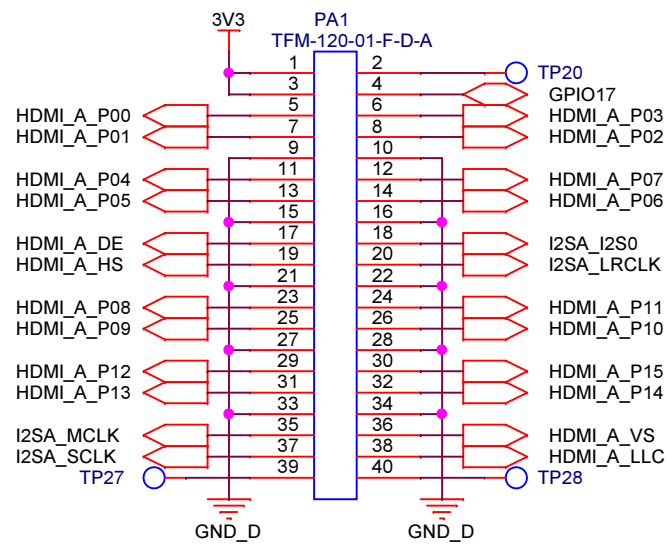
Rev.	Description	CO Number	Date	Approved By	Checked By
1.3	piSmasher alpha design	00003	10/05/2015	BEH	JJW



NOTES:

1 Place decoupling capacitors as close to IC pins as possible.

	krtkl inc. 350 Townsend Street Suite 301A San Francisco, CA 94107 +1 415 857 4857	TITLE piSmasher SBC
		PATH /
	Copyright 2015 krtkl inc. This schematic is made available under a Creative Commons Attribution-ShareAlike 4.0 International License. To view a copy of this license please visit <a href="http://creativecommons.org/licenses/by-sa/4.0/">http://creativecommons.org/licenses/by-sa/4.0/</a>	DESCRIPTION Power Supplies
		SIZE B
DRAWN BY B. Hammond	DATE 10/05/2015	DRAWING NO. 15081802-01
CHECKED BY J. Weatherbee	DATE 10/05/2015	REV 1.3
APPROVED BY R. Cousins	DATE 10/05/2015	SHEET 1 OF 7



# NOTES:

- Place decoupling capacitors as close to IC pins as possible.



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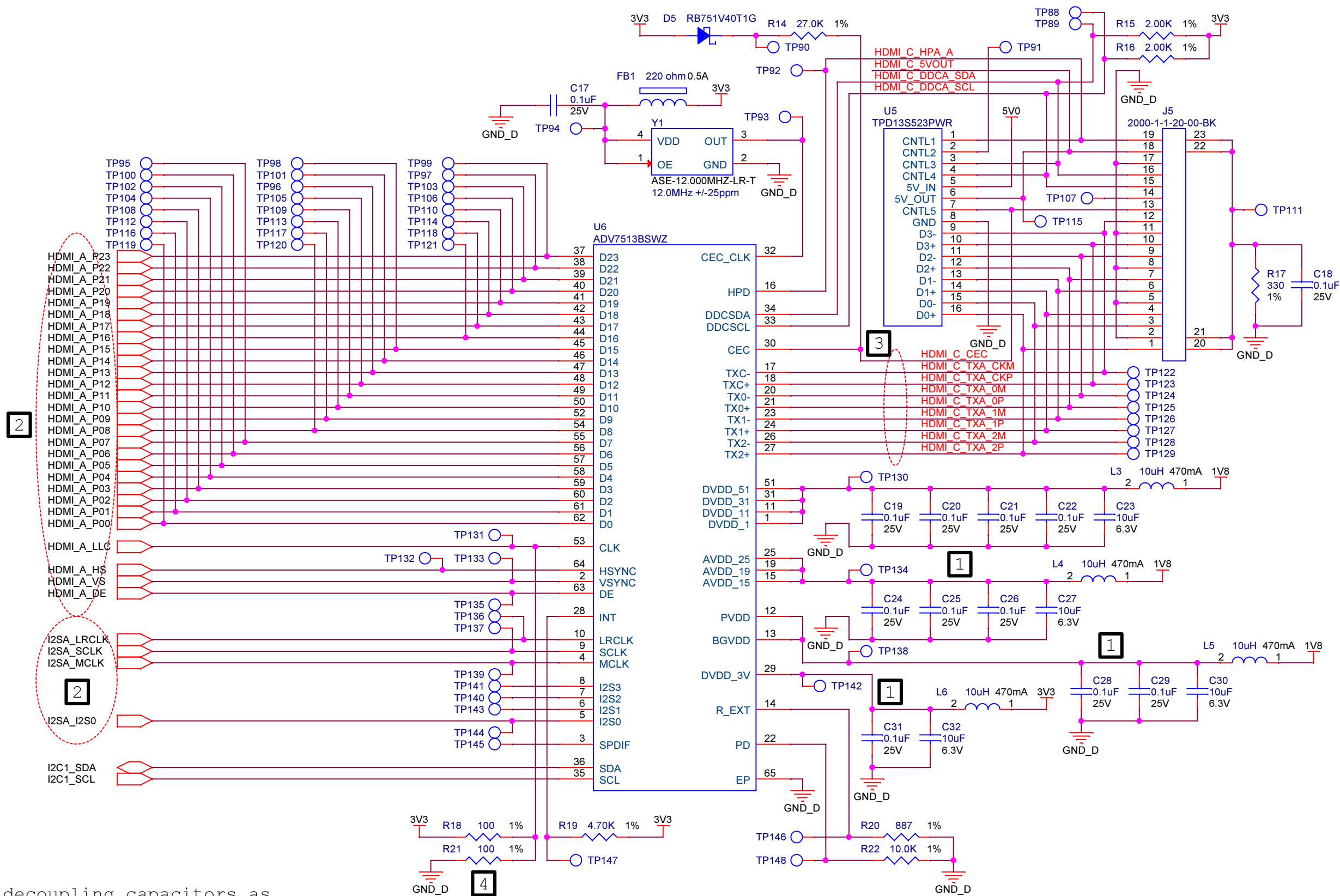


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NAME	DATE	SIZE	DRAWING NO.	REV
B. Hammond	10/05/2015	B	15081802-01	1.3
J. Weatherbee	10/05/2015			
R. Cousins	10/05/2015			

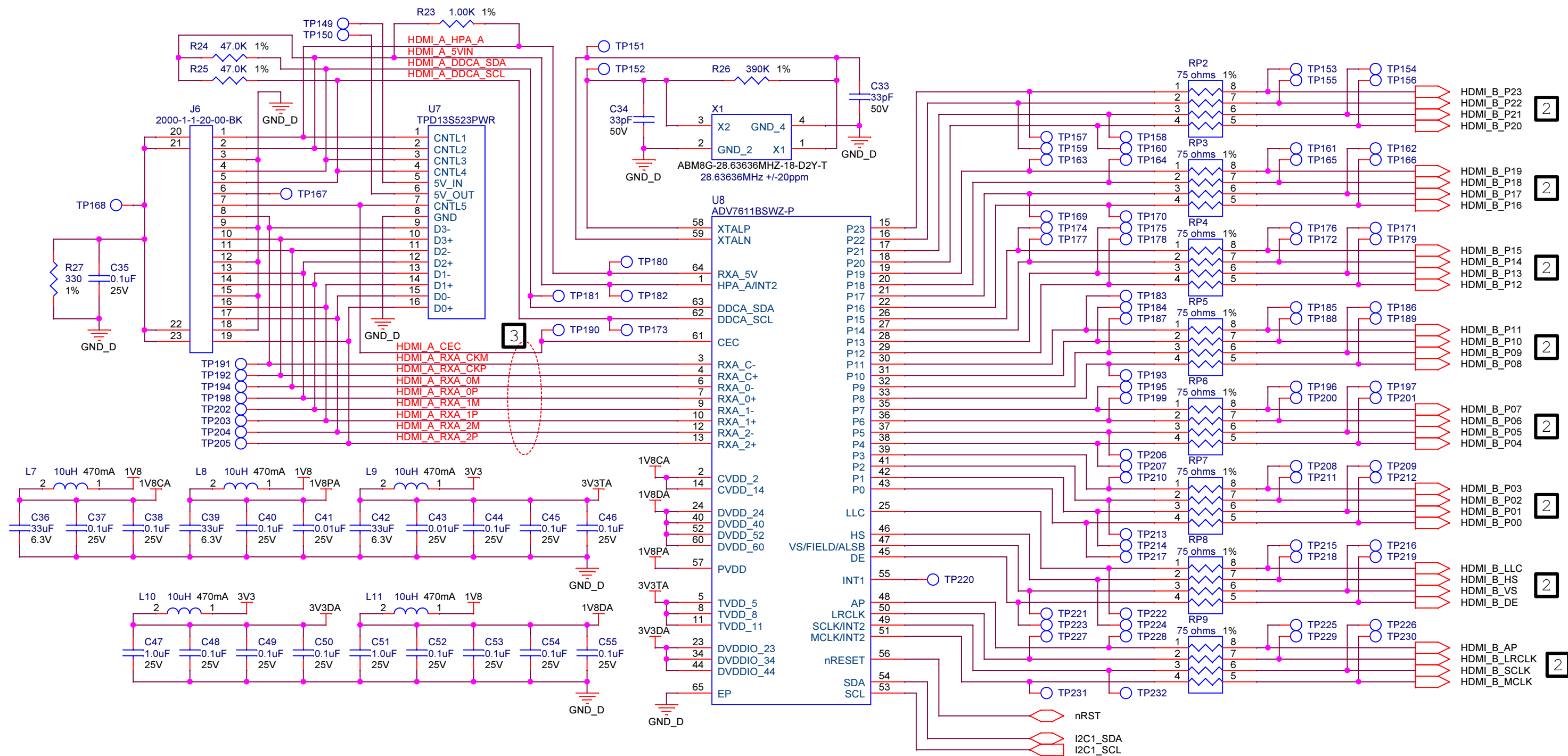
TITLE	PATH	DESCRIPTION	REV
piSmasher SBC	/	Main Interconnect	

SHEET	OF
2	7



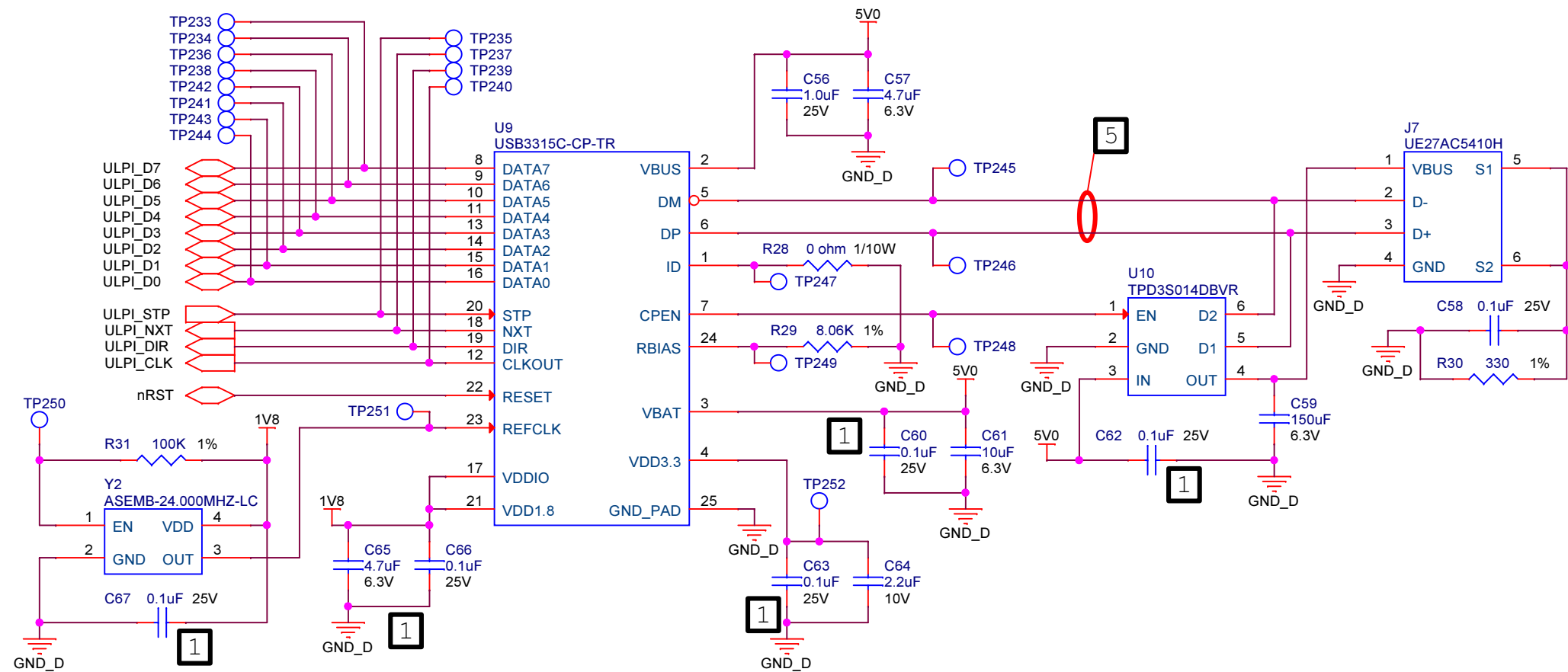
# NOTES:

- 1 Place decoupling capacitors as close to IC pins as possible.
- 2 High speed signal traces should be length matched to within 50mils for each set of grouped signals.
- 3 Route signals with 50ohm single, 100ohm differential impedance.
- 4 Place resistors as close to IC pin as possible with minimum trace stubs.



# NOTES:

- 1 Place decoupling capacitors as close to IC pins as possible.
- 2 High speed signal traces should be length matched to within 50mils for each set of grouped signals.
- 3 Route signals with 50ohm single, 100ohm differential impedance.

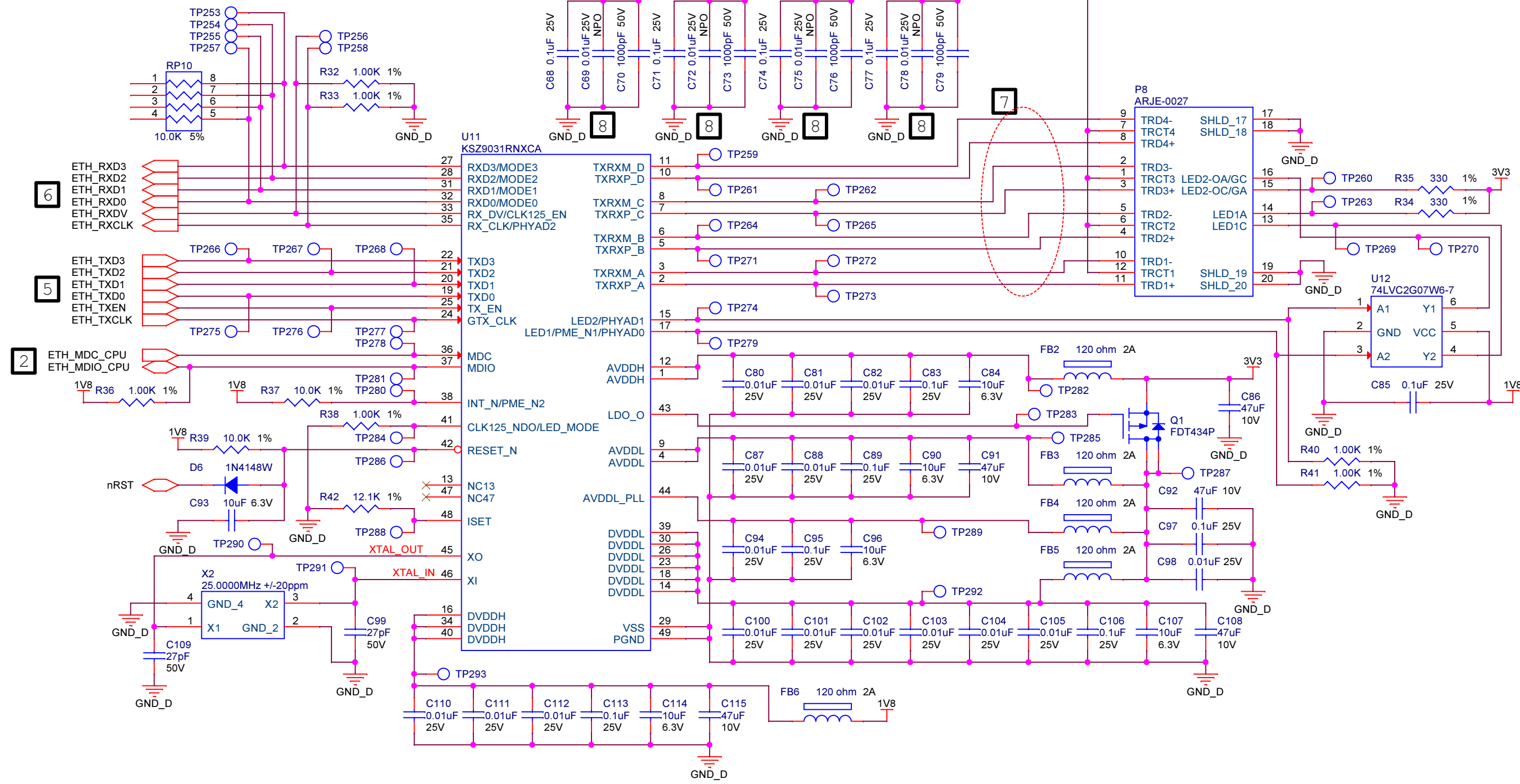


# NOTES:

- 1 Place decoupling capacitors as close to IC pins as possible.
- 5 Route indicated signals as 90 ohm differential pairs.

		<b>krtkl inc.</b> 350 Townsend Street Suite 301A San Francisco, CA 94107 +1 415 857 4857		<b>TITLE</b> piSmasher SBC	
 Copyright 2015 krtkl inc. This schematic is made available under a Creative Commons Attribution-ShareAlike 4.0 International License. To view a copy of this license please visit <a href="http://creativecommons.org/licenses/by-sa/4.0/">http://creativecommons.org/licenses/by-sa/4.0/</a>		<b>PATH</b> /		<b>DESCRIPTION</b> ULPI USB PHY	
		<b>NAME</b>		<b>DATE</b>	
		<b>DRAWN BY</b> B. Hammond	10/05/2015	<b>SIZE</b> B	<b>DRAWING NO.</b> 15081802-01
<b>CHECKED BY</b> J. Weatherbee	10/05/2015				
		<b>APPROVED BY</b> R. Cousins	10/05/2015	<b>SHEET 5 OF 7</b>	





NOTES:

- 1

Place decoupling capacitors as close to IC pins as possible.
- 2

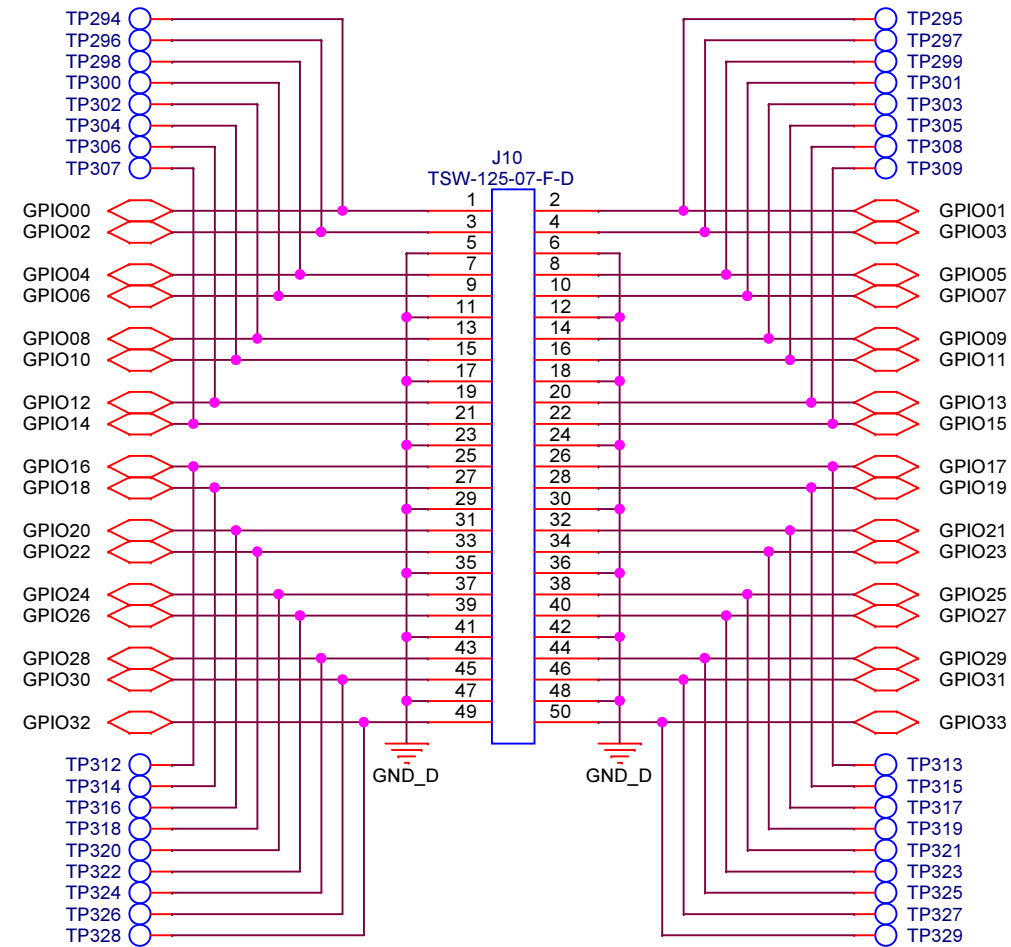
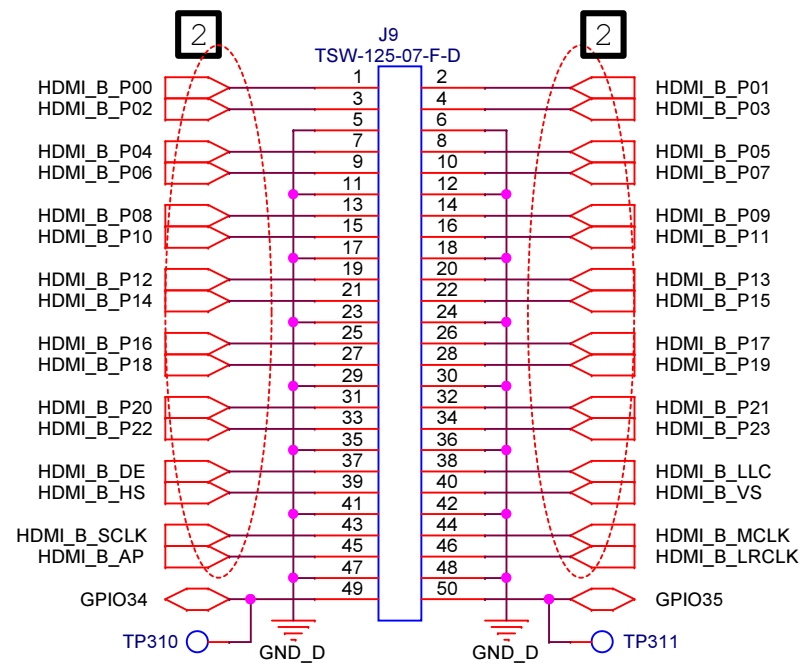
High speed signal traces should be length matched to within 50mils for each set of grouped signals.
- 5

U11 RGMII inputs ETH\_TXxx: match trace lengths from connector P3 to U11 such that data bit to data bit skew is between -500ps and +500ps, and data bits to clock skew is between 1.0ns and 2.6ns.
- 6

U11 RGMII outputs ETH\_RXxx: match trace lengths from connector P3 to U11 such that data bit to data bit skew and data bits to clock skew is between -500ps and +500ps.
- 7

Route signal pairs with 50ohm single, 100ohm differential impedance.
- 8

Place each set of three decoupling capacitors as close to Ethernet connector pins as possible.



NOTES:

2 High speed signal traces should be length matched to within 50mils for each set of grouped signals.