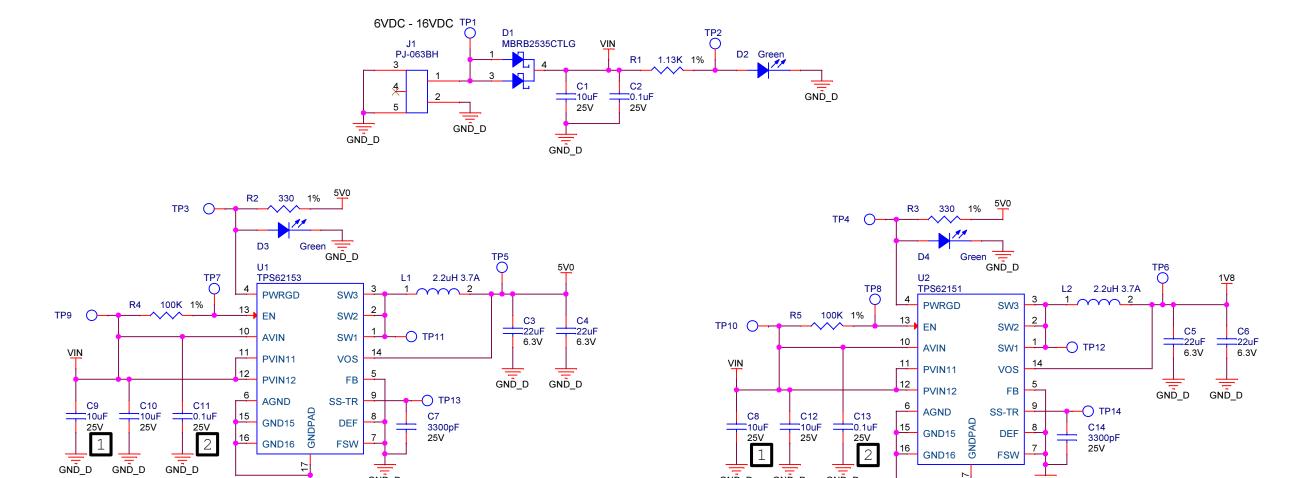
1.2 Refer to Change Order 8 CO8 10/05/2015 BEH

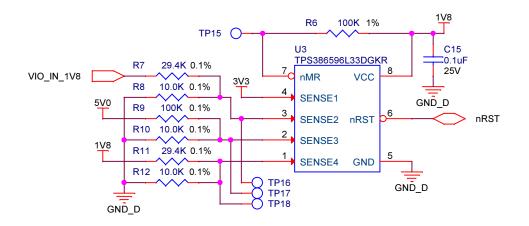


GND_D GND_D

GND_D

GND_D

GND_D

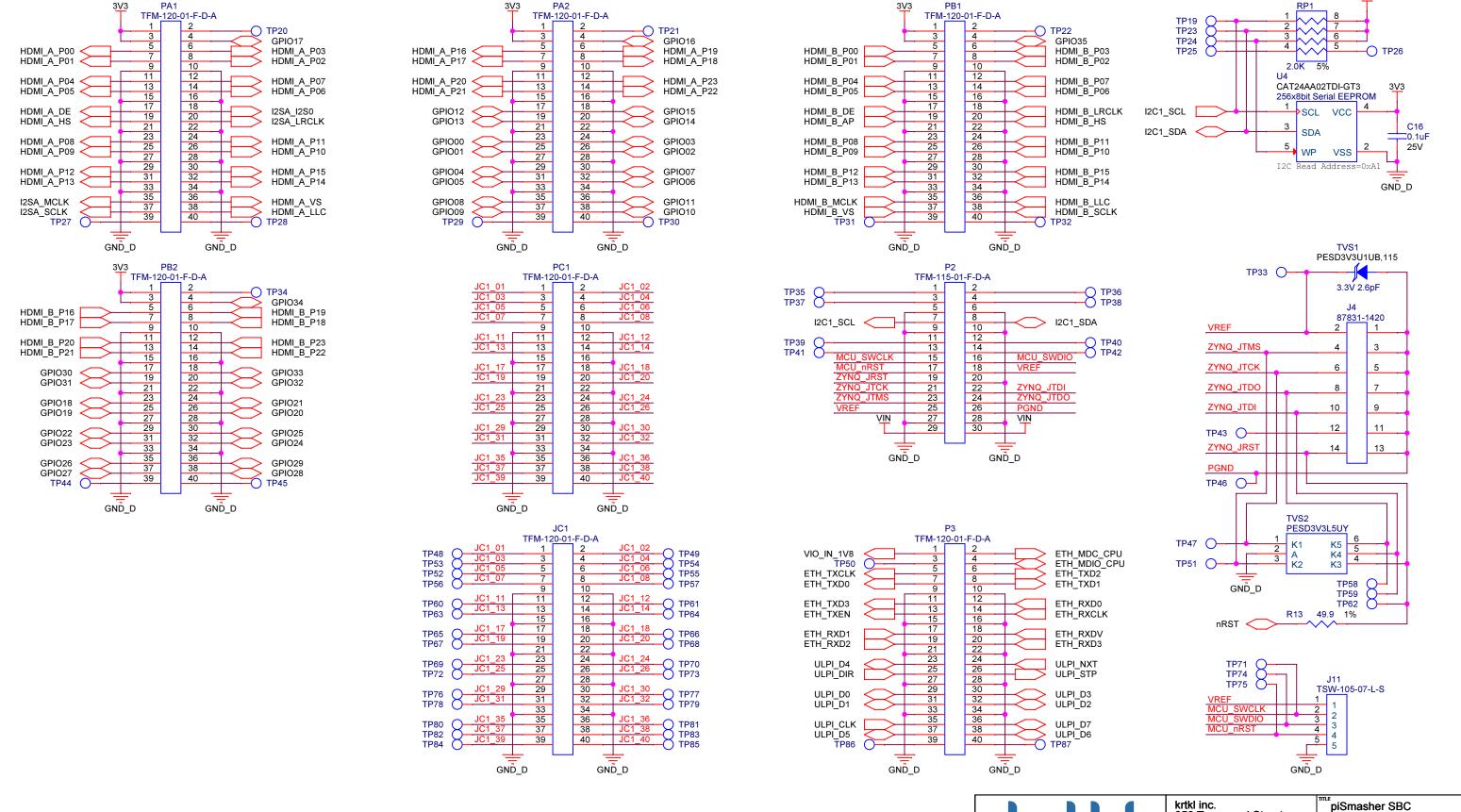


GND_D

GND_D

Place decoupling capacitors as close to IC pins as possible.

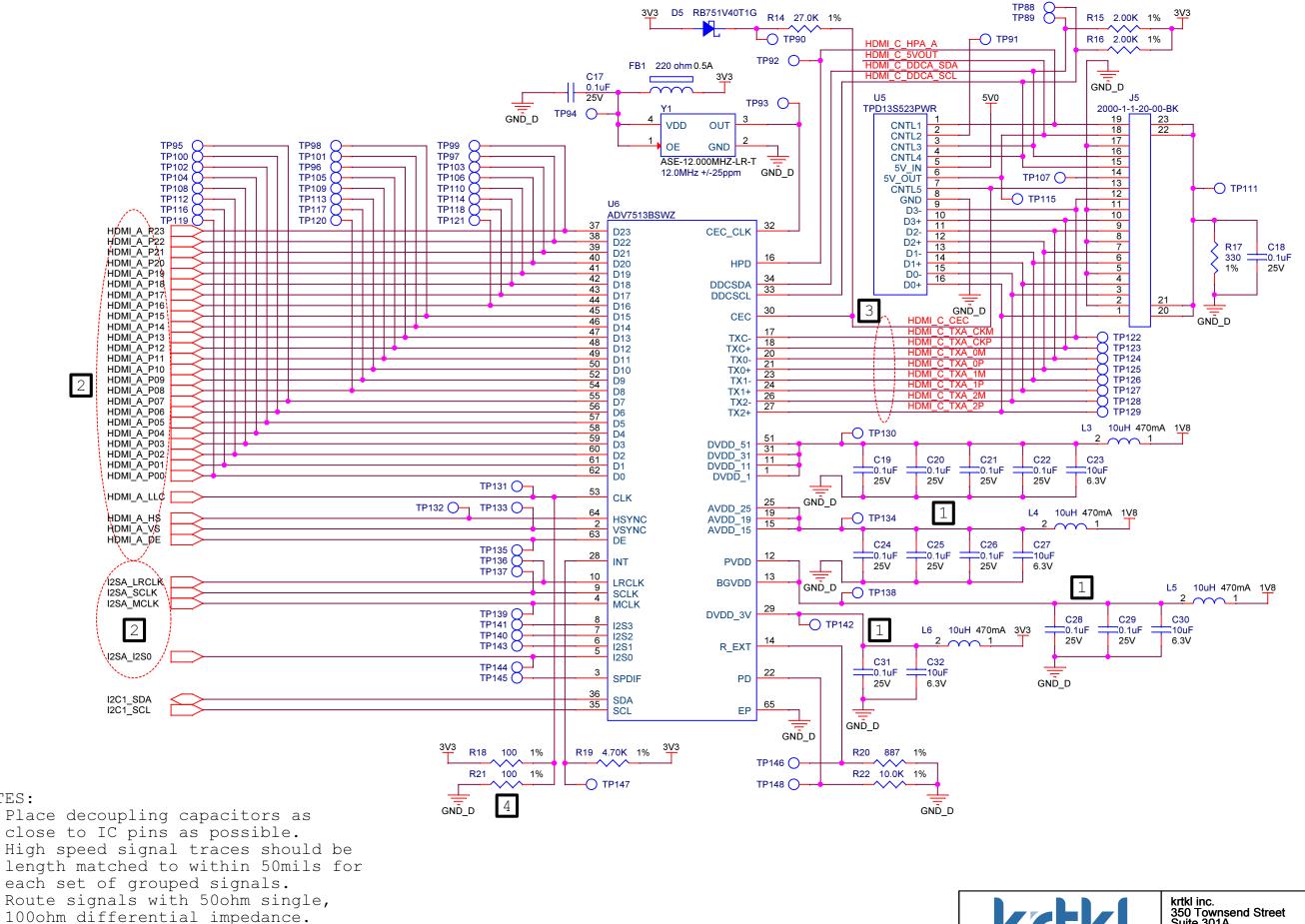
	krtkl			krtkl inc. 350 Townsend Street Suite 301A San Francisco, CA 94107 +1 415 857 4857			piSmasher SBC PATH / DESCRIPTION Power Supplies		
		Copyright 2015 krtkl inc. This schematic is		NAME	DATE	SIZE	DRAWING NO.	REV	
	made available under a Creative Commons Attribution-ShareAlike 4.0 International License.		DRAWN BY	B. Hammond	10/05/2015	Ь	15081802-01	1.2	
	BY SA Introduction-nativated with international License. To view a copy of this license please visit http://creativecommons.org/licenses.by-sa/4.0/	CHECKED BY	J. Weatherbee	10/05/2015	В				
		APPROVED BY	R. Cousins	10/05/2015		SHEET 1 OF 7			



Place decoupling capacitors as close to IC pins as possible.

krtkl inc. 350 Townsend Street Suite 301A San Francis +1 415 857 NAME

Suite 301A San Francisco, CA 94107 +1 415 857 4857			DESCRIPTION Main Interconnect				
		NAME	DATE	SIZE	DRAWING NO.	REV	
	DRAWN BY	B. Hammond	10/05/2015	В	15081802-01	1.2	
	CHECKED BY	J. Weatherbee	10/05/2015	D			
	APPROVED BY	R. Cousins	10/05/2015		SHEET 2 OF 7		

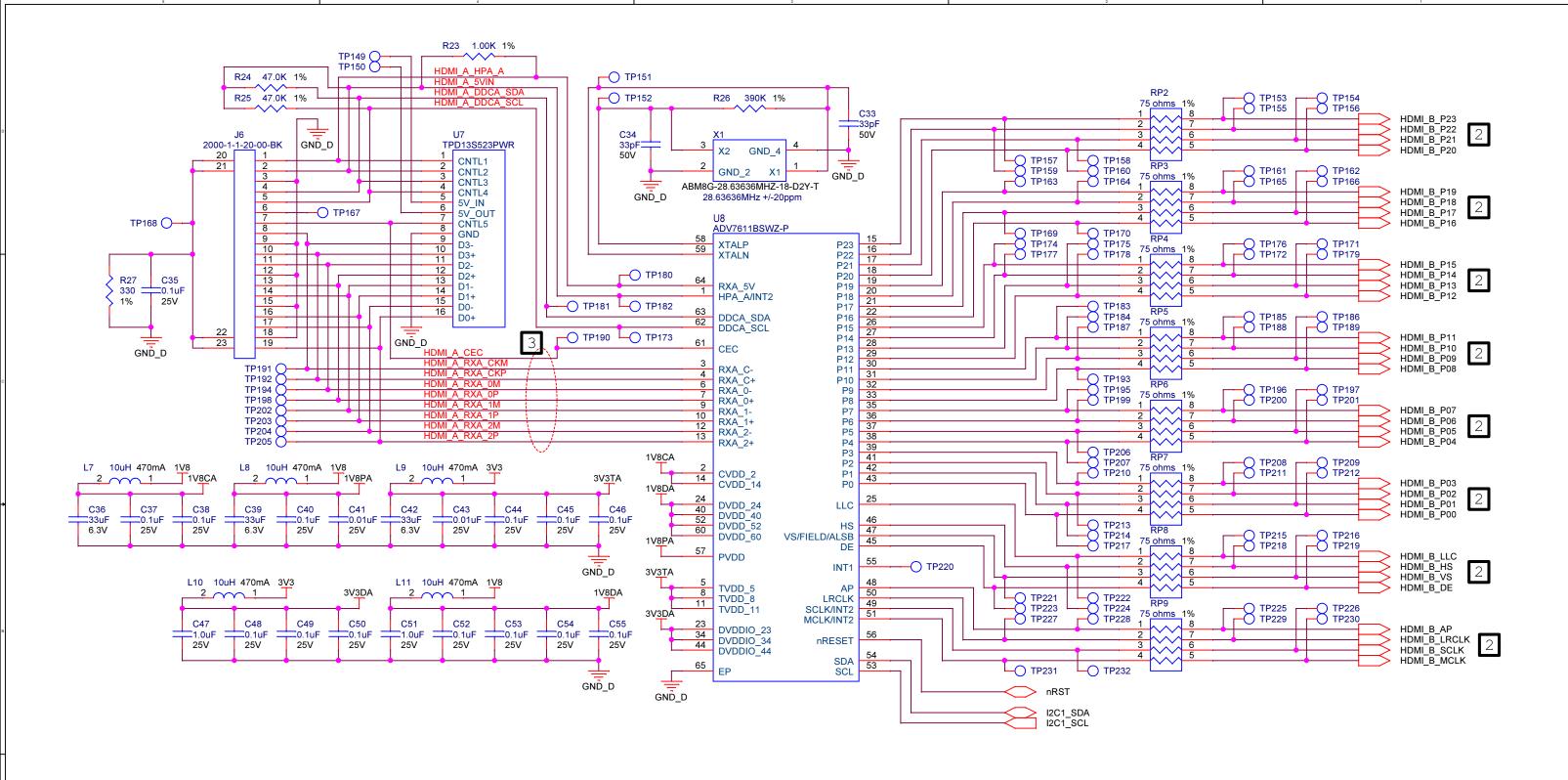


Place resistors as close to IC

pin as possible with minimum

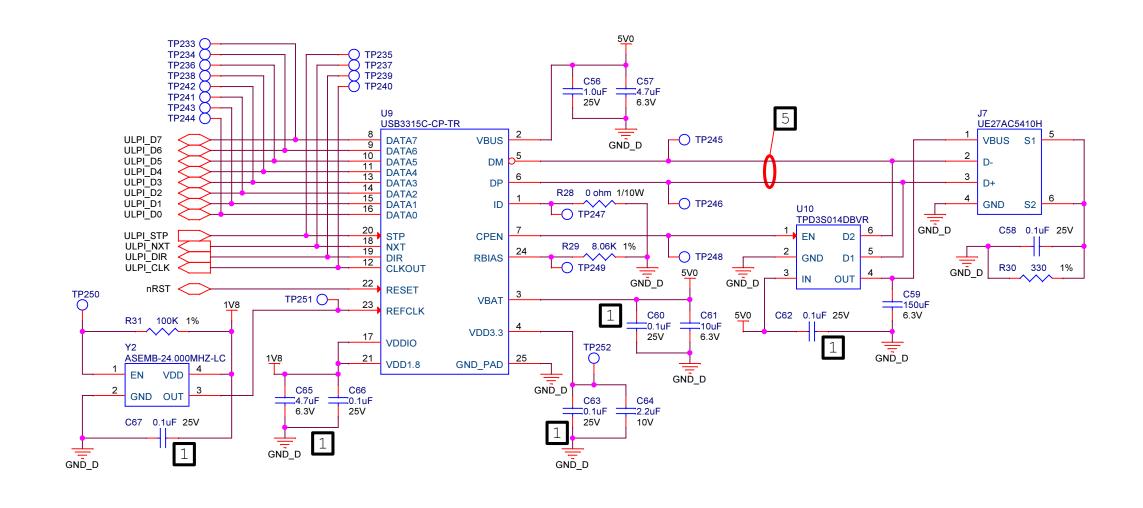
trace stubs.

krtkl inc. 350 Townsend Street Suite 301A piSmasher SBC San Francisco, CA 94107 scription HDMI Transmitter +1 415 857 4857 1.2 15081802-01 DRAWN BY B. Hammond 10/05/2015 В CHECKED BY J. Weatherbee 10/05/2015 SHEET 3 OF 7



- Place decoupling capacitors as close to IC pins as possible.
- High speed signal traces should be length matched to within 50mils for
- each set of grouped signals.
 Route signals with 50ohm single,
 100ohm differential impedance.

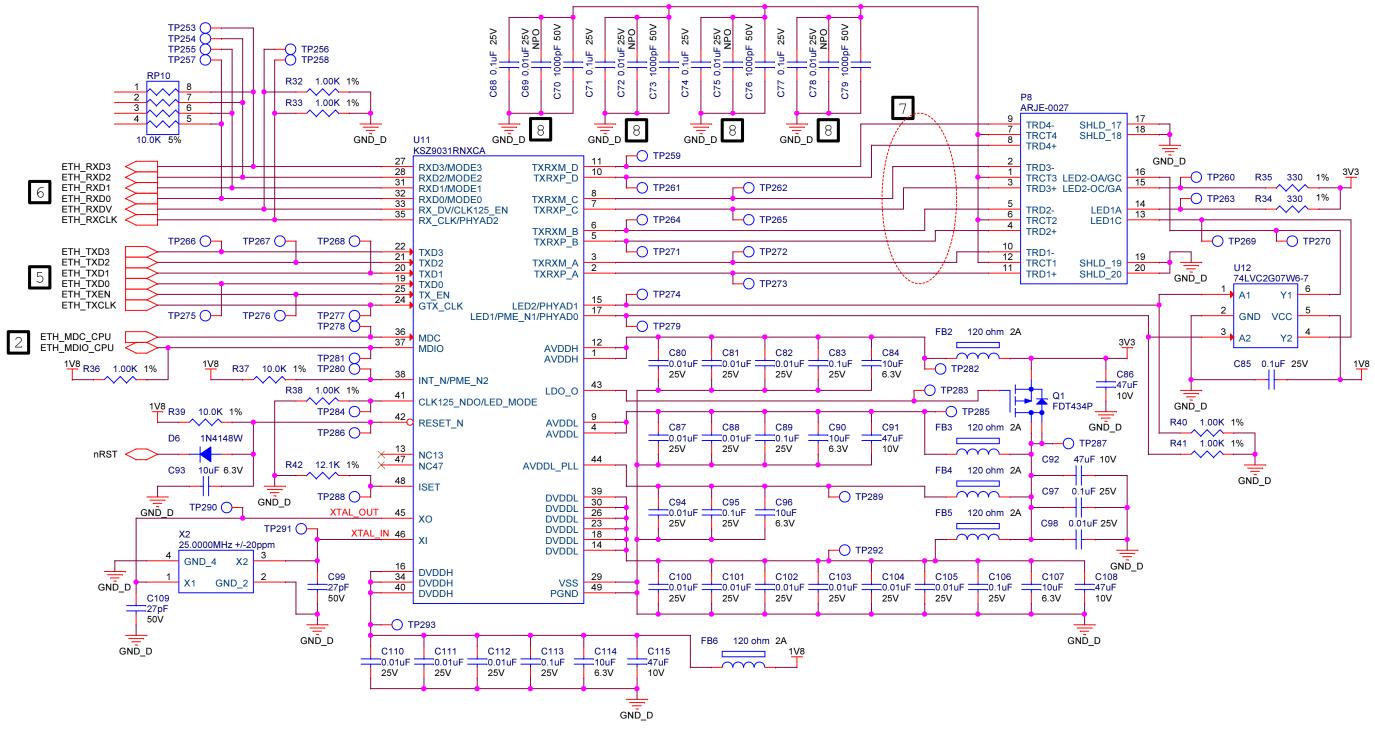
krtkl	Suite San F	ownsend Stree		piSmasher SBC PATH / DESCRIPTION HDMI Receiver			
		NAME	DATE	SIZE	DRAWING NO.	REV	
Copyright 2015 kritkl inc. This schematic is made available under a Creative Commons attribution. ShareAlike 4.0 laterational License.	DRAWN BY	B. Hammond	10/05/2015	ь	15081802-01	1.1.	
To view a copy of this license please visit	CHECKED BY	J. Weatherbee	10/05/2015	B	1000100201		
http://creativecommons.org/licenses/by-sa/4.0/	APPROVED BY	P. Cousins	10/05/2015		SHEET 4 OF 7		



NOTES:
1 Place Place decoupling capacitors as close to IC pins as possible.
Route indicated signals as 90 ohm differential pairs.

leable	35	krtkl inc. 350 Townsend Street Suite 301A San Francisco, CA 94107 +1 415 857 4857			path,		
KILK	Sa				DESCRIPTION ULPI USB PHY		
Copyright 2015 krtkl inc. This	a ashamatia ia		NAME	DATE	SIZE	DRAWING NO.	
made available under a Creat	ive Commons DRAWN	N BY	B. Hammond	10/05/2015	D	15081802	-01
Attribution-ShareAlike 4.0 International License. To view a copy of this license please visit http://creativecommons.org/licenses/by-sal/4.0/	please visit CHECKE	ED BY	J. Weatherbee	10/05/2015	В		
http://creativecommons.org/iii	APPRO	VED BY	R. Cousins	10/05/2015		SHEET 5 OF 7	

1.2



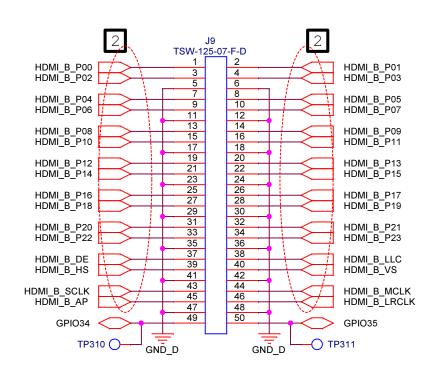
- Place decoupling capacitors as
- close to IC pins as possible. High speed signal traces should be length matched to within 50mils for each set of grouped signals.
- Ull RGMII inputs ETH TXxx: match trace lengths from connector P3 to Ull such that data bit to data bit skew is between -500ps and +500ps, and data bits to clock skew is between 1.0ns and 2.6ns.
- U11 RGMII outputs ETH RXxx: match trace lengths from connector P3 to Ull such that data bit to data bit skew and data bits to clock skew is between -500ps and +500ps.
- Route signal pairs with 50ohm single, 100ohm differential impedance.
- Place each set of three decoupling capacitors as close to Ethernet connector pins as possible.

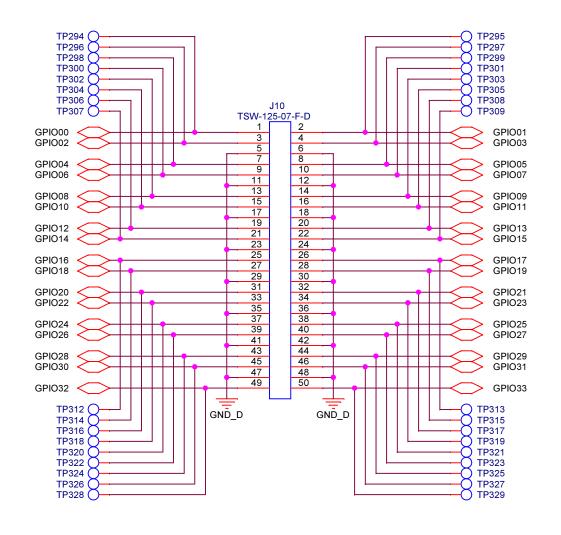


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Suite 3		/			
	5 857 4857	Ethernet Transceiver			
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ECKED BY	J. Weatherbee	10/05/2015		1	

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High speed signal traces should be length matched to within 50mils for each set of grouped signals.

