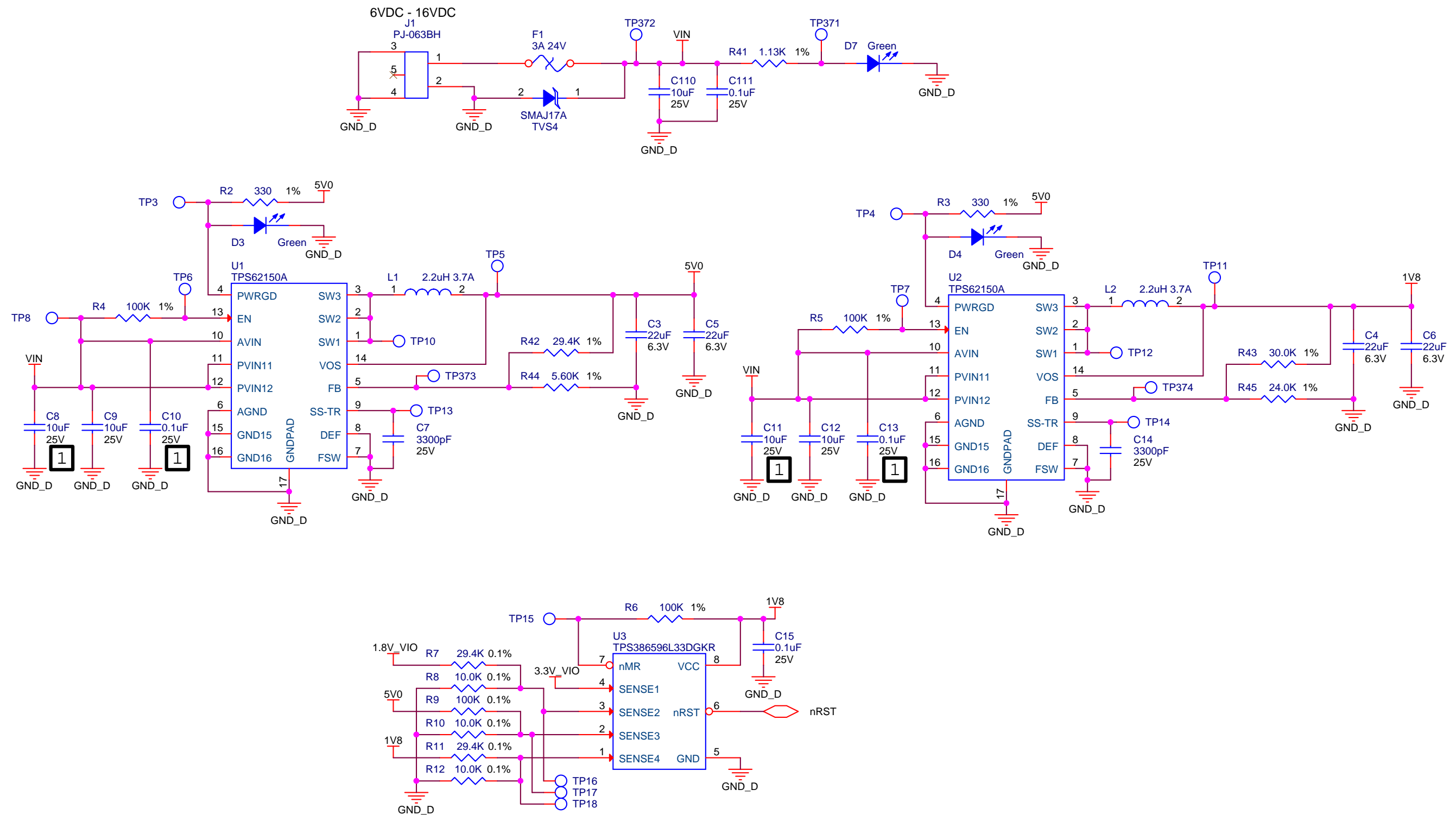


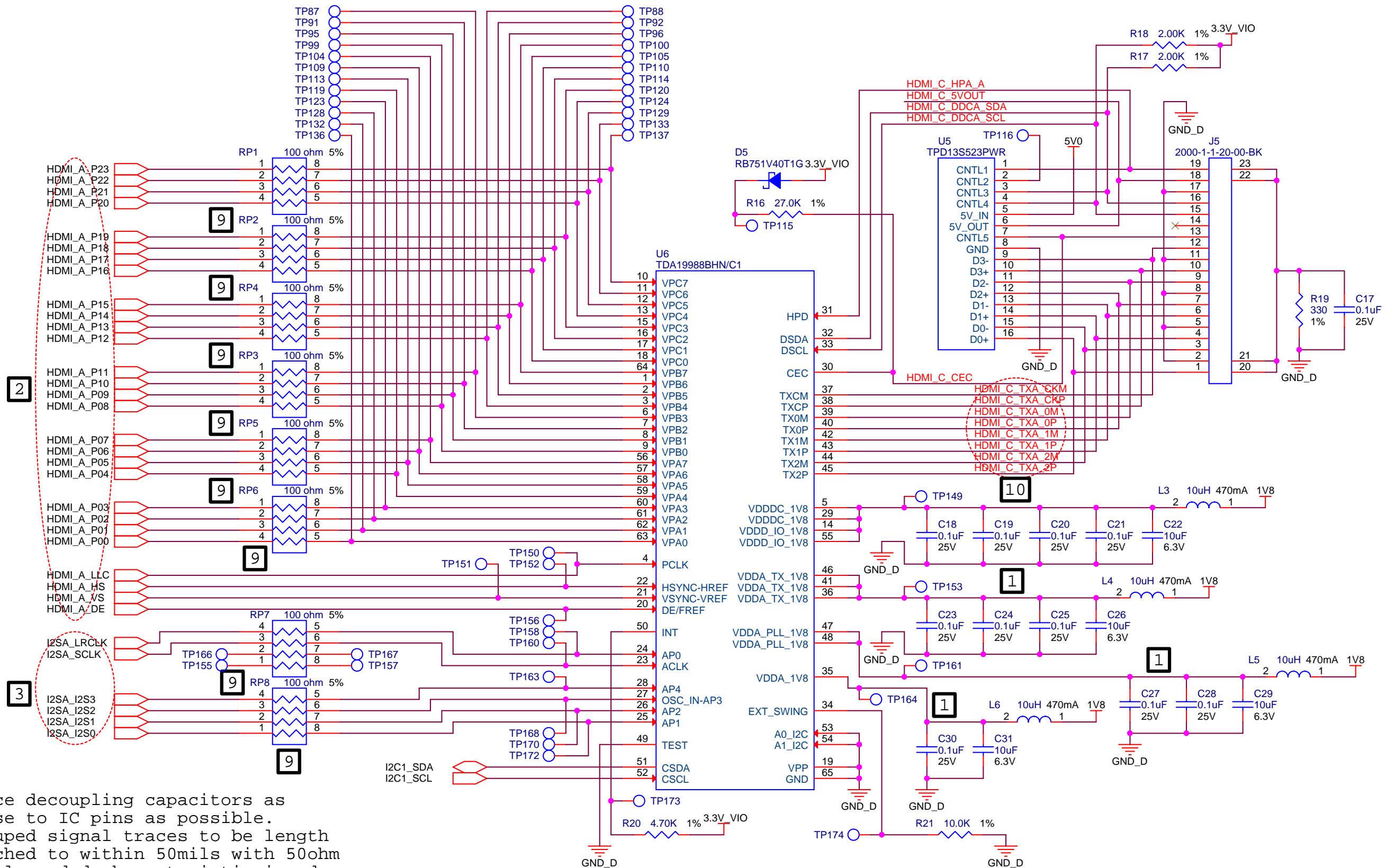
Rev.	Description	CO Number	Date	Approved By	Checked By
3.4	piSmasher alpha design	00003	03/30/2016	BEH	JJW



NOTES:

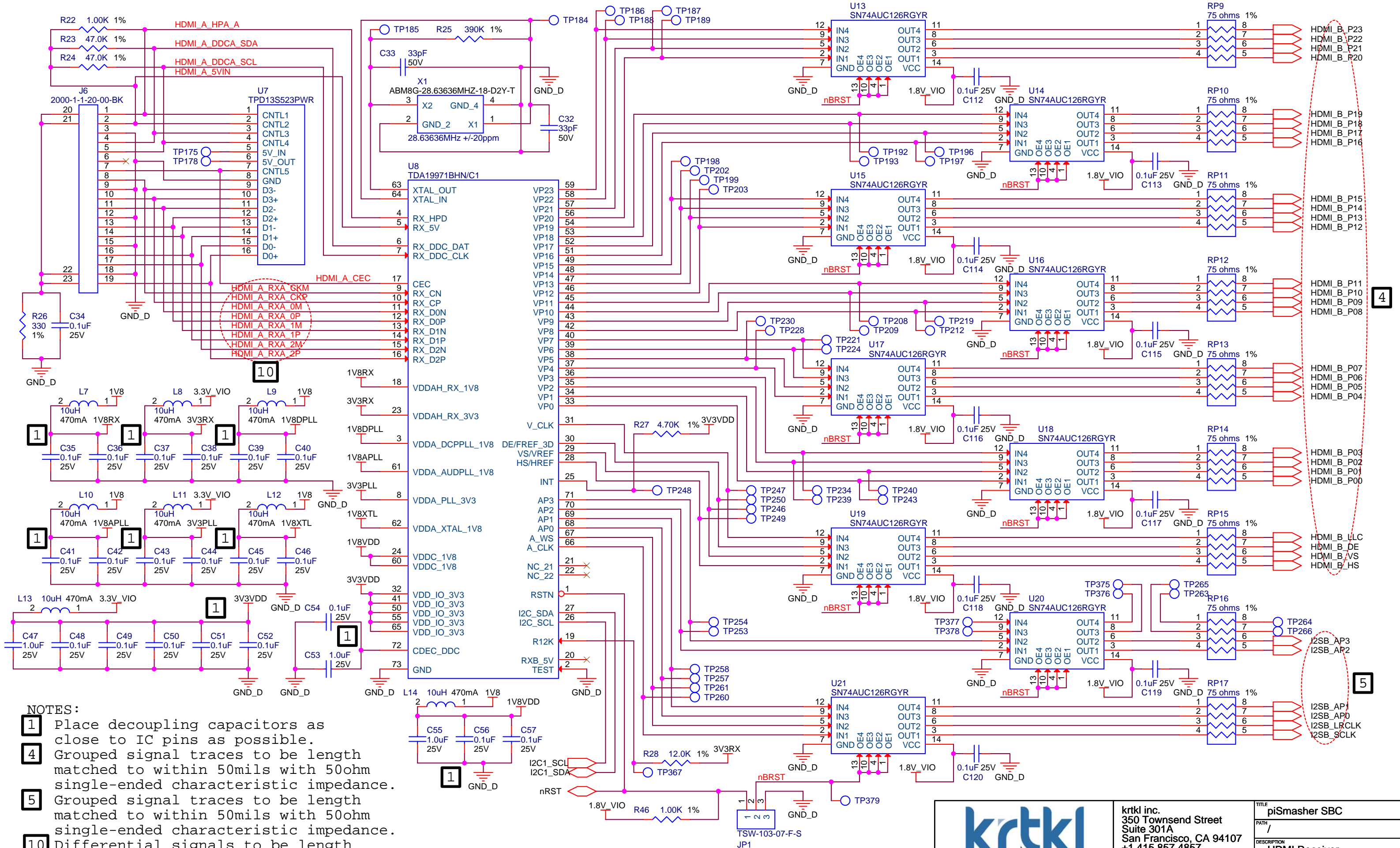
- 1 Place decoupling capacitors as close to IC pins as possible.

	krtkl inc. 350 Townsend Street Suite 301A San Francisco, CA 94107 +1 415 857 4857		TITLE piSmasher SBC				
			PATH /				
			DESCRIPTION Power Supplies				
 Copyright 2015 krtkl inc. This schematic is made available under a Creative Commons Attribution-ShareAlike 4.0 International License. To view a copy of this license please visit http://creativecommons.org/licenses/by-sa/4.0/		NAME		DATE			
	DRAWN BY	B. Hammond		10/05/2015	SIZE B	DRAWING NO. 15081802-01	REV 3.4
	CHECKED BY	J. Weatherbee		03/30/2016			
	APPROVED BY	R. Cousins		03/30/2016			
					SHEET 1 OF 7		

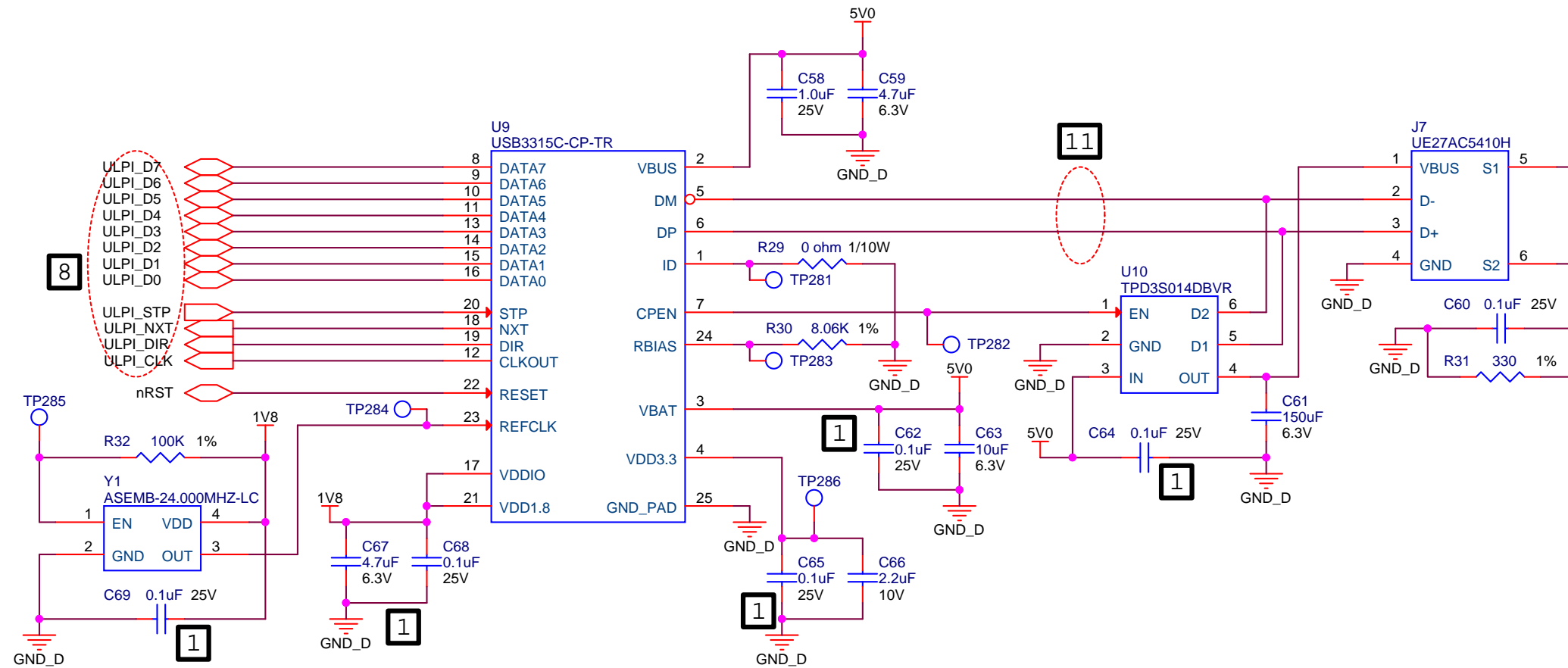


NOTES:

- 1 Place decoupling capacitors as close to IC pins as possible.
- 2 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
- 3 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
- 9 Place resistor arrays as close to IC as possible.
- 10 Differential signals to be length matched to within 10mils with 100ohm characteristic impedance.

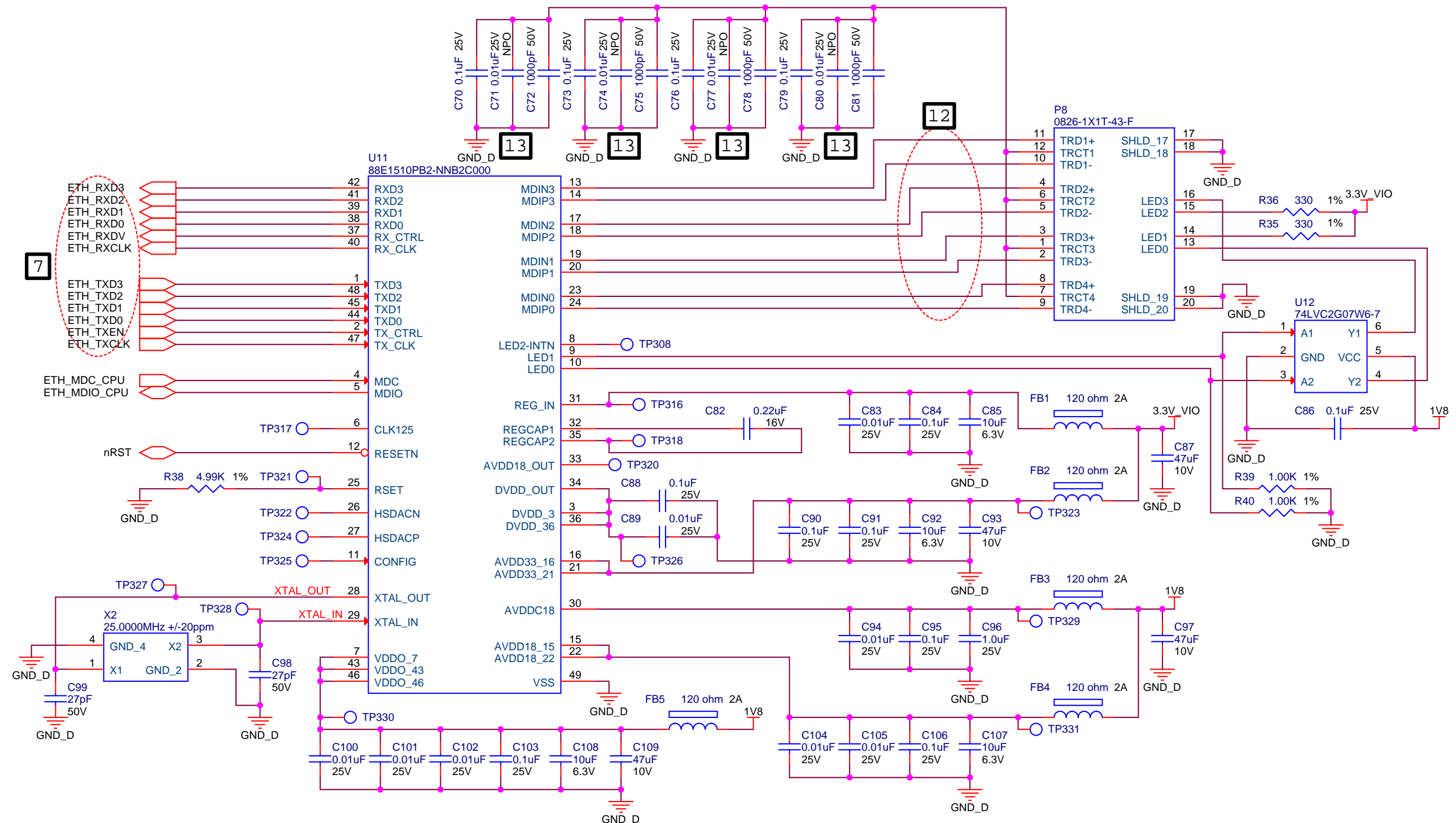


- NOTES:
- 1 Place decoupling capacitors as close to IC pins as possible.
 - 4 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
 - 5 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
 - 10 Differential signals to be length matched to within 10mils with 100ohm characteristic impedance.



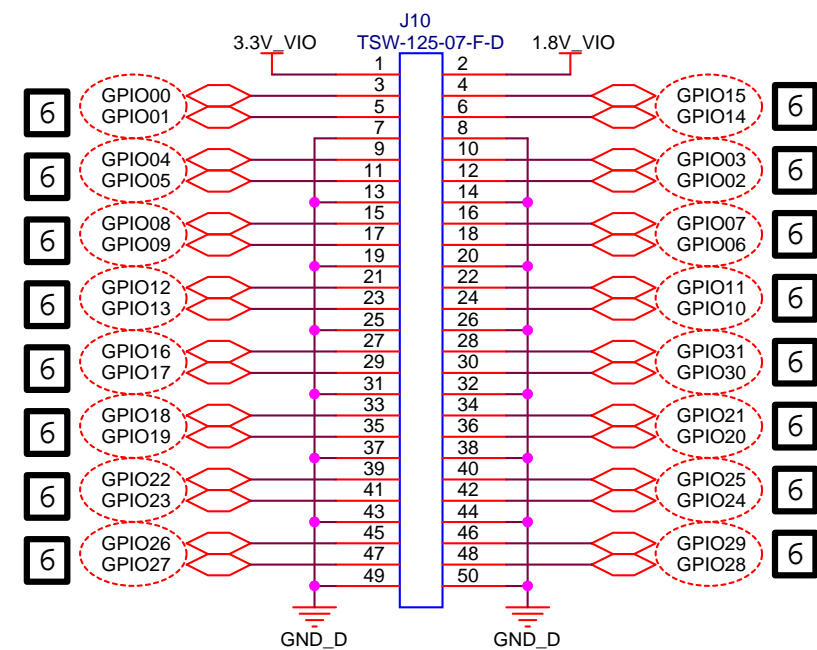
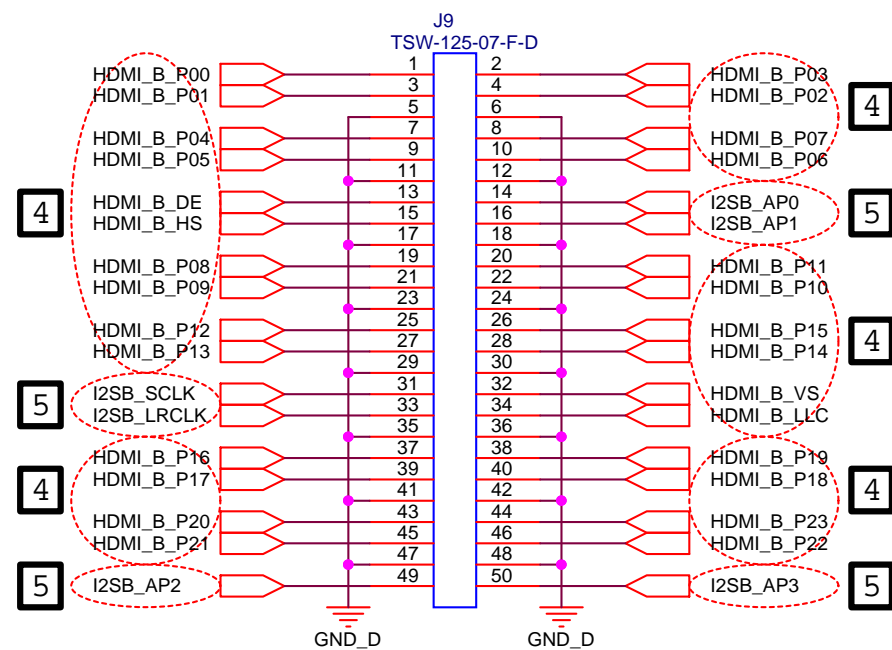
NOTES:

- 1 Place decoupling capacitors as close to IC pins as possible.
- 8 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
- 11 Grouped pair of differential signals to be length matched to within 10mils with 90ohm characteristic impedance.



- NOTES:
- 1** Place decoupling capacitors as close to IC pins as possible.
 - 7** Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
 - 12** Each pair of differential signals to be length matched to within 10mils with 100ohm characteristic impedance.
 - 13** Place each set of three decoupling capacitors as close to Ethernet connector pins as possible.

		krtkl inc. 350 Townsend Street Suite 301A San Francisco, CA 94107 +1 415 857 4857		TITLE piSmasher SBC	
		DESCRIPTION Ethernet Transceiver		PATH /	
DRAWN BY B. Hammond	NAME B. Hammond	DATE 10/05/2015	SIZE B	DRAWING NO. 15081802-01	REV 3.4
CHECKED BY J. Weatherbee	NAME J. Weatherbee	DATE 10/05/2015	SHEET 6	OF 7	
APPROVED BY R. Cousins	NAME R. Cousins	DATE 10/05/2015			



- NOTES:
- 4 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
 - 5 Grouped signal traces to be length matched to within 50mils with 50ohm single-ended characteristic impedance.
 - 6 Each grouped pair of differential signals to be length matched to within 10mils with 100ohm characteristic impedance.