

HomeWork 2

Monday, February 26, 2024 6:09 PM

2.3: 1: $\$+1, 32$
add $\$+0, \$s6, \$+1$
lw $\$+0, 0(\$+0)$

sub $\$+2, \$s3, \$s4$
sll $\$+2, \$+2, 2$
add $\$+2, \$+2, \$s7$
sw $\$+0, 0(\$+2)$

2.4: addi $\$+2, \$+0, 4$ # $\$+2$ register stores the $\$+0 + 4$
lw $\$+0, 0(\$+2)$ # loads $\$+2$ and stores in $\$+0$
add $\$+0, \$+0, \$s0$ # Adds values stored in $+0$ and $s0$ and stores in $+0$
sw $\$+0, 0(\$+1)$ # stores $+0$ in the memory address stored in $+0$

2.23.1: This would be an Itype format because it deals with Immediate values and value changes.

2.23.2:

Main:

l: $\$s0, 29$

loop_start:

addi $\$s0, \$s0, -1$

bnez $\$s0, loop_start$

2.24.1: The value of $\$s2$ will be $10 \cdot 2 = \boxed{20}$

2.24.2:

C Code:

Int A = 10; // $\$t1$

Int B = 0; // $\$s2$

while (A > 0) { // Loop
A = A - 1;

B = B + 2;

}

2.24.3: $\$s+1$ is the value n and the loop instructions is $5 \cdot n$

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Number of MIPS Instructions executed: $5 \cdot N$

2.31

```
addi $sp, $sp, -12
sw $ra, 1($sp)
sw $a2, 4($sp)
sw $a3, 8($sp)
jal func
add $a0, $r0, $0
lw $a2, 4($sp)
lw $a3, 8($sp)
add $a1, $a2, $a3
jal func
lw $ra, 1($sp)
addi $sp, $sp, 12
jr $ra
```