

# ADC-ASP Testing Instructions

1. Open "ModelSim - Intel FPGA Started Edition 10.5 b"
2. Click "File" and select "Change Directory ..."
3. Select the parent folder "Fosu562-942929392-ADC-ASP"
4. Click "Compile", "Compile ..." and compile each of these files in order:
  1. "TdmaMin/TdmaMinTypes.vhd"
  2. "constants.vhd"
  3. "adcasp.vhd"
  4. "test\_adcasp.vhd"

If prompted to create a library "work" click "Yes"

5. Click "Simulate", "Start Simulation ..."
6. Expand "work" and select "testadc", leaving resolution as "default", then click "Ok"
7. Add any waves desired and simulate the wave for a period of 100 ms.