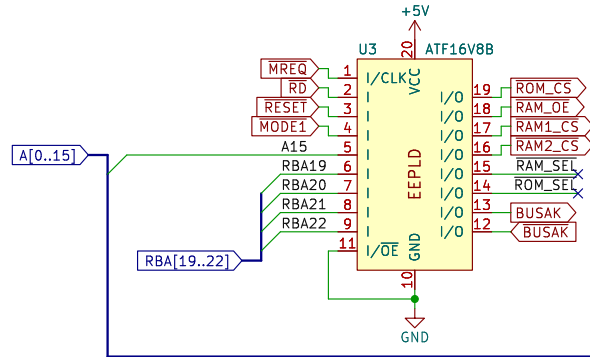
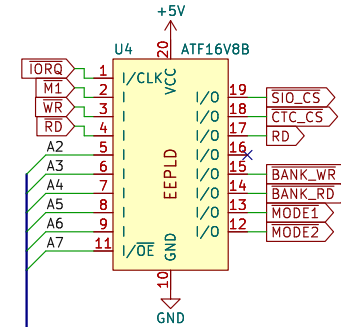


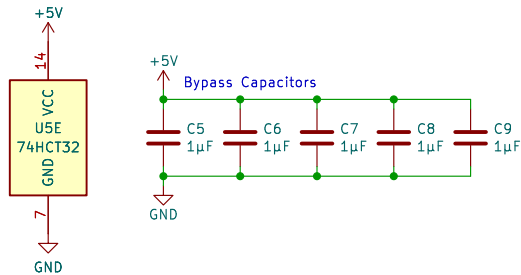
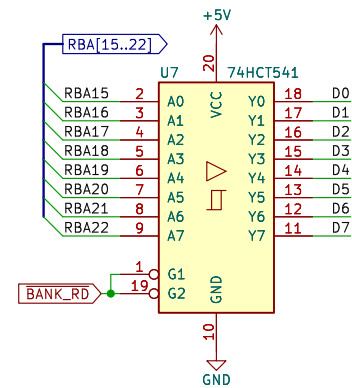
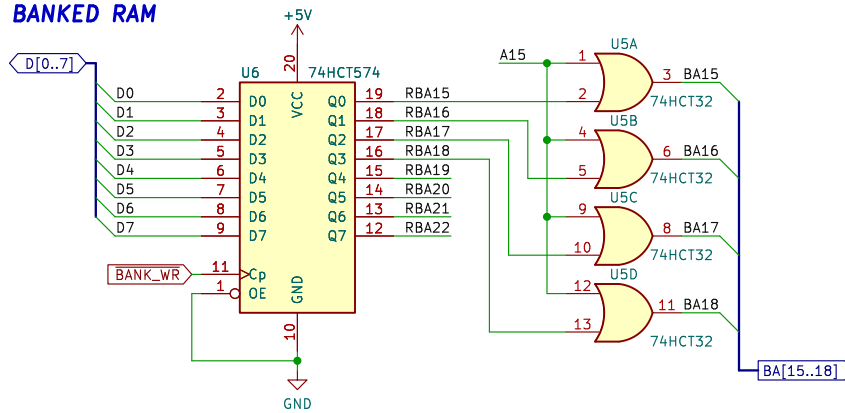
SHADOW AND MEMORY LOGIC



I/O LOGIC



BANKED RAM



Thank you to John Winans for his inspiration, as well as Grant Searle and Sergey Kiselev

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Frédéric Segard

Sheet: /Glue Logic/

File: GlueLogic.kicad_sch

Title: Glue Logic

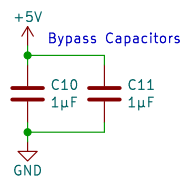
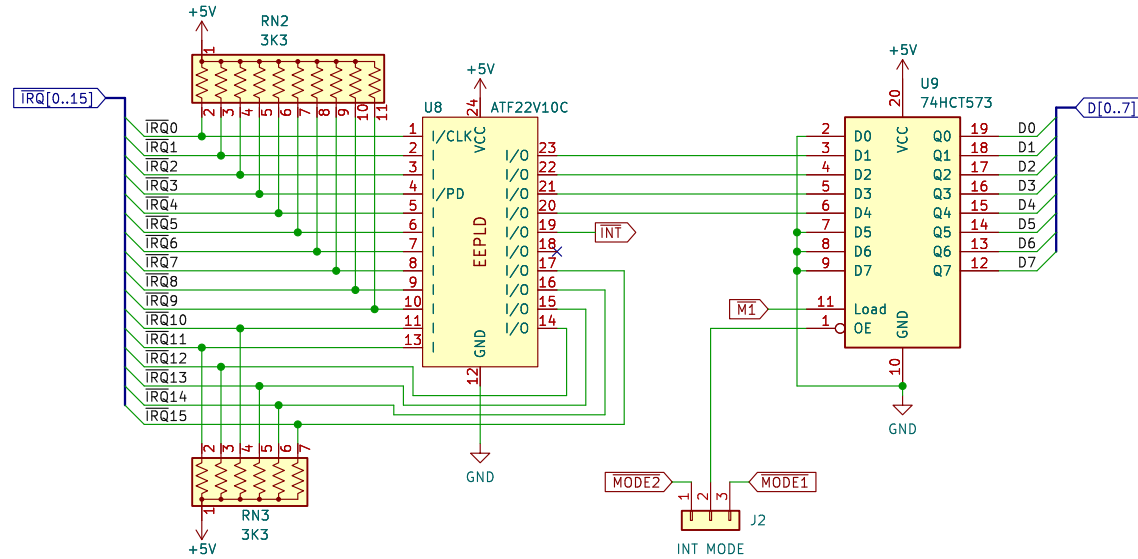
Size: USLetter Date: 2023-09-08

KiCad E.D.A. kicad 7.0.7

Rev: 2.12

Id: 2/7

PRIORITY INTERRUPT ENCODER



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Sheet: /Interrupt Encoder/

File: InterruptEncoder.kicad_sch

Title: Priority Interrupt Encoder

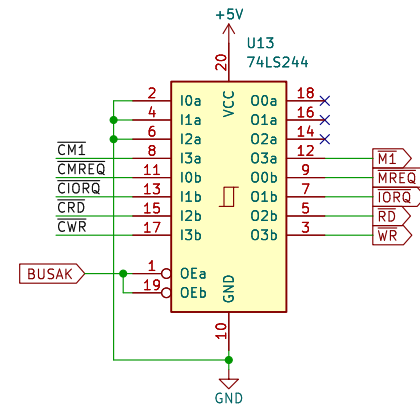
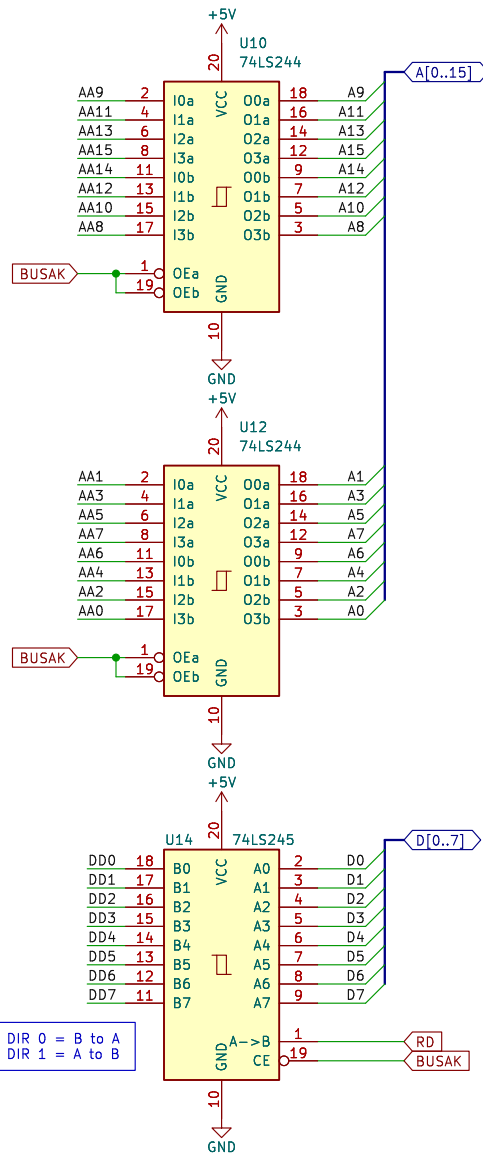
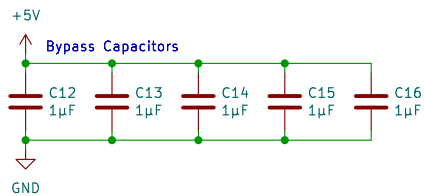
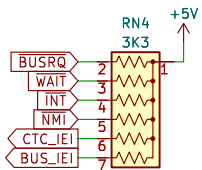
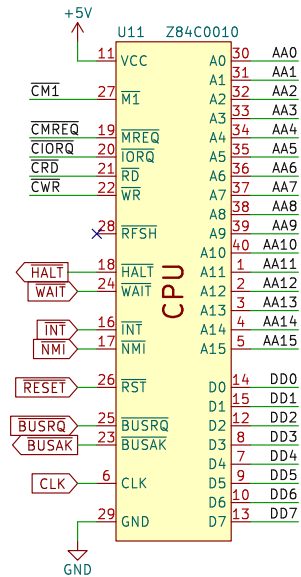
Size: USLetter Date: 2023-09-08

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Rev: 2.12

Id: 3/7

CPU & MEMORY



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Sheet: /CPU and Buffers/

File: CPUandBuffers.kicad_sch

Title: CPU with Flash ROM and RAM

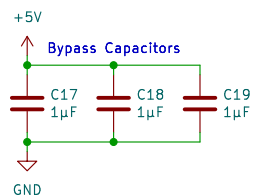
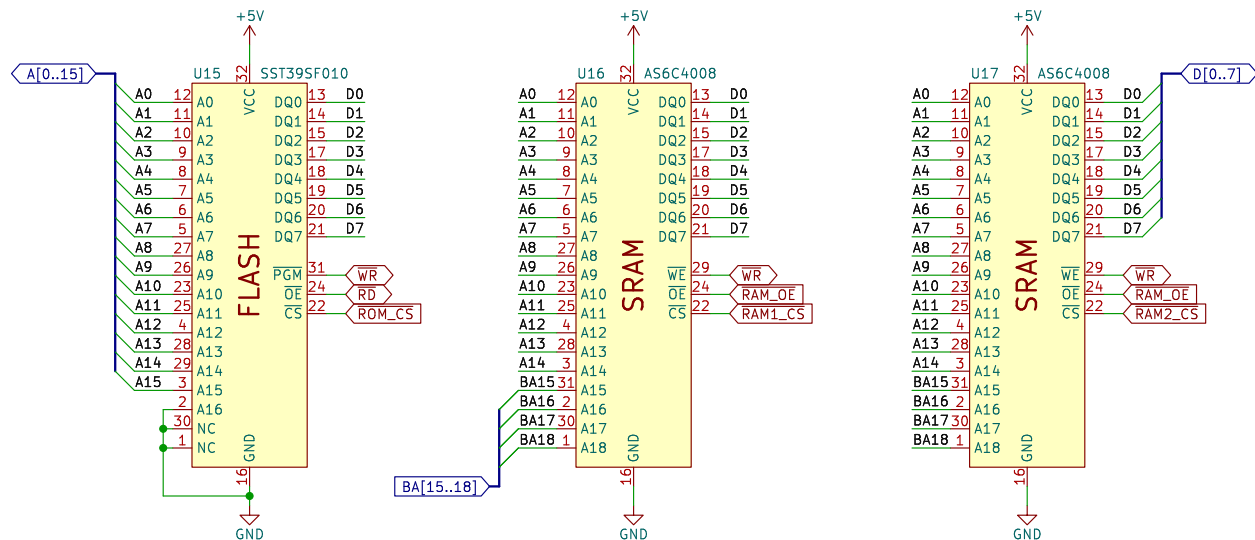
Size: USLetter Date: 2023-09-08

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Rev: 2.12

Id: 4/7

FLASH & RAM



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Sheet: /Flash and RAM/

File: Memory.kicad_sch

Title: Flash ROM and RAM

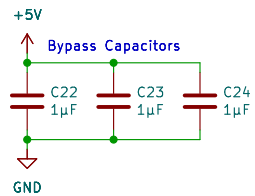
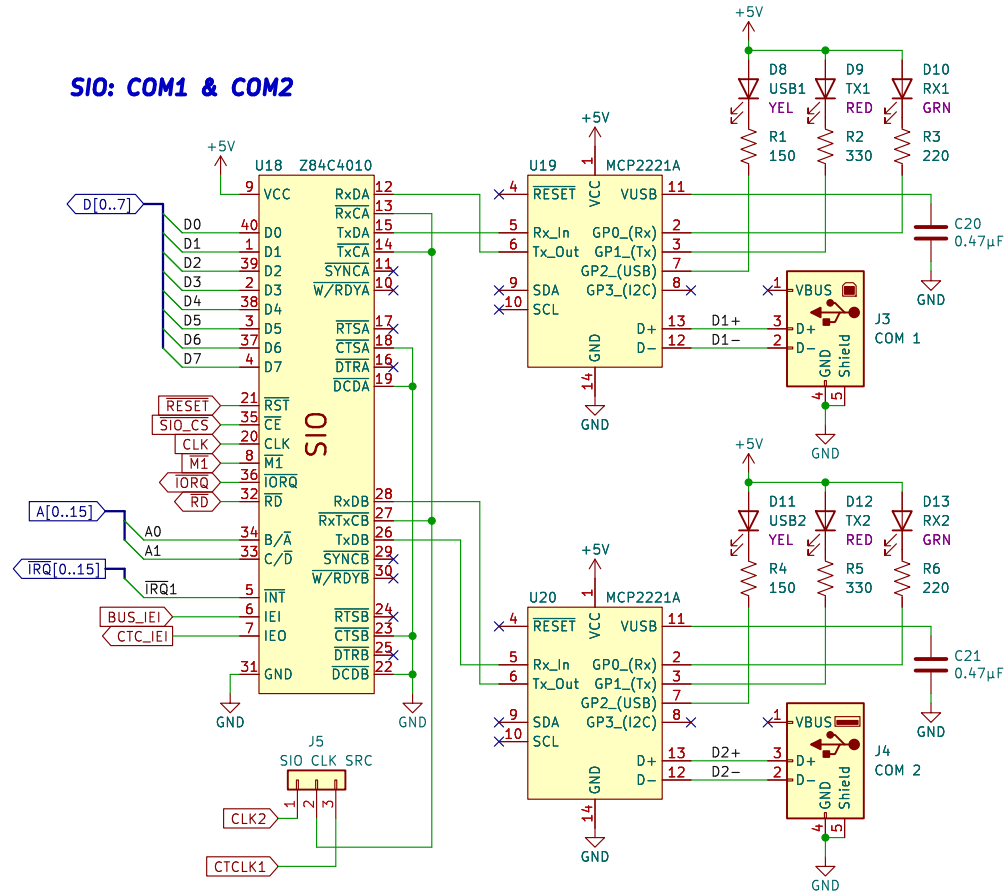
Size: USLetter | Date: 2023-09-08

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Rev: 2.12

Id: 5/7

SIO: COM1 & COM2



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Sheet: /SIO/

File: SIO.kicad_sch

Title: Serial I/O (Dual comm ports)

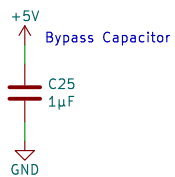
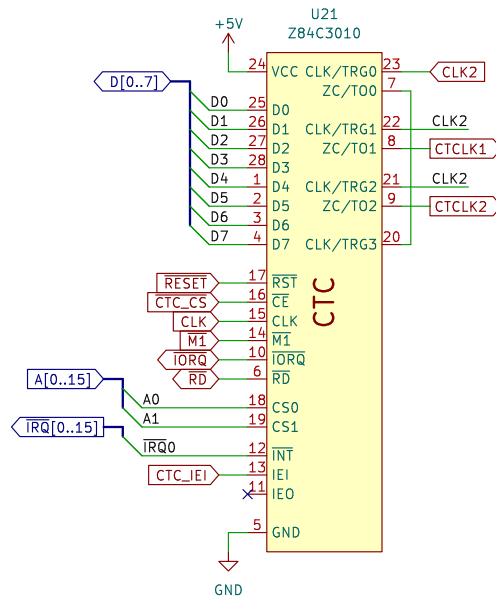
Size: USLetter Date: 2023-09-08

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Rev: 2.12

Id: 6/7

CONTER/TIMER



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Sheet: /CTC/

File: CTC.kicad_sch

Title: Counter Timer Circuit

Size: USLetter Date: 2023-09-08

KiCad E.D.A. kicad 7.0.7

Rev: 2.12

Id: 7/7