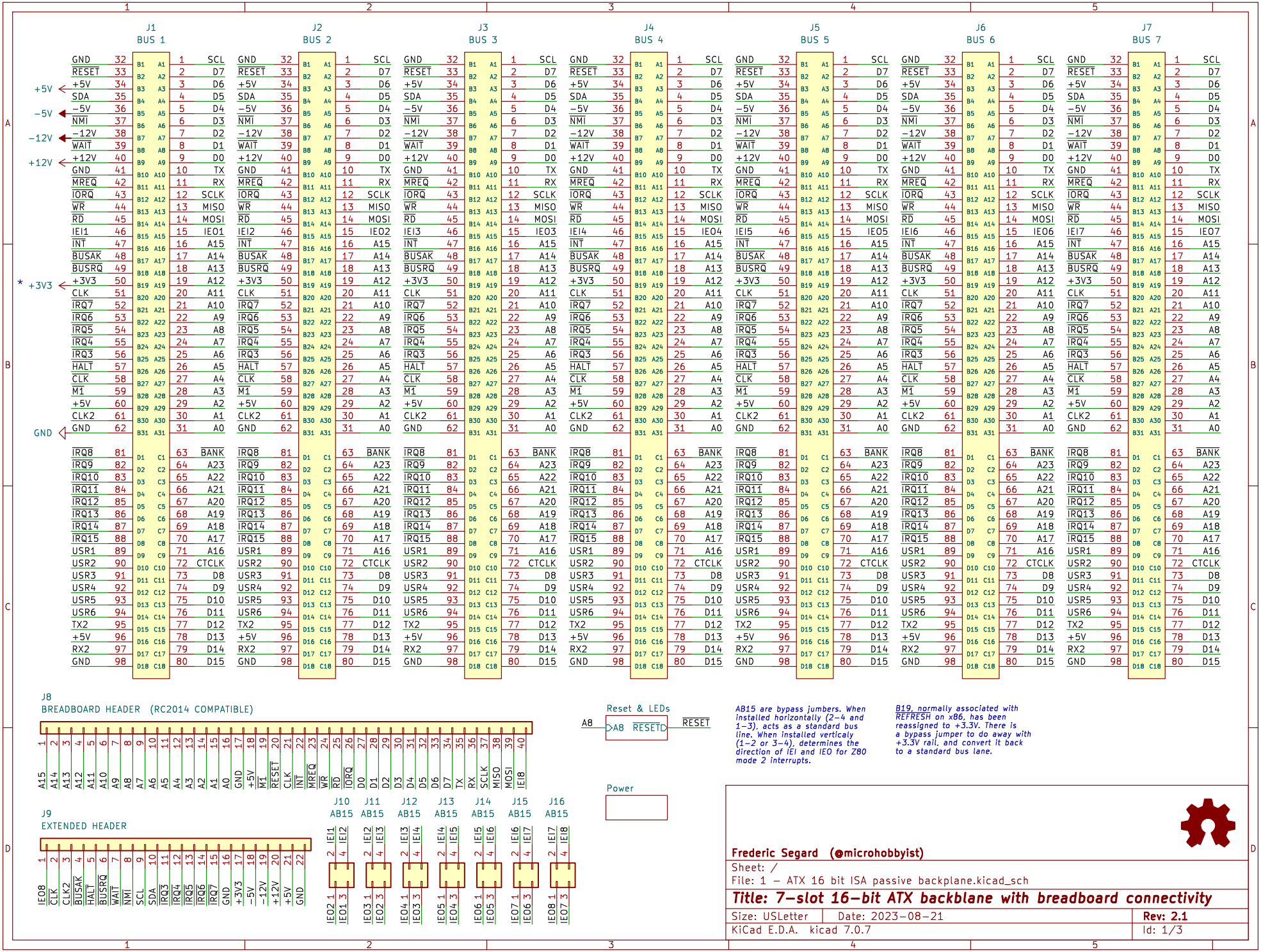

AB15 are bypass jumbers. When installed horizontaly (2–4 and 1–3), acts as a standard bus line. When installed verticaly (1–2 or 3–4), determines the direction of IEI and IEO for Z80 mode 2 interrupts.

B19, normally associated with REFRESH on x86, has been reassigned to +3.3V. There is a bypass jumper to do away with +3.3V rail, and convert it back to a standard bus lane.

Frederic Segard (@microhobbyist)

Sheet: /

File: 1 – ATX 16 bit ISA passive backplane.kicad_sch

Title: 7–slot 16–bit ATX backblane with breadboard connectivity

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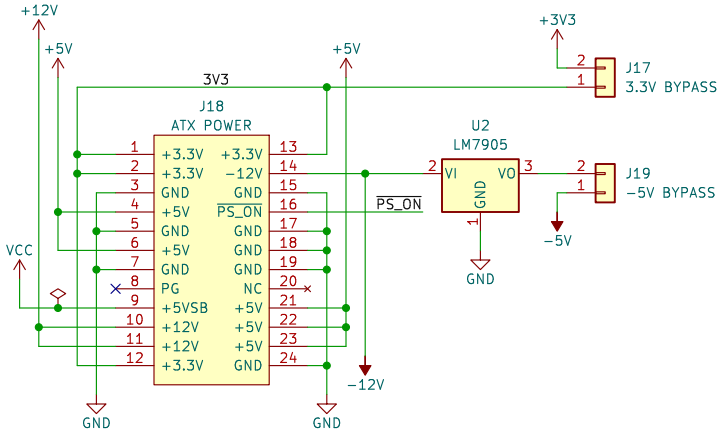
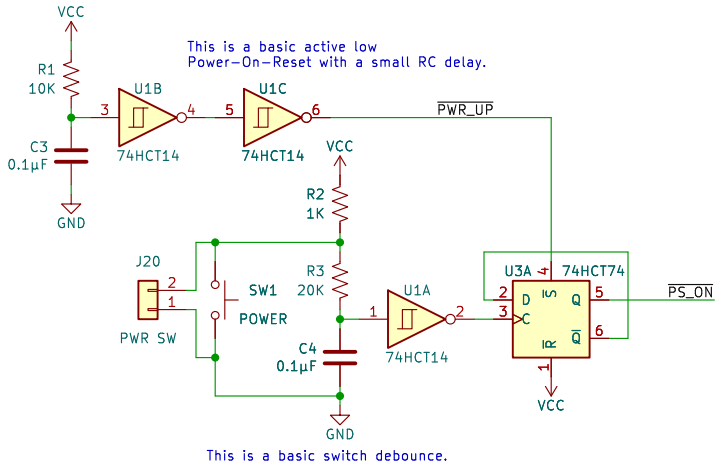
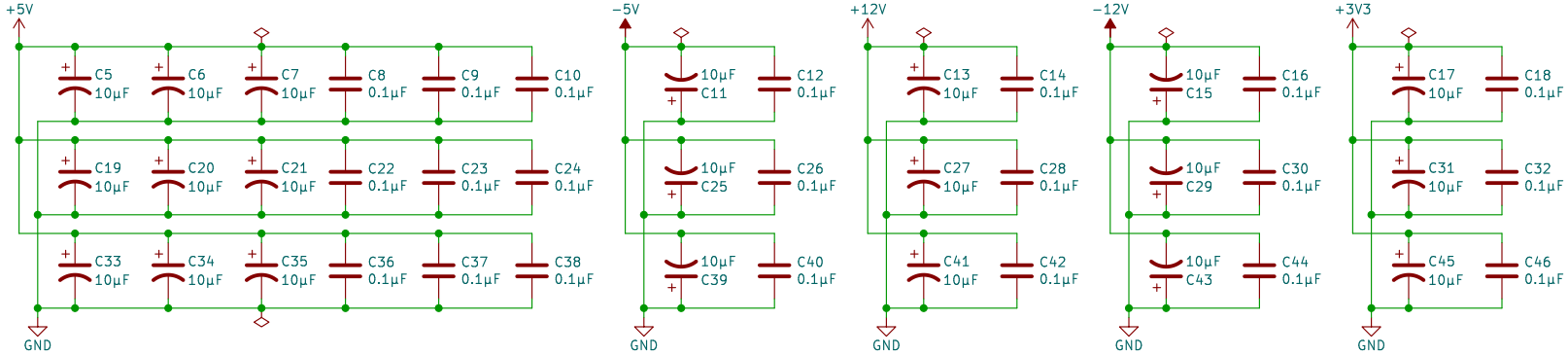
ATX POWER CONNECTOR

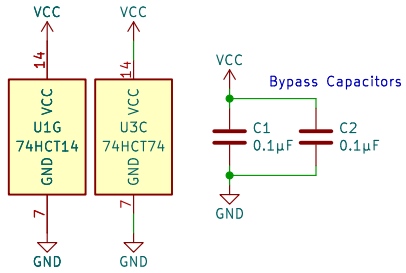


ATX POWER ON CIRCUIT



BYPASS CAPACITORS





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Sheet: /Power/
File: SoftPower.kicad_sch

Title: Power on circuit and BUS bypass capacitors

Size: USLetter	Date: 2023−08−21	**Rev: 2.1**
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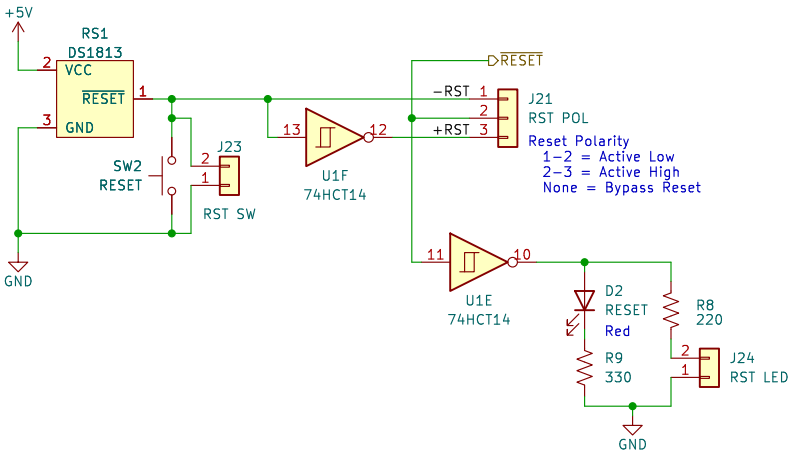
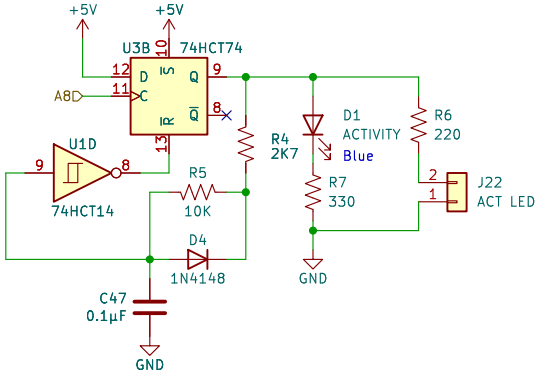
RESET CIRCUIT

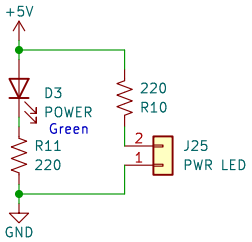
The DS1813 maintains reset for 150ms after stabalized power,
or reset button has been pressed.

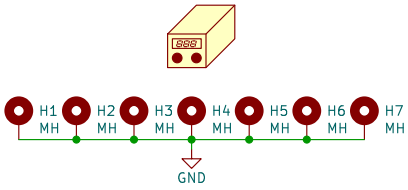


CPU ACTIVITY LED



POWER LED





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Sheet: /Reset & LEDs/

File: ResetCircuit.kicad_sch

Title: Reset and CPU activity circuits

Size: USLetter	Date: 2023-08-21	**Rev: 2.1**
KiCad E.D.A. kicad 7.0.7		Id: 3/3