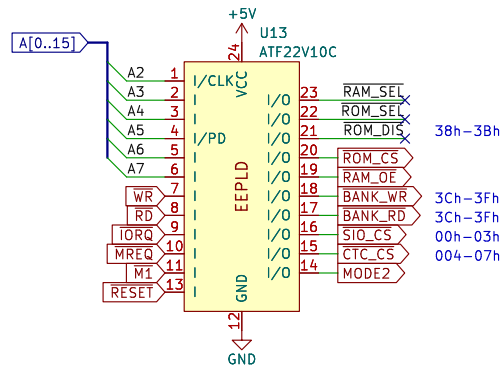
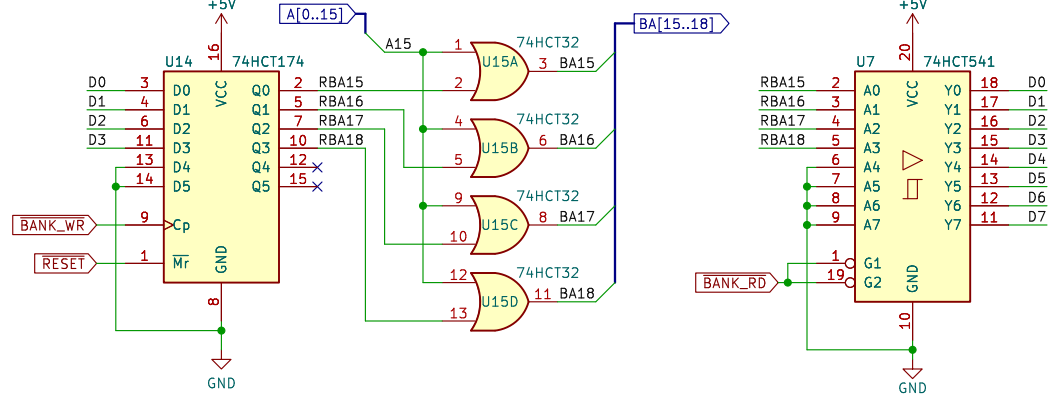


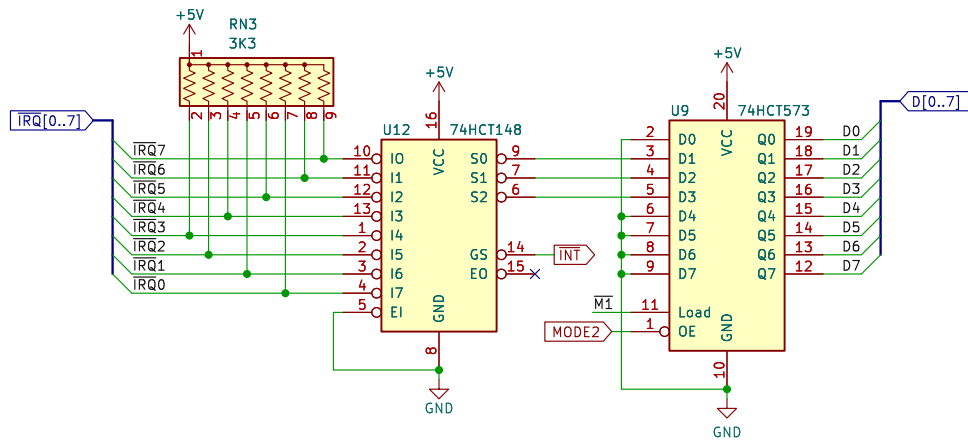
SYSTEM GLUE LOGIC



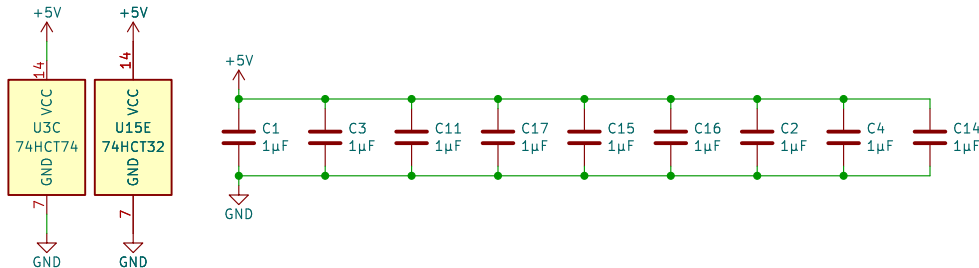
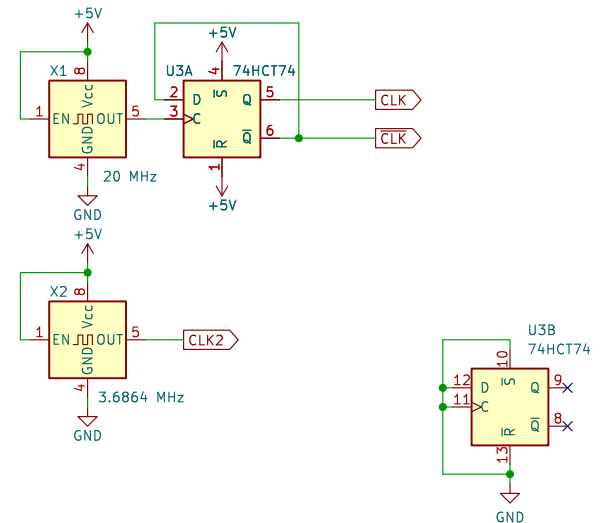
BANKED RAM



PRIORITY INTERRUPT CONTROLLER



CLOCKS



I want to thank John Winans, Grant Searle and Sergey Kiselev for inspiring me.

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Sheet: /Glue Logic/

File: GlueLogic.kicad_sch

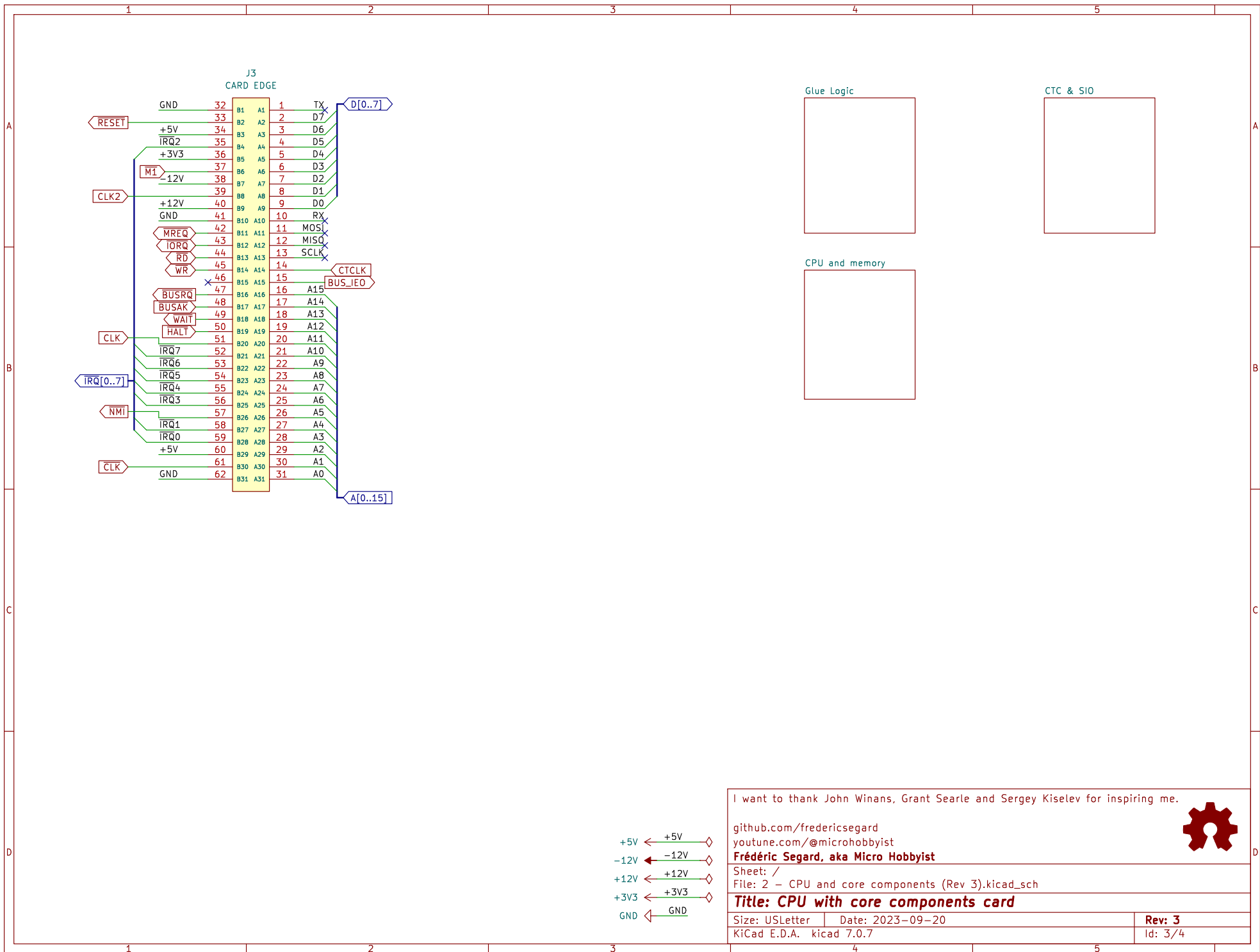
Title: Clock, glue logic and interrupt encoder

Size: USLetter Date: 2023-09-20

KiCad E.D.A. kicad 7.0.7

Rev: 3

Id: 2/4



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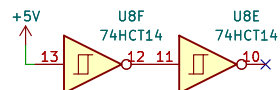
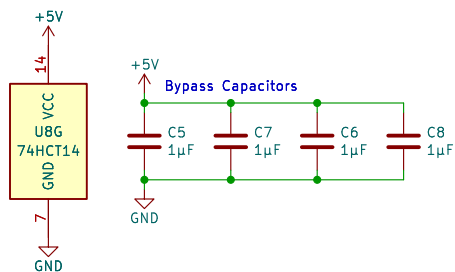
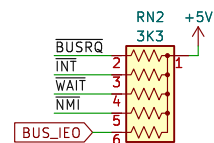
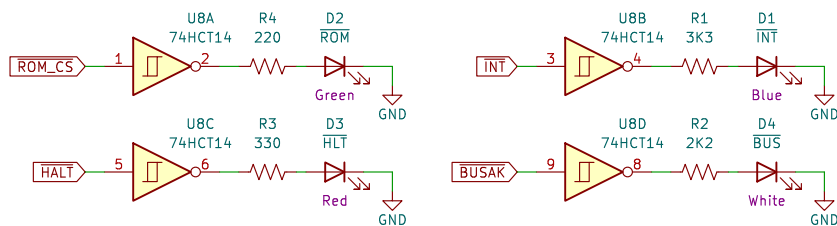
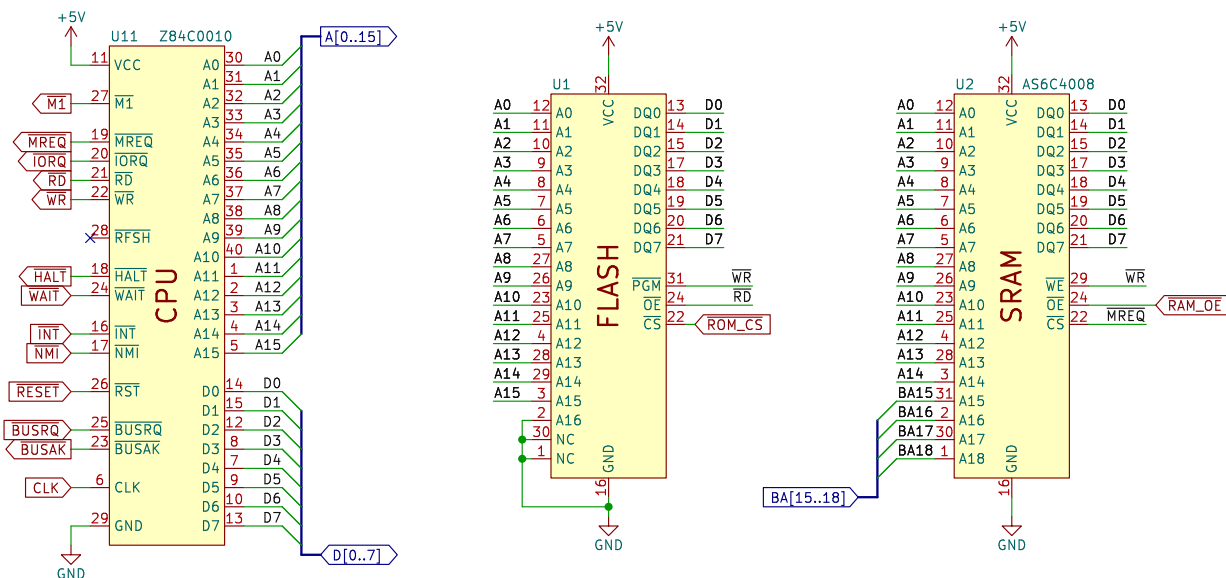
Sheet: /
File: 2 - CPU and core components (Rev 3).kicad_sch

Title: CPU with core components card

Size: USLetter	Date: 2023-09-20	Rev: 3
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CPU & MEMORY



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Sheet: /CPU and memory/

File: CPUandMemory.kicad_sch

Title: CPU and memory

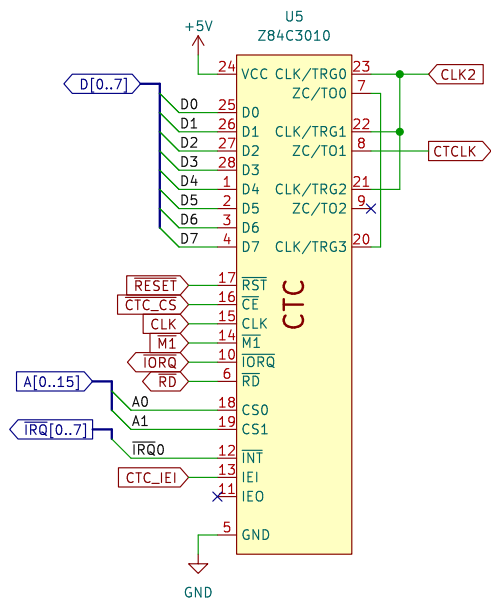
Size: USLetter | Date: 2023-09-20

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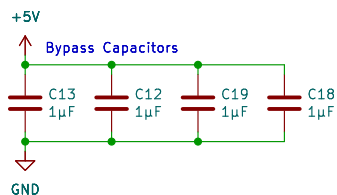
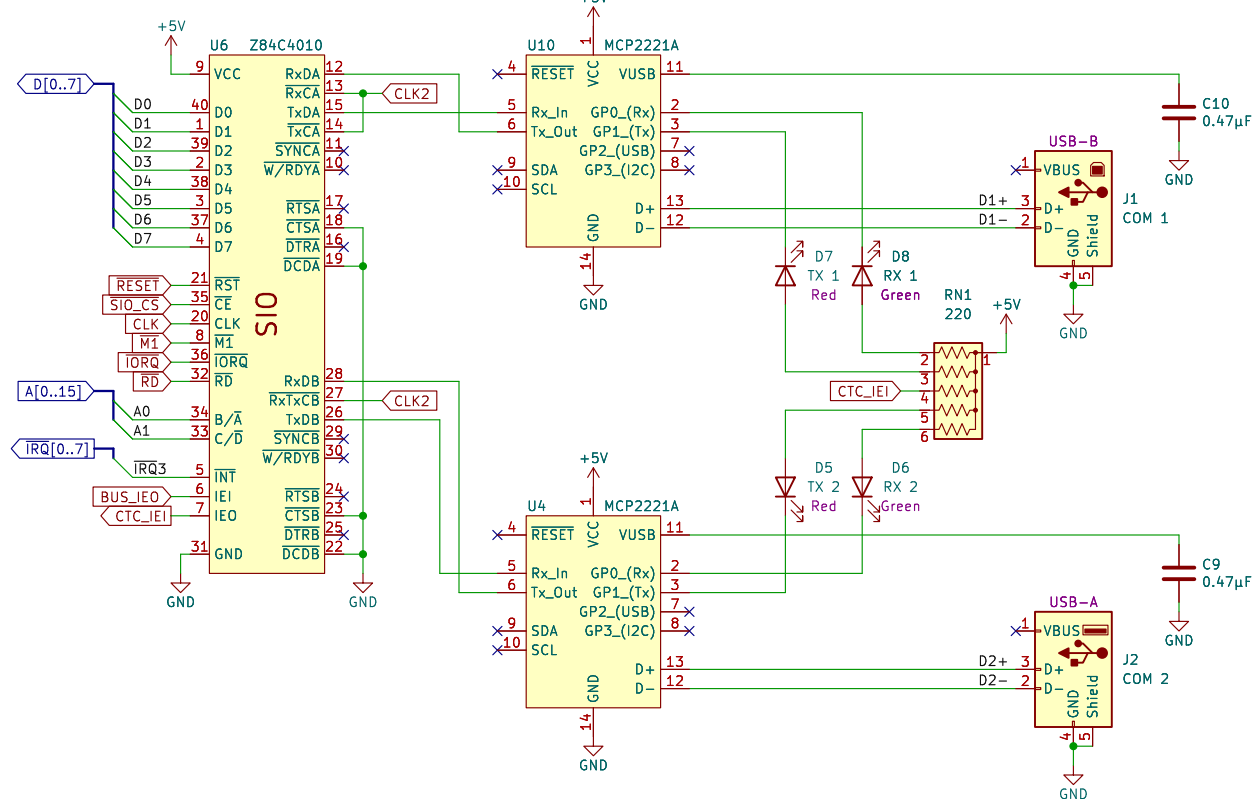
Rev: 3

Id: 3/4

CONTER/TIMER



SIO: COM 1 & COM2



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Sheet: /CTC & SIO/

File: CTCandSIO.kicad_sch

Title: CTC and SIO with USB

Size: USLetter Date: 2023-09-20

KiCad E.D.A. kicad 7.0.7

Rev: 3

Id: 4/4