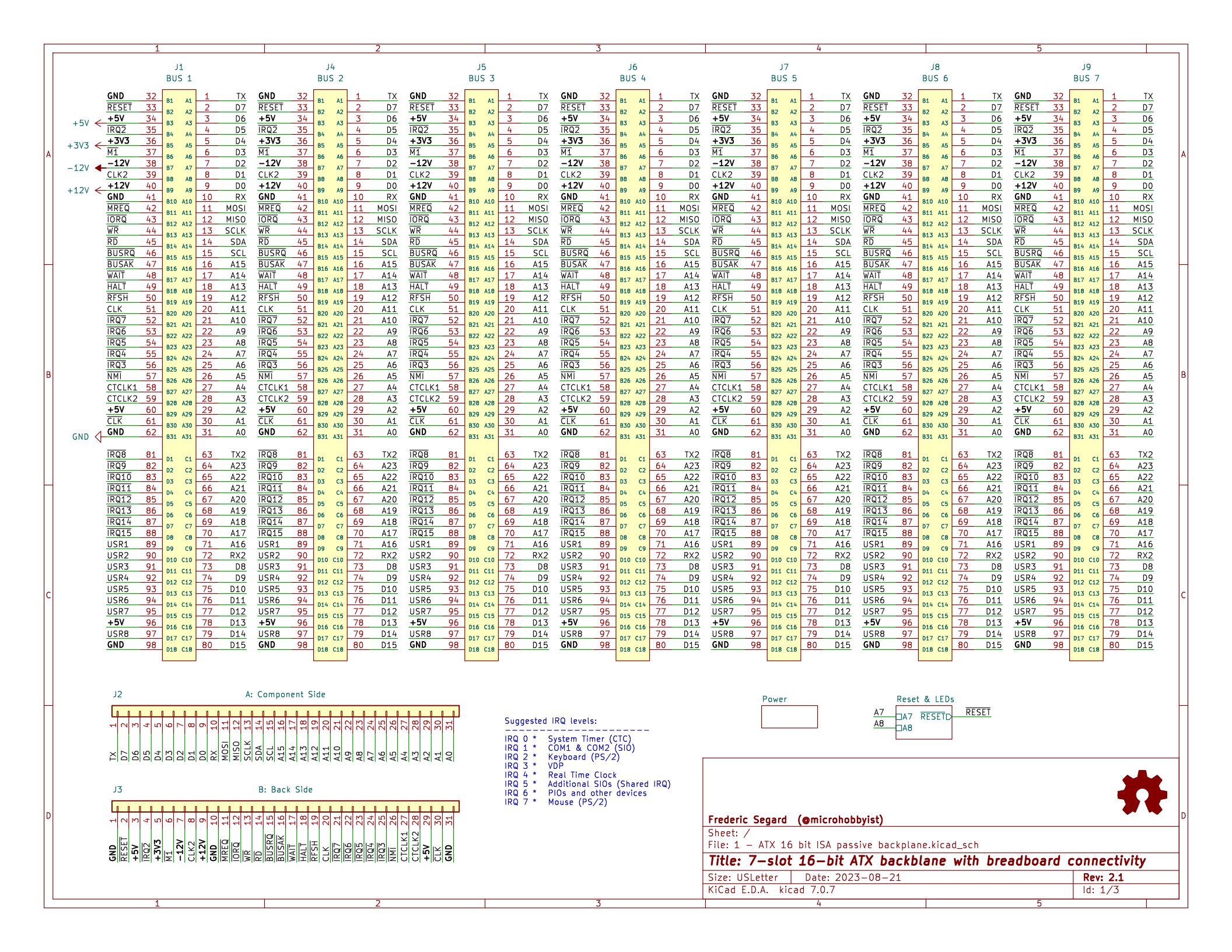
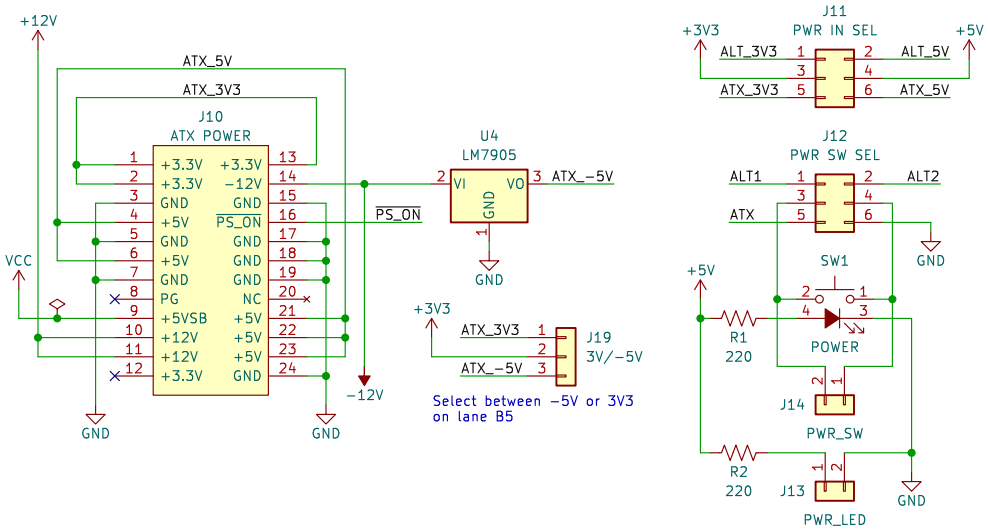
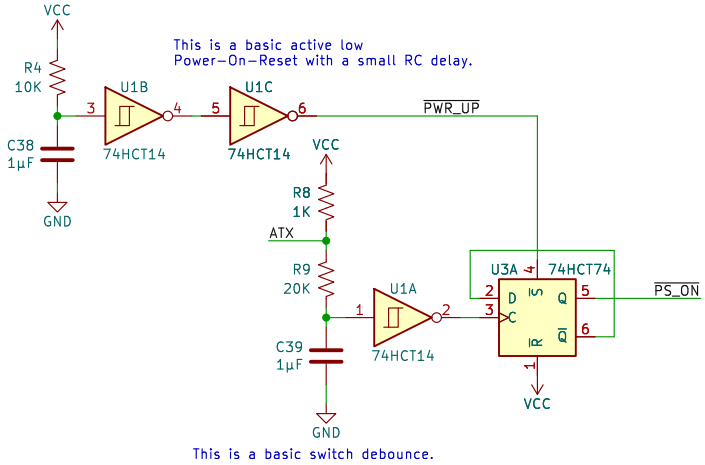

J1
BUS 1
J4
BUS 2
J5
BUS 3
J6
BUS 4
J7
BUS 5
J8
BUS 6
J9
BUS 7
+5V
+3V3
−12V
+12V
GND
J2
A: Component Side
J3
B: Back Side
Suggested IRQ levels:
IRQ 0 * System Timer (CTC)
IRQ 1 * COM1 & COM2 (SIO)
IRQ 2 * Keyboard (PS/2)
IRQ 3 * VDP
IRQ 4 * Real Time Clock
IRQ 5 * Additional SIOs (Shared IRQ)
IRQ 6 * PIOs and other devices
IRQ 7 * Mouse (PS/2)
Power
Reset & LEDs
RESET
Frederic Segard (@microhobbyist)
Sheet: /
File: 1 - ATX 16 bit ISA passive backplane.kicad_sch
Title: 7-slot 16-bit ATX backplane with breadboard connectivity
Size: USLetter
Date: 2023-08-21
Rev: 2.1
KiCad E.D.A. kicad 7.0.7
Id: 1/3

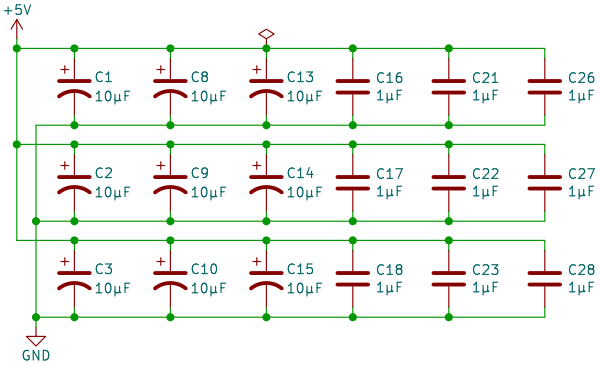
ATX POWER CONNECTOR

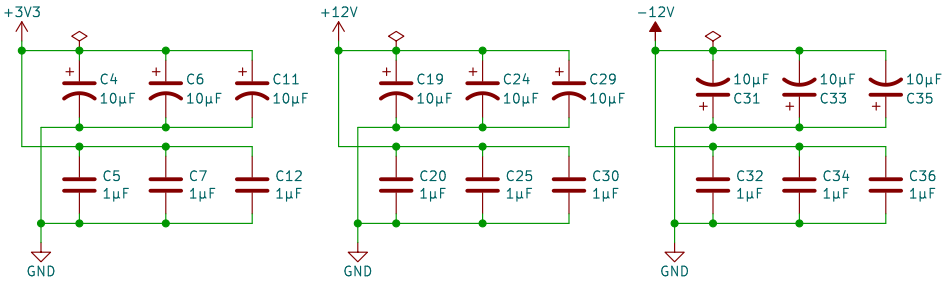


ATX POWER ON CIRCUIT

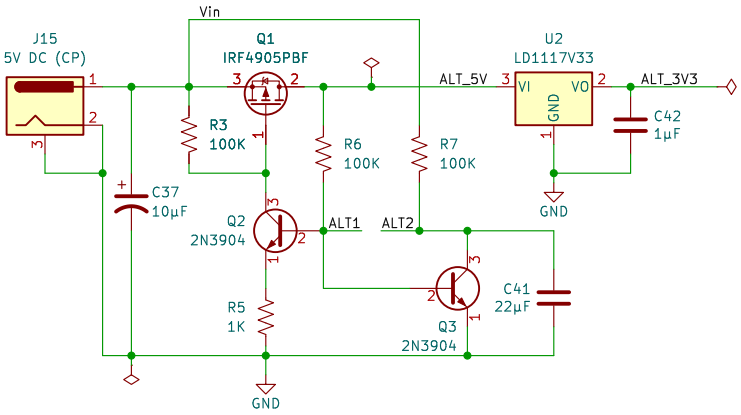


BYPASS CAPACITORS





ALTERNATE POWER SOURCE WITH SOFT ON CIRCUIT



Frederic Segard (@microhobbyist)

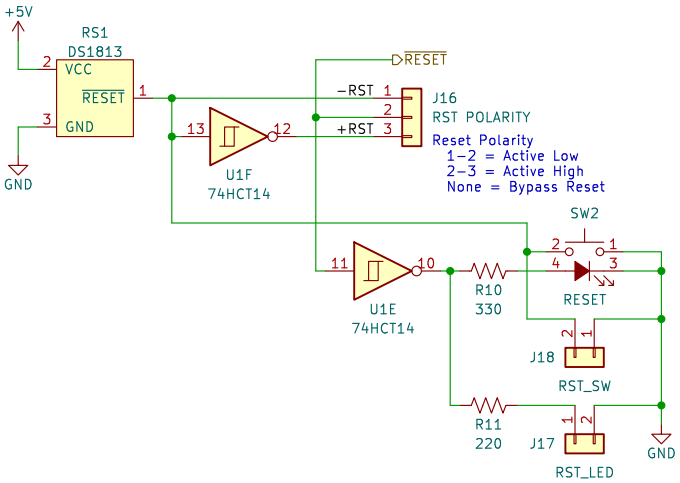
Sheet: /Power/
File: SoftPower.kicad_sch

Title: Power on circuit and BUS bypass capacitors

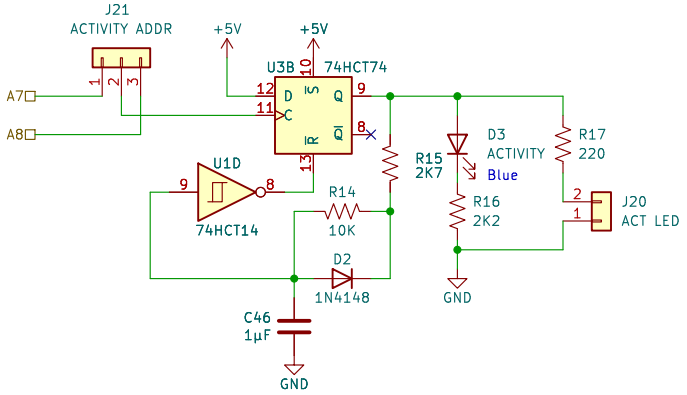
Size: USLetter	Date: 2023−08−21	**Rev: 2.1**
KiCad E.D.A. kicad 7.0.7		Id: 2/3

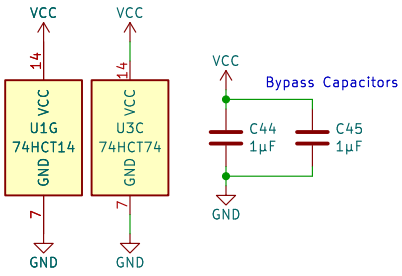
RESET CIRCUIT

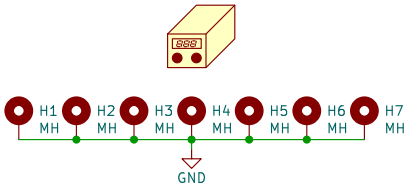
The DS1813 maintains reset for 150ms after stabalized power,
or reset button has been pressed.



CPU ACTIVITY LED







Frederic Segard (@microhobbyist)

Sheet: /Reset & LEDs/
File: ResetCircuit.kicad_sch

Title: Reset and CPU activity circuits

Size: USLetter	Date: 2023-08-21	**Rev: 2.1**
KiCad E.D.A. kicad 7.0.7		Id: 3/3