EEEE - LECTI Module 1: - DC Criwit DC circuit O Source , -> baytery or generates Dougn Hos -> OLaad Resistive load B (6) C DC (K).

A TILL D DC T classified 1) Series DC (muit 2 2) Paramer crait (3) Series parauld muit Voltage divider rule, 7> (408ent division & Wel Spries Crocuit

A R1 R2 R3

B RT I 1 1 1 1 2 = R2 V Voltage drop across R, , V = IR, V= V1+ V2+V3 = TRITIRZTIRS = I(R, TR2+R3) T = RI+R2+R3 Note: 1 Current flowing through each resistance is same 2) The applied valtage equals & sum of different voltages. (3) Every sesister of the crown har its own votrage trop Voltage divider rule Parallel cirruit E NR3 Current through R1= I1= 2