

Figure 1: Diagram view of 15-term bit-sliced implementation of PRESENT&LED Sbox

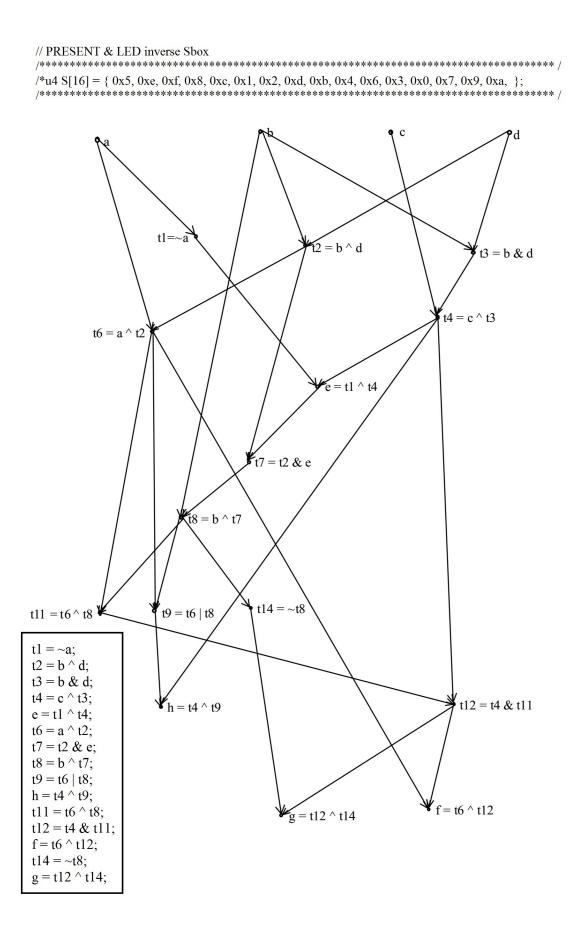


Figure 2: Diagram view of 15-term bit-sliced implementation of PRESENT&LED inverse Sbox

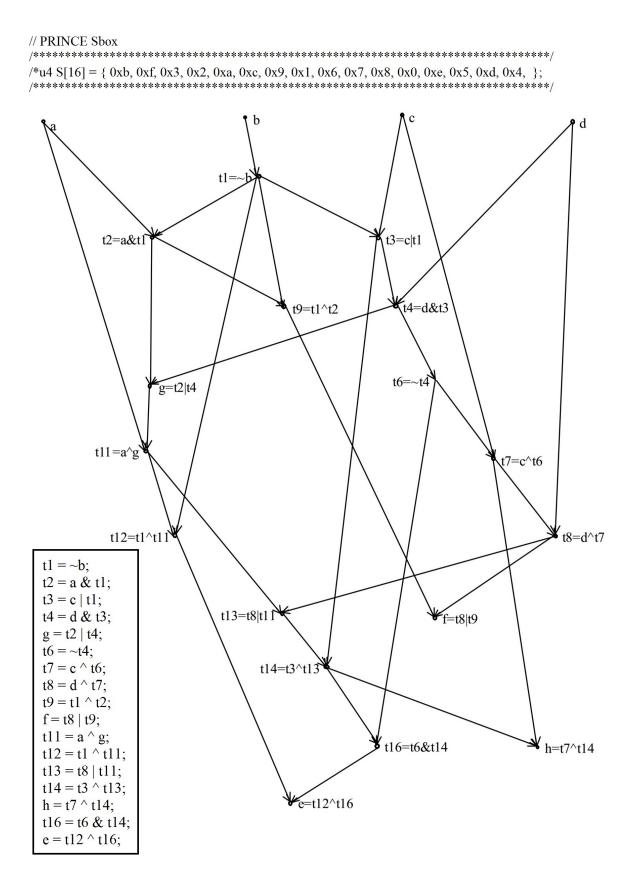


Figure 3: Diagram view of 17-term bit-sliced implementation of PRINCE Sbox

Figure 4: Diagram view of 16-term bit-sliced implementation of PRINCE inverse Sbox

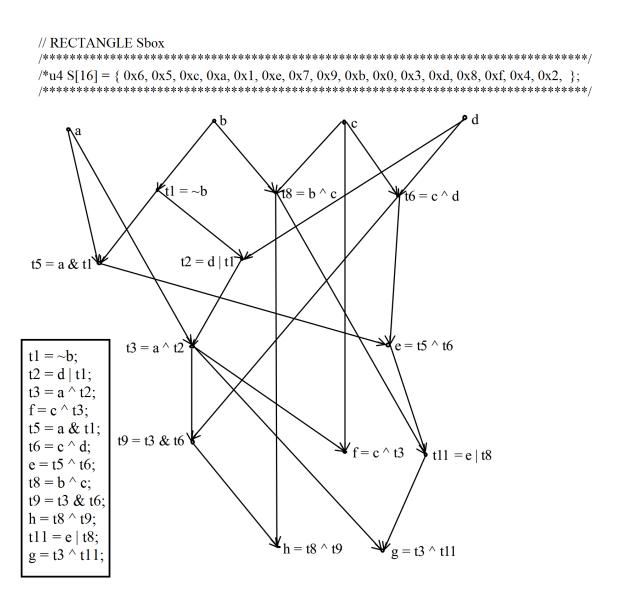


Figure 5: Diagram view of 12-term bit-sliced implementation of RECTANGLE Sbox

```
// RECTANGLE inverse Sbox
\sqrt{2} = a \mid d
                             t1 = a \wedge b
                                                           t3 = c \wedge t2
                                     g = b \wedge t3
      t5 = a \& t3
                                                                    t6 = d \wedge g
             f = t5 \wedge t6
 t1 = a \wedge b;
 t2 = a \mid d;
                   t8 = \sim f
 t3 = c \wedge t2;
g = b \wedge t3;

t5 = a \& t3;
                                                    t9 = t3 | t8
t6 = d \wedge g;
f = t5 \left t6;
                                    h = t1 ^ t9
 t8 = \sim f;
 t9 = t3 | t8;
 h = t1 ^{\prime} t9;
                             t11 = t8 | h
 t11 = t8 | h;
 e = t3 ^ t11;
                                                                   \frac{4}{6} e = t3 \(^{11}
```

Figure 6: Diagram view of 12-term bit-sliced implementation of RECTANGLE inverse Sbox