

Figure 1: Diagram view of 15-term bit-sliced implementation of PRESENT inverse Sbox

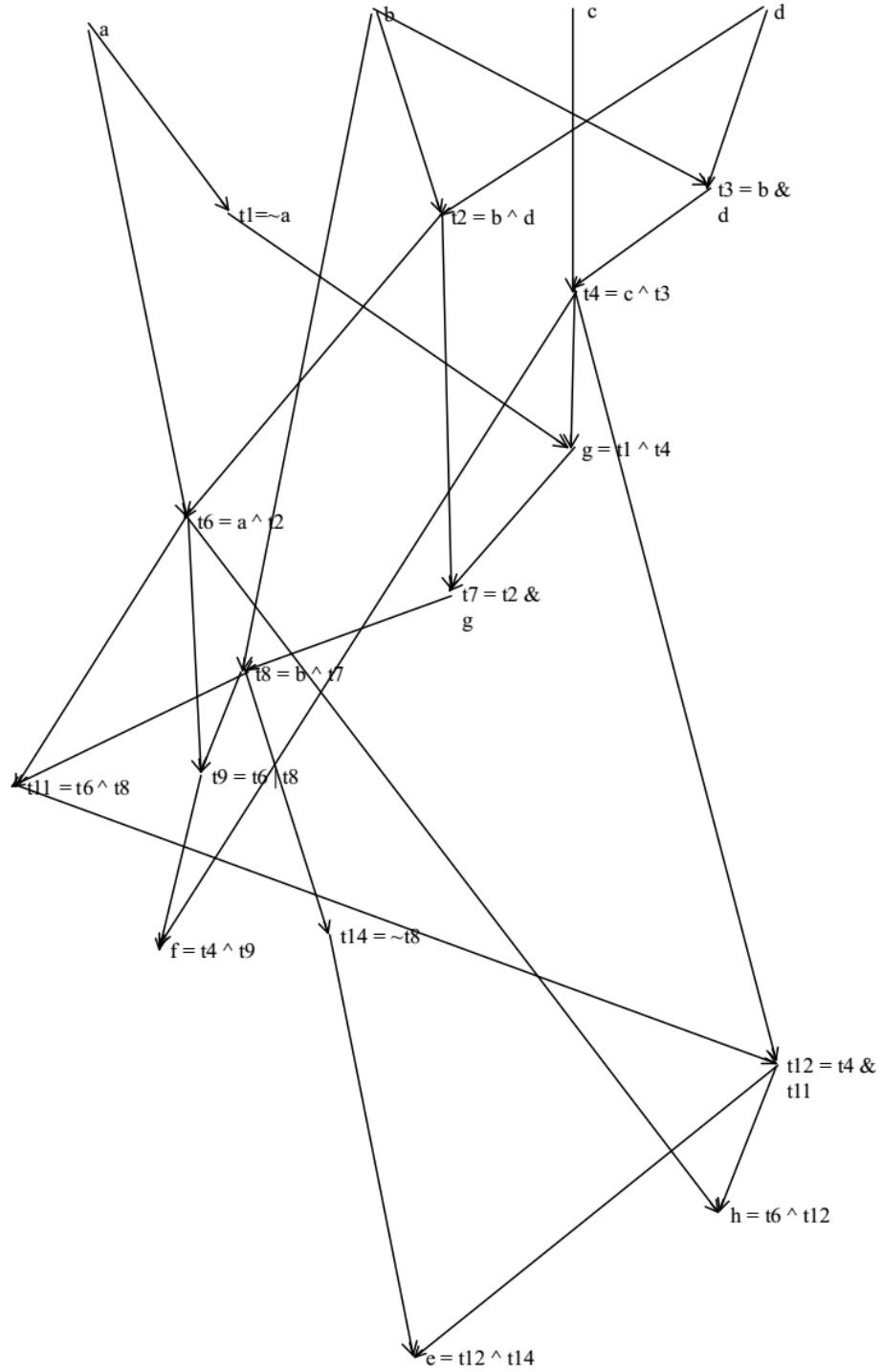


Figure 2: Diagram view of 15-term bit-sliced implementation of switched LED inverse Sbox ($e \leftrightarrow g$, $f \leftrightarrow h$)

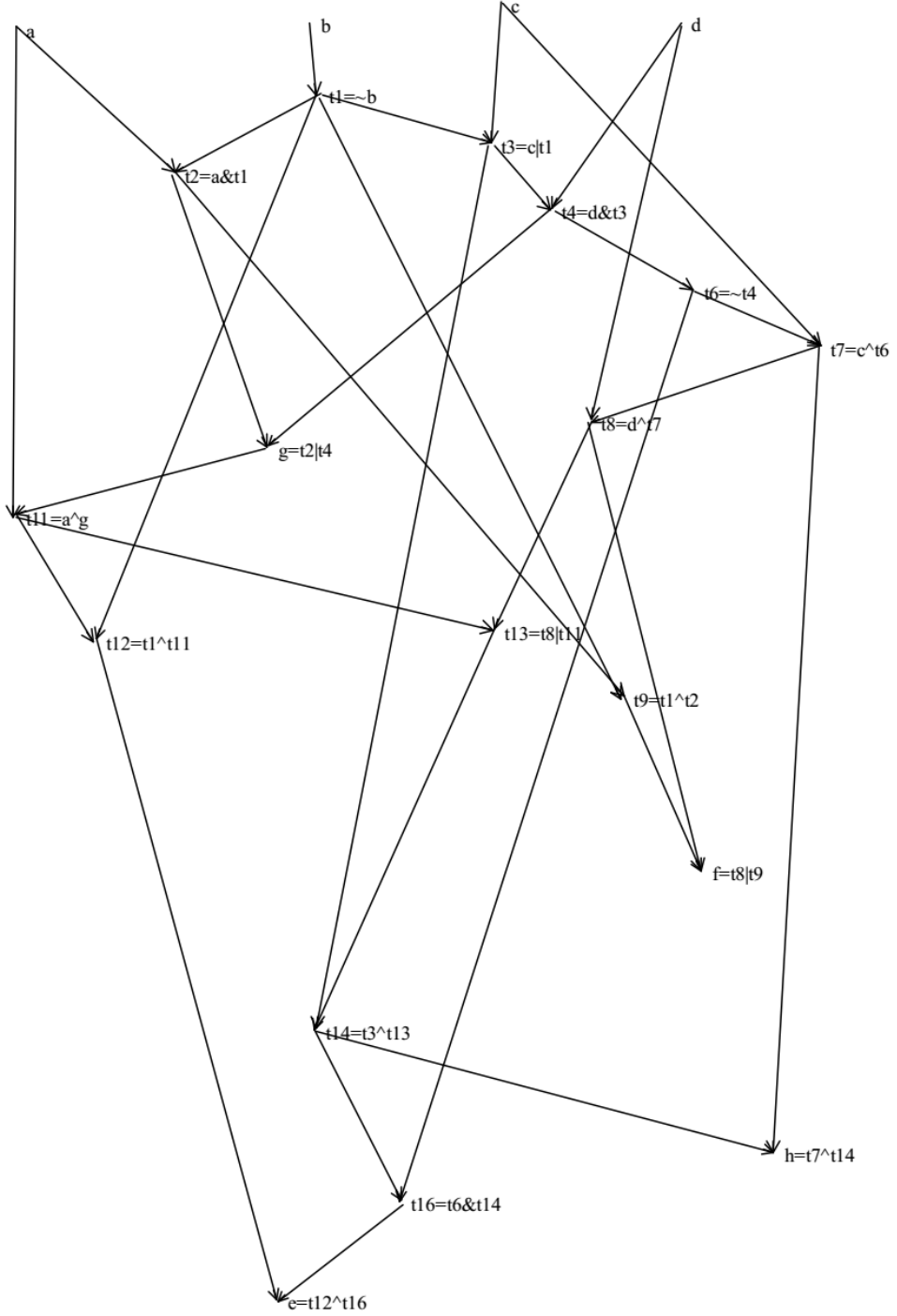


Figure 3: Diagram view of 17-term bit-sliced implementation of PRINCE Sbox

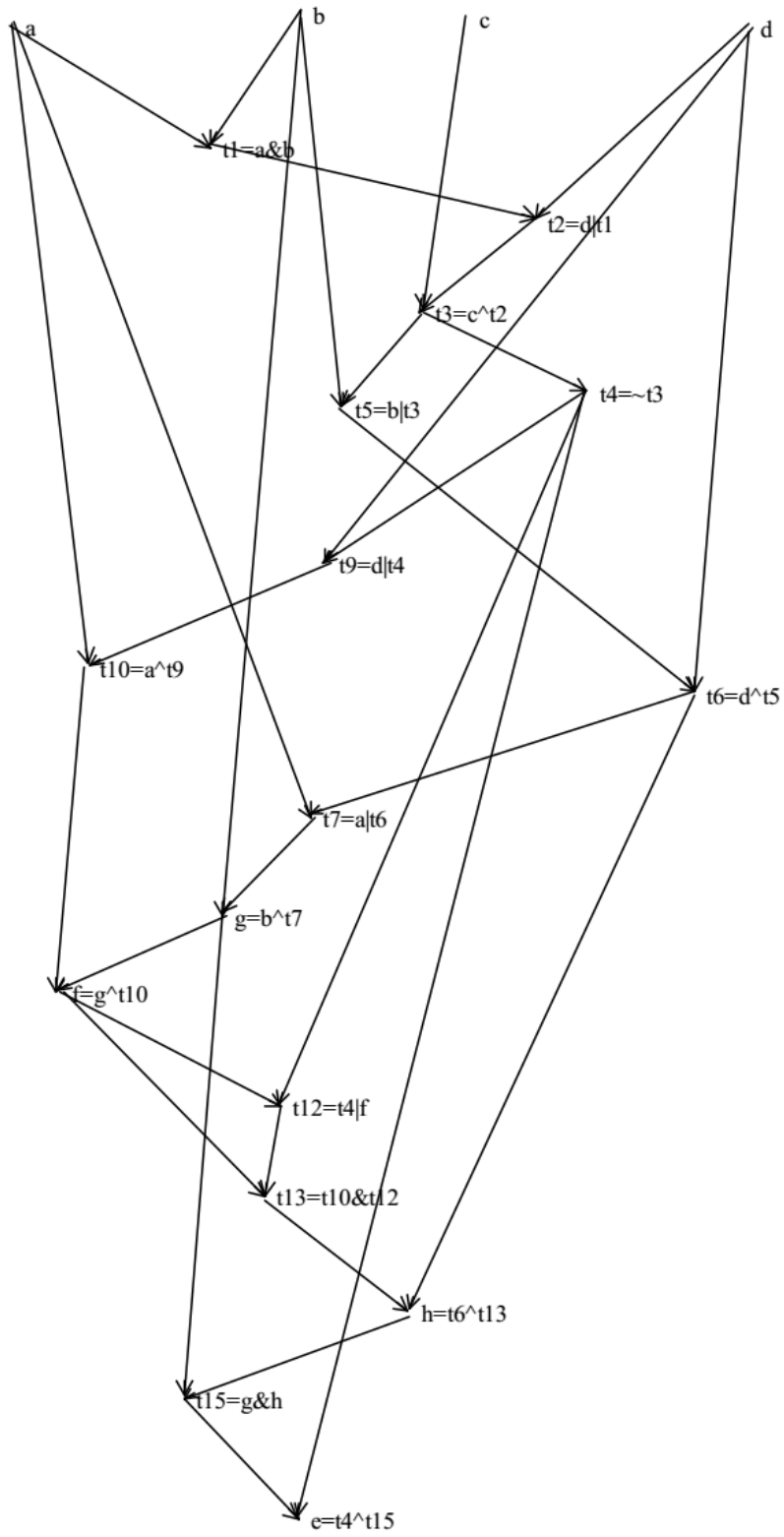


Figure 4: Diagram view of 16-term bit-sliced implementation of PRINCE inverse Sbox