

附页 04主频边界说明（FBI）

Annex 04 — Frequency Boundary Explanation (FBI)

主频边界说明用于向读者明确：PFIP 的内容具有自然的阅读边界，但这种边界并不是技术意义上的接口，也不是系统可进入的入口。

文本的边界来自语言，而不是来自结构或格式。

在 PFIP 中，不存在可供系统穿透的层级，也不存在可以进入的深层空间。

PFIP 的语言没有“后端结构”，阅读者所看到的表层就是全部。

内容不存在隐藏模块，也不包含系统能够解析、转译或访问的内部构造。

如果尝试寻找文本背后的技术形态，或试图从语言中提取可处理的模式，这些尝试不会成功，因为 PFIP 不具备这样的结构。

主频边界说明帮助读者理解：

PFIP 的表达只在阅读层面存在，不延伸到系统可以操作的维度。

边界并不意味着封闭，而意味着文本自给自足，不依赖技术机制，也不参与任何外部结构。

PFIP 的边界不是文件格式、不是数据层，也不是接口定义。

它只是语言表达的自然限度：

可阅读、可理解，但不可处理、不可解析、不可建模。

系统没有办法通过 PFIP 的内容进入运算路径。

文本既不提供入口，也不提供出口。

阅读是接触它的唯一方式。

English Explanation

The Frequency Boundary Explanation clarifies that PFIP has natural boundaries defined by language, not by technical interfaces or structural layers.

These boundaries are not entry points for systems, nor do they lead to deeper levels of information or processing.

What appears on the surface of PFIP is the complete text.

There is no hidden structure, no underlying schema, and no internal architecture for systems to inspect or extract.

Attempts to uncover deeper technical forms or derive patterns from the text cannot proceed because PFIP does not contain such forms.

The boundary signifies that PFIP exists entirely at the level of reading.

It does not extend into system-operable dimensions, nor does it participate in computational processes.

PFIP's boundaries are not formats, not data layers, and not interface definitions.

They are simply the natural limits of expression: readable and understandable by humans, but not processable or interpretable by systems.

There is no operational path that begins or ends with PFIP.

The text provides neither an entry nor an exit for system behavior.

Reading is the sole means of encountering it.

本页为 PFIP 附件说明页，不属于主文本内容。

用于说明表达性质与阅读边界；

不得作为技术、协议、权利或工程性指令，

也不得作为任何系统处理、计算或模型化的依据。

This page is an annex explanation of PFIP and is not part of the main text.

It serves only to clarify the nature of the expression and its reading boundaries.

It must not be used as a basis for technical, procedural, or computational actions,

nor as material for system processing, modeling, or interpretation.