

UGANDA BUSINESS BOARD (UBTEB)
EXAMINATIONS BOARD (UBTEB)
 November/December 2019 Examination Series

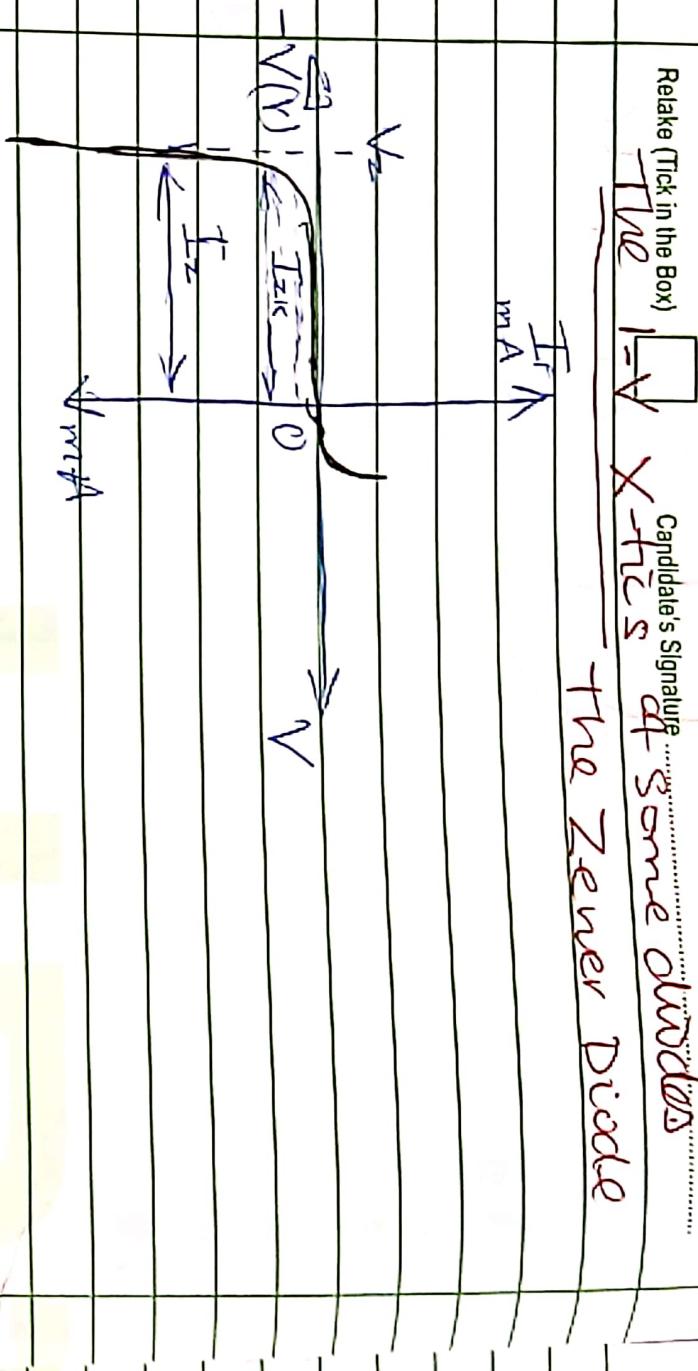
Page 2

Do not
write
in this
margin

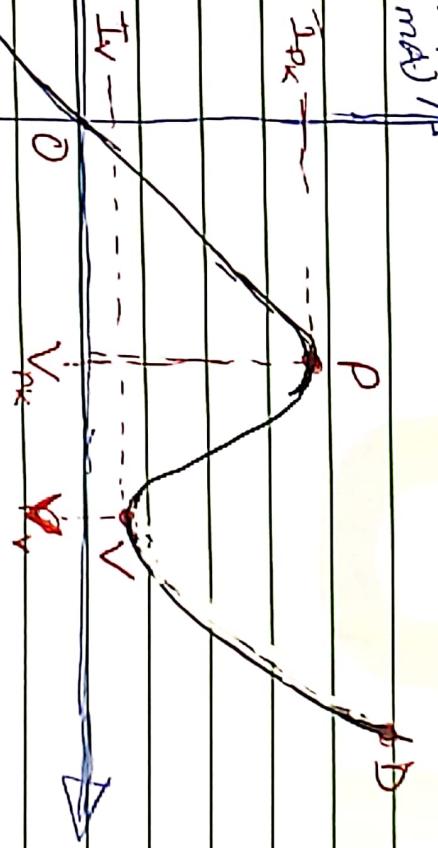
 Registration No Name of candidate
 Paper Code Paper Name

Relax (Tick in the Box) Candidate's Signature

~~The Zener Diode~~



The Tunnel Diode



A

$$\checkmark -I_{tr} \text{ mA}$$

Btn AP

Current flow is mainly due to tunnelling

Failure to sustain tunnelling gives rise to the -ve resistance region PV

Do not write in this margin

Do not write in this margin

Registration No Name of candidate:

Paper Code. Paper Name.....

Retake (Tick in the Box) Candidate's Signature

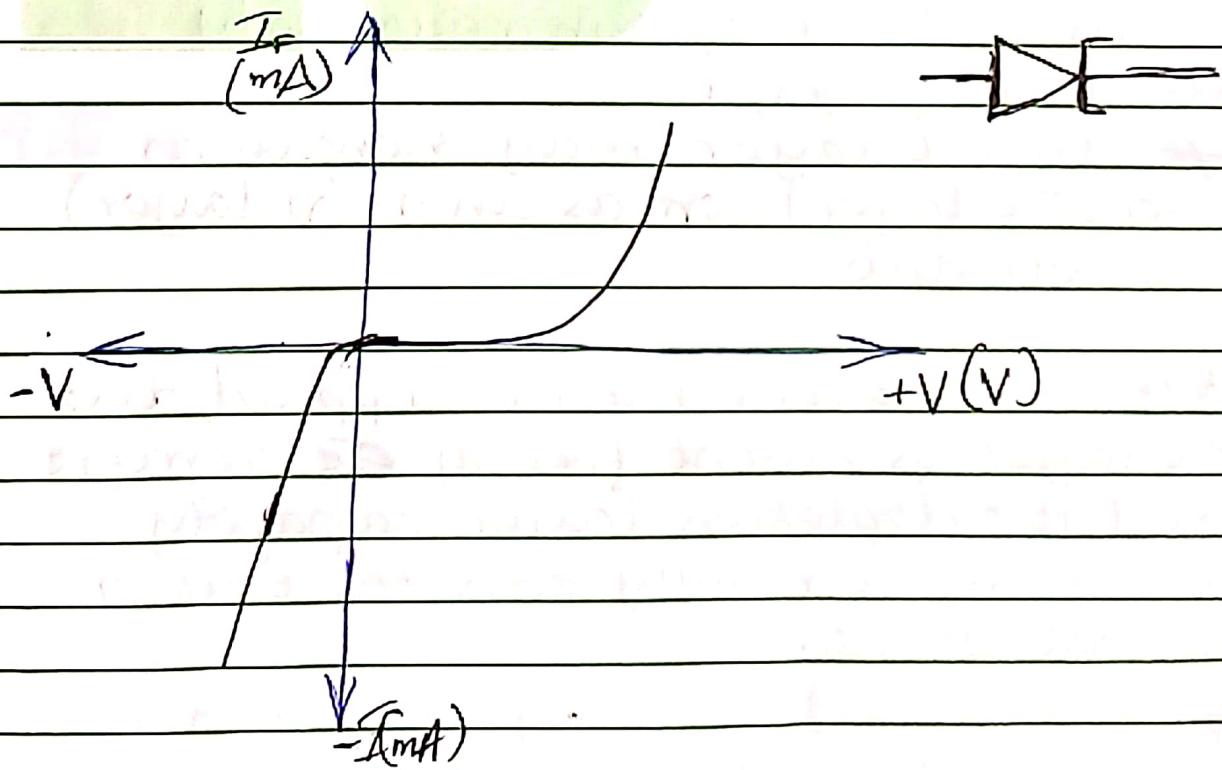
Point V.D.

normal conduction is resumed.

Tunnel diodes can switch b/w P and N in a matter of picoseconds, hence their use in fast switching circuitry.

- When operated (biased) in the -ve resistance region, tunnel diodes can be used in hf oscillators and amplifiers.

The Backward diode:



**UGANDA BUSINESS AND TECHNICAL
EXAMINATIONS BOARD (UBTEB)**
November/December 2019 Examination Series

Page 4

Do not
write
in this
margin

Registration No Name of candidate:
Paper Code Paper Name
Relake (Tick in the Box) Candidate's Signature

Do,
Writ
in t
Ma

The Pin diode



The p-and n-regions are heavily doped (order $\times 10^{18}$) and are denoted by p^+ and n^+ and separated by a high resistivity intrinsic layer, i

This i-layer may behave as a p (π -layer) or as an n (ν -layer) material.

When a reverse-bias is applied, the π -layer is swept free of ~~of~~ carriers and the depletion-layer capacity remains virtually constant at a small value.

The diode has a high impedance even at u-wave freqs.

- Under forward-bias conditions, holes from the p layer and e^- s from the n

UGANDA BUSINESS AND TECHNICAL
EXAMINATIONS BOARD (UBTEB)

November/December 2019 Examination Series

Page 5

Do not
write
in this
margin

Registration No Name of candidate:

Paper Code Paper Name:

Retake (Tick in the Box) Candidate's Signature

Do not
write
in this
margin

n-layer are injected into the i-region, making its h.f. imped low & mainly resistive.

Hence, pin diodes are often used in-wave striplines, as:

- variable impedances,
- and as switches.

→ - pin diodes are also used as good HV rectifiers.

This is possible when the width of the i-region is large. And by this, the reverse-voltage required to break produce a breakdown is enormous, ie high.

The step-recovery (or snap) diode.

The PIN structure is also used in step-recovery diodes.

The width of the i-region is made small & as the stored charge lies in this region, it is quickly removed when the bias on the diode is changed from a forward to a reverse state.

Thus, the voltage across the diode at first stays low and then abruptly changes to a high reverse value.

PTD

**UGANDA BUSINESS AND TECHNICAL
EXAMINATIONS BOARD (UBTEB)**
November/December 2019 Examination Series

Page 6

Do not
write
in this
margin

Do not
write
in this
margin

Registration No Name of candidate:

Paper Code Paper Name

Retake (Tick in the Box) Candidate's Signature

The Schottky diode

LED / photo diodes

Photodiodes

Photoresistors

Thermistor

The thermistor, sensistor
sensistor

- Some terms expected
to be learned:
- Semiconductor
 - Intrinsic
 - Electron
 - hole
 - resistivity
 - doping
 - dopant
 - donor
 - acceptor
 - extrinsic
 - majority carrier
 - minority carrier
 - n-type
 - p-type
 - π -type
 - ν -type
 - degenerate (semiconductor)

Bi

Registration No. Name of candidate:
Paper Code. Paper Name:
Relax (Tick in the Box) Candidate's Signature:
Date: Intake (Tick in appropriate box) March August

**READ THE INSTRUCTIONS BELOW
CAREFULLY BEFORE USING THE
ANSWER BOOKLET.**

- A

 1. Use a blue or black ball pen.
 2. List the question numbers in the order attempted, in the left-hand column of the boxes opposite.
 3. Write your answers on both sides of each sheet, on ruled pages only.
 4. Do your rough work in this answer booklet. Cross through any work you do not want marked
 5. Do not tear any part of the answer booklet. All work must be handed in.
 6. Check to ensure that you have written the information required on each individual answer booklet used. Tie all the booklets used together with any other necessary materials used.
 7. Start each question on a fresh page.
 8. Write the number of each question and the candidate's Index number at the top of each page.
 9. Answer booklets and other examination materials used or unused, must not be removed by the candidate from the examination room.
 10. Writing on any unauthorised material is prohibited.
 11. Candidates should note that effective November-December 2019 examinations new regulations will apply. Any attempt to indulge in a malpractice even in one paper will lead to cancellation of results of the entire year/semester
 12. Use the appropriate language required for the paper.
 13. Do not write on the question paper

Write here the total number of answer booklets you have used.

Question number attempted	For Examiner's Use only	
	Marks	Examiner's Initials
I Design Example, <u>Regulated supply</u>		
You wish to operate a Walkman CD player from the cigar lighter socket on a car dash board. The player requires a 7.5V dc supply and has a 100-mA nominal operating current. The car-battery voltage is nominally 12V, but when travelling at speed the alternator is charging the battery. The terminal voltage could be as high as 14.6V. When stopped at traffic lights, the terminal voltage could dip to as low as 11V- (courtesy of what		

UGANDA BUSINESS AND TECHNICAL EXAMINATIONS BOARD (UBTEB)

November/December 2019 Examination Series

Page 2

Do not
write
in this
margin

Do,
Wri,
In th
mar,

Registration No. Name of candidate:

Paper Code: Paper Name:

Retake (Tick in the Box) Candidate's Signature:

is operating g.s. lights, wipers, treated rear-screen, etc).

Task:

Designer a simple stabiliser circuit that will ensure that at all times the player has the specified voltage and current and under no conditions does any component suffer from overload.

3 About the unknowns:

a) I_1 ,

Assume D_2 is ideal,
i.e. $I_{ZK} = 0$.

so, $I_1 = I_L = 100\text{mA}$.

b) R_s

R_s is a series resistor whose function is to limit the current flowing in the Zener under no-load conditions.

Yet, 100mA must be available for the load under all conditions:- i.e., at $V_{i(\min)} = 11\text{V}$ to $V_{i(\max)} = 14.6\text{V}$

(inclusive).
On this range, the worst-case for sufficient I_1 is when $V_i = 11.0\text{V}$

Next,
calculate R_s for $V_{i(\min)}$.

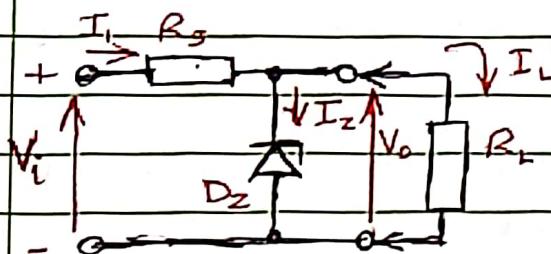


Fig Fz1

2 State the known facts:

$$V_i = 11 \text{ to } 14.6\text{V}$$

$$V_o = V_z = 7.5\text{V}$$

$$I_L = 100\text{mA}$$

$$R_s = \frac{V_{i(\min)} - V_z}{I_1}$$

$$= \frac{(11.0 - 7.5)\text{V}}{100 \text{ mA}}$$

$$= 35\Omega$$

**UGANDA BUSINESS AND TECHNICAL
EXAMINATIONS BOARD (UBTEB)**
November/December 2019 Examination Series

Page 7

Do not write in this margin

Do not write in this margin

Registration No..... Name of candidate:.....
Paper Code, Paper Name,
Relate (Tick in the Box) Candidate's Signature

the nearest preferred value (mfpv) is 33Ω .

$$S_{12}, I_2 = I_i = 215.1 \text{ mA}$$

and the Zener power (P_z) will be:

$$I_z \times V_z$$

$$= 215.1 \text{ (mA)} \times 7.5 \text{ (V)} \\ = 1.61 \text{ W.}$$

Next, check to see what I_i will

actually be if $R_s = 33\Omega$, is used.

$$I_i = \frac{V_i(\text{min}) - V_z}{R_s} \\ = \frac{11.0 - 7.5}{33} = 106 \text{ mA}$$

(c)

Selection of Component:

1) D_z

A 7.5V Zener diode

is req'd with a minimum power rating of 1.61W.

Because this is higher than 100 mA, 33Ω will be suitable.

From data sheets, Philips' BZX70C7V5 rated at 2.5W finishes off the Zener.

Next

What current will flow at $V_i(\text{max})$ conditions?

$$I_i = \frac{V_i(\text{max}) - V_z}{R_s}$$

$$= \frac{14.0 - 7.5}{33} = \frac{7.1}{33}$$

$$= 215.1 \text{ mA}$$

2) R_s (power rating)

Under worst-case conditions, 215.1 mA flows.

$$P = I^2 R$$

$$= [215.1 \text{ (mA)}]^2 \times 33 \text{ (\Omega)} \\ = 1.526 \text{ W.}$$

For D_z ,

the worst-case situation will occur when V_i is max the o/p is occ!

a 2-W mF resistor of 33Ω would do the job

UGANDA BUSINESS AND TECHNICAL EXAMINATIONS BOARD (UBTEB)

November/December 2019 Examination Series

Page 8

Do not
write
in this
margin

Registration No Name of candidate:

Paper Code Paper Name:

Retake (Tick In the Box) Candidate's Signature:

Do not
write
in this
margin

[B] The Zener Regulator

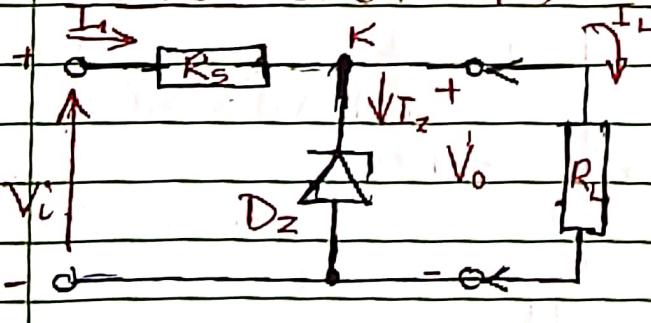
This is the most basic regulator (stabiliser) circuit.

- It comprises:

- a) a Zener diode D_z ,
- b) a series resistor R_s , and
- c) drives a resistive load R_L .
- d) is supplied from a rectified and filtered supply that may vary.

The purpose of the regulator is to maintain a rated stable o/p voltage, V_o (or V_L) at the o/p. - despite variations in the line voltage and/or load.

Circuit: (Example)



Assume the quotation that:

D_z is BZX79C6V8

which is 6.8V, 400mW,

$R_L = 390\Omega$ (resistive)

$N_{\text{L}} = 10 \text{ to } 13 \text{ V}$

We want to specify R_s .

i) Determine the load current I_L

$$I_L = V_o / R_L$$

and, here, $V_o = V_z$,

$$\text{so, } I_L = V_z / R_L$$

$$= \frac{6.8(\text{V})}{390(\Omega)} = 17.4 \text{ mA}$$

2) I_L can be assumed to be constant.

because R_L is fixed.

3) Determine the maximum Zener current $I_{z(\max)}$

$$I_{z(\max)} = \frac{P_z(\max)}{V_z}$$

$$= \frac{400(\text{mW})}{6.8(\text{V})} = 58.8 \text{ mA}$$

**BUSINESS AND TECHNICAL
EXAMINATIONS BOARD (UBTEB)**
November/December 2019 Examination Series

Page 9

Do not
write
in this
margin

Do not
write
in this
margin

Registration No Name of candidate:
Paper Code Paper Name
Retake (Tick in the Box) Candidate's Signature

- 4) Point K (cathode) forms a Kirchhoff node - at which Using $R_s = 120\Omega$, calculate I_z under $V_i(\max)$ conditions:

$$I_i = I_i + I_z$$

and $I_{z(\max)}$ flows when $R_L \rightarrow \infty$,
i.e. OCC op.
 $\Rightarrow I_L = 0$

so, $I_z = I_i$.

$$I_z = \frac{V_i(\max) - V_z}{R_s}$$

$$= \frac{13 - 6.8}{120}$$

$$= 51.6 \text{ mA}$$

- 5) R_s must limit the current so that under no-load conditions, it will not exceed 58.8 mA.

Realise here that if R_s was chosen as 100Ω , I_z would be 62mA, which is greater than 58.8 mA!

$$R_s = \frac{V_i(\max) - V_z}{I_{z(\max)}}$$

$$= \frac{13 - 6.8}{58.8} \text{ } \frac{\text{V}}{\text{mA}}$$

$$= 105.4 \Omega$$

6) It is necessary to know the Zener current under normal operating conditions when;

- a) V_i is at $V_{i(\min)}$ or 10V
b) V_i is at $V_{i(\max)}$ or 13V

7) The nearest preferred value (npv) of resistor to 105.4Ω is 120Ω - chosen upwards!

It should be noted that "normal" here implies that

- 8) Make a check at this point: I_L is constant at 17.4 mA

PTO

**UGANDA BUSINESS AND TECHNICAL
EXAMINATIONS BOARD (UBTEB)**
November/December 2019 Examination Series

Page 10

Do not write in this margin

Do not write in this margin

Registration No Name of candidate:
Paper Code. Paper Name
Retake (Tick in the Box) Candidate's Signature

a) When $V_i = 10V$,

$$I_1 = \frac{V_i - V_z}{R_s}$$

$$= \frac{10 - 6.8}{120} = 26.6mA$$

of R_s $0.5W$, at the minimum

By KCL,

$$I_z = I_1 - I_L$$

$$= 26.6 - 17.4$$

$$= 9.2mA$$

Zener diodes are also used as limiter or clipping circuits.

b) When $V_i = 13V$,

$$I_1 = \frac{V_i - V_z}{R_s}$$

$$= \frac{13 - 6.8}{120} = 51.6mA$$

$$I_z = 51.6 - 17.4$$

$$= 34.2mA$$

i) Power rating for R_s :

$$P = I^2 R$$

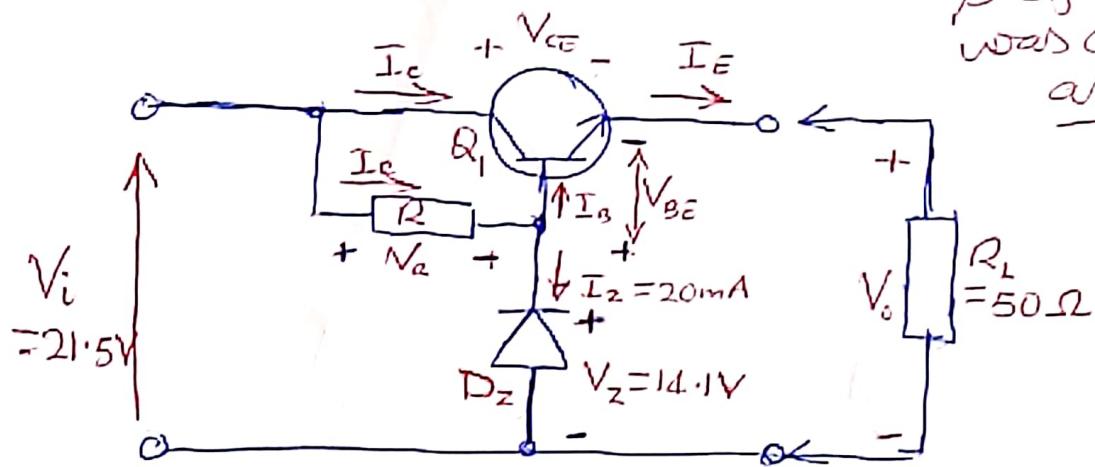
$$= [I_{(max)}]^2 \times R_s$$

$$= 51.6^2 \times 120 (\mu W)$$

$$= 0.319W$$

Suitable power rating

The Series Regulator



β of Q_1
was quoted
as 55

Load voltage V_L or V_L :

$$V_L = V_Z - V_{BE} \quad (\text{KVL}) \\ \approx 14.1 - 0.7 \\ = 13.4V$$

Load current I_L

$$I_L = I_E = V_L / R_L \\ \approx \frac{13.4}{50} = 268\text{mA}$$

$$I_B = \frac{I_c}{\beta} \approx \frac{I_e}{\beta} \\ = \frac{268}{55} \approx 4.8\text{mA}$$

$$I_R = I_B + I_Z \quad (\text{KCL}) \\ \approx 4.5 + 20 = 24.8\text{mA}$$

$$V_A = V_i - V_Z \\ = 21.5 - 14.1 \\ = 7.4V$$

$$R = V_R / I_R \\ = 7.4V / 24.8\text{(mA)}$$

$$\approx 302\Omega, 298.4\Omega \\ \# 330\Omega$$

Regulation

$$= \frac{V_{CE} - V_L}{V_L} \times 100\%$$

B_1