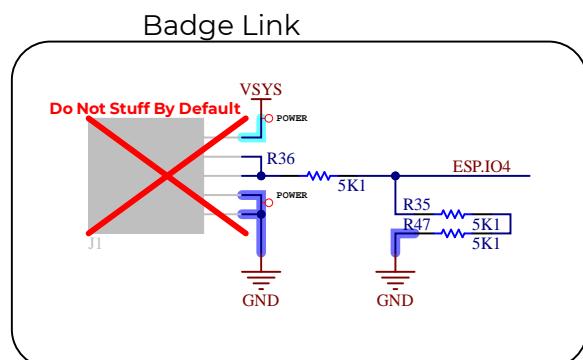
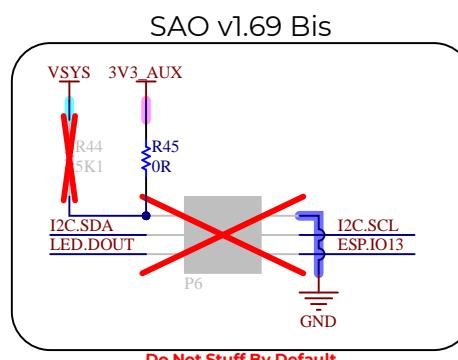
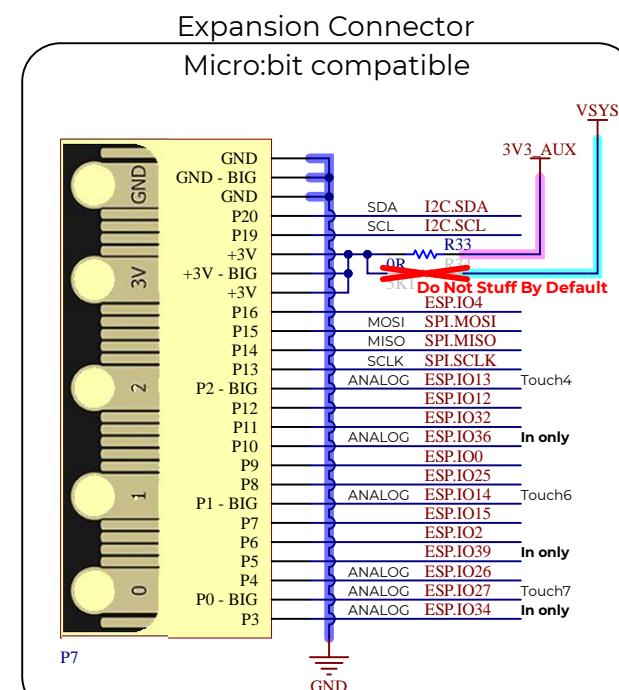
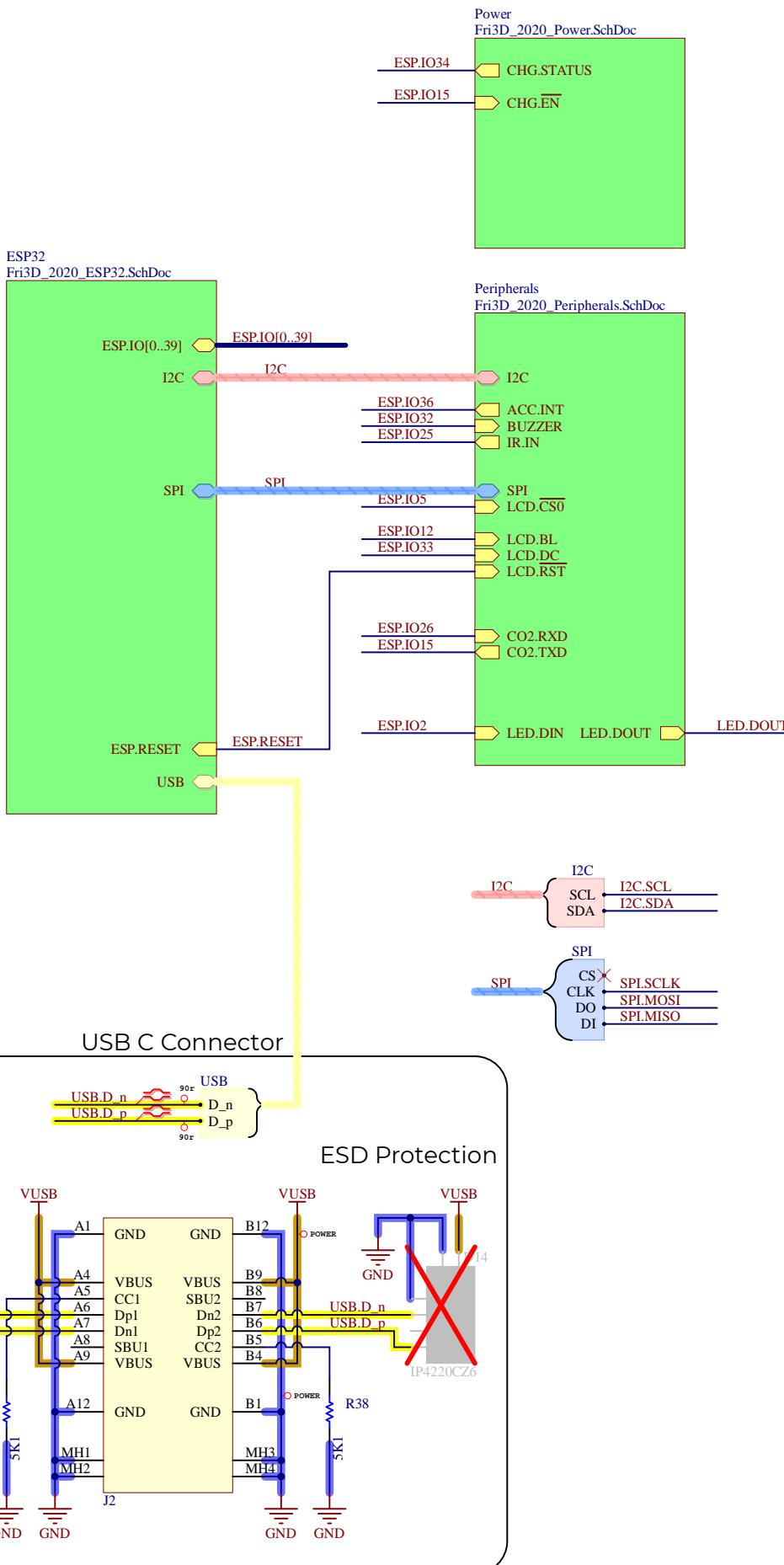
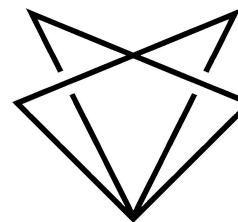


Rev. 00 : Initial release
 Rev. 01 : Micro:bit compatible
 Rev. 02 : Bugfixes, new shape

Fri3D Badge 2020

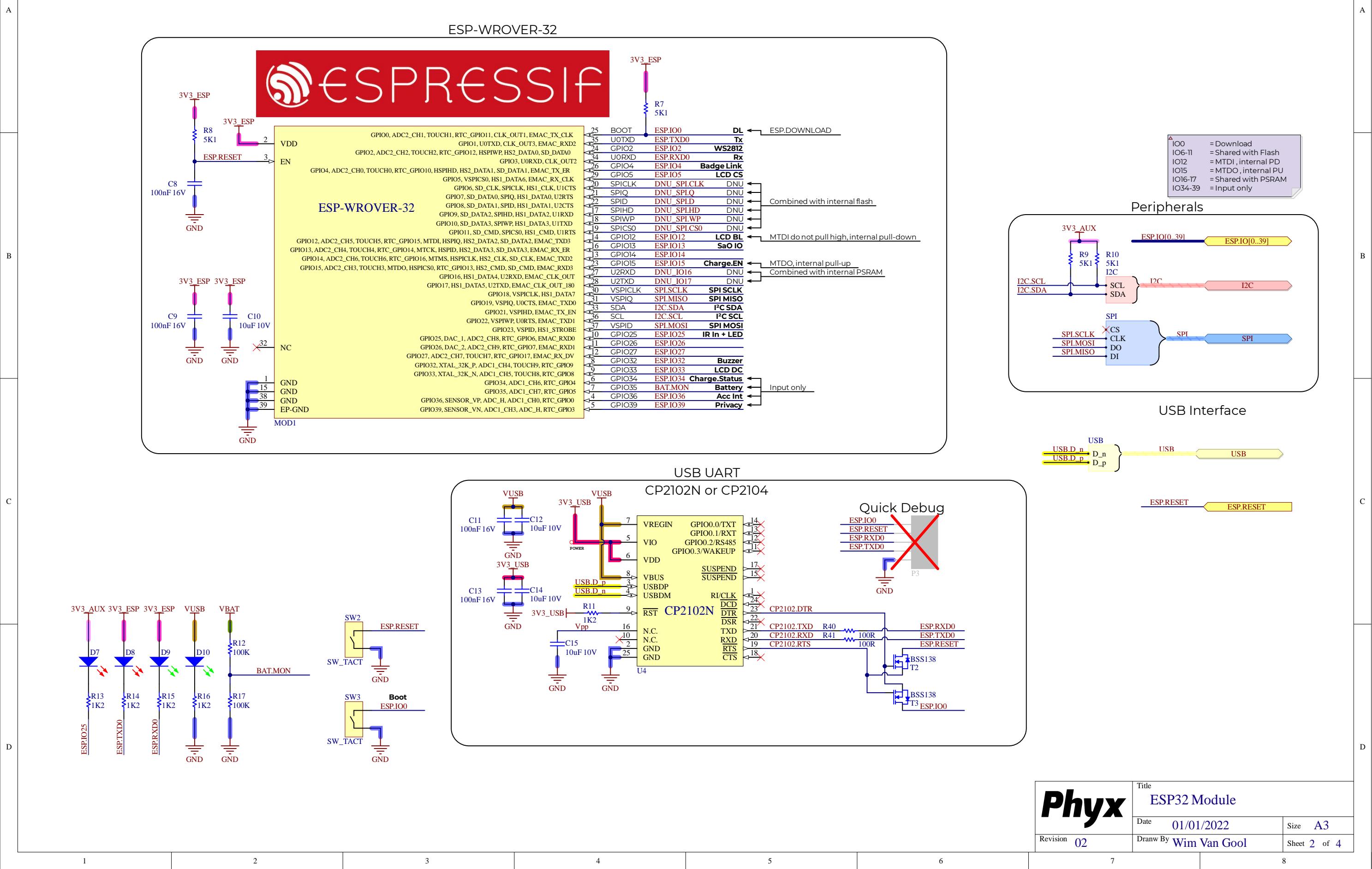
Top Level



IO0	= Download
IO6-7-8-11	= Shared with Flash
IO12	= MTDI , internal PD
IO15	= MTDO , internal PU
IO16-17	= Shared with PSRAM
IO34-39	= Input only

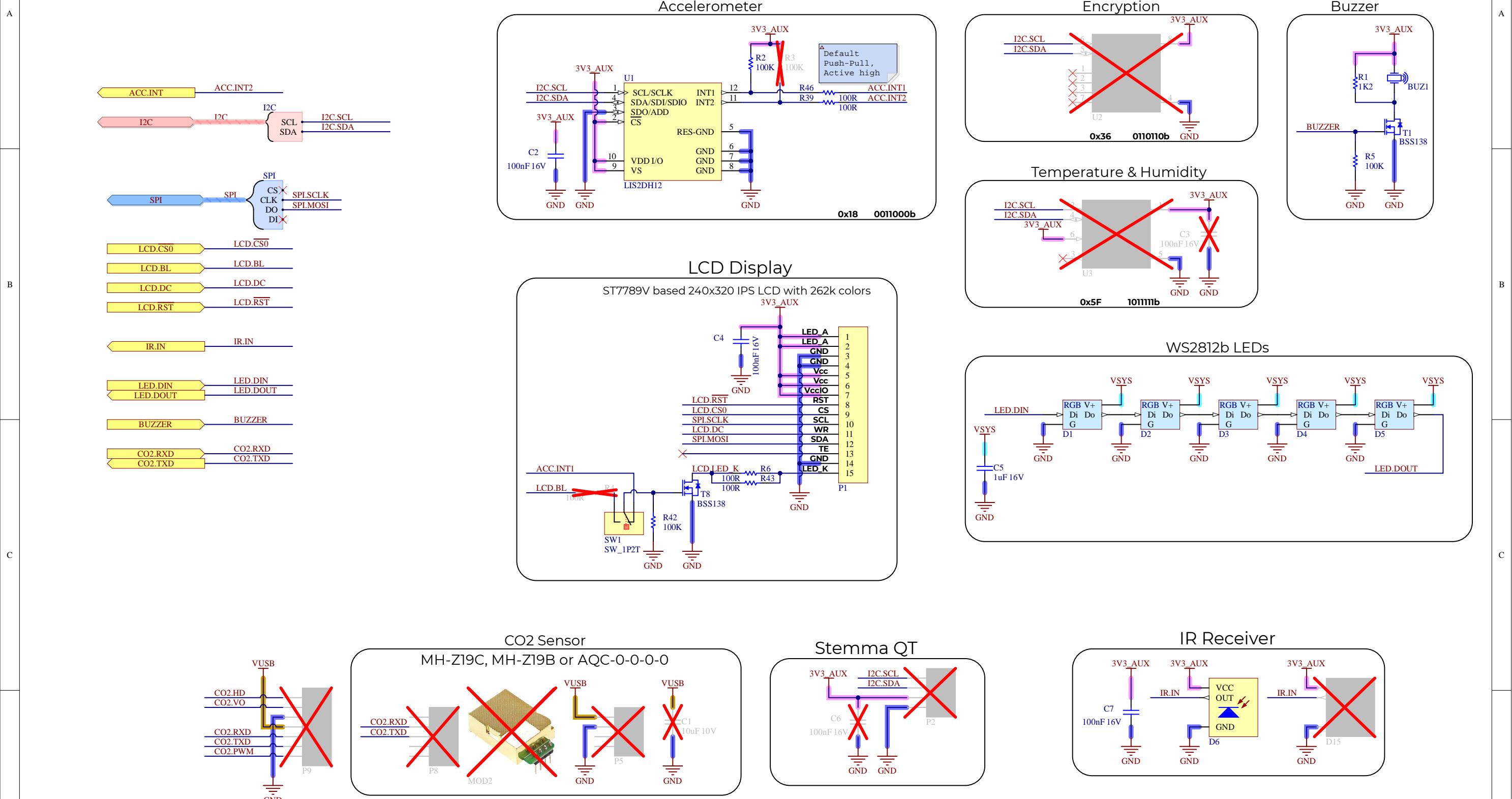
Fri3D Badge 2020

ESP32 Module



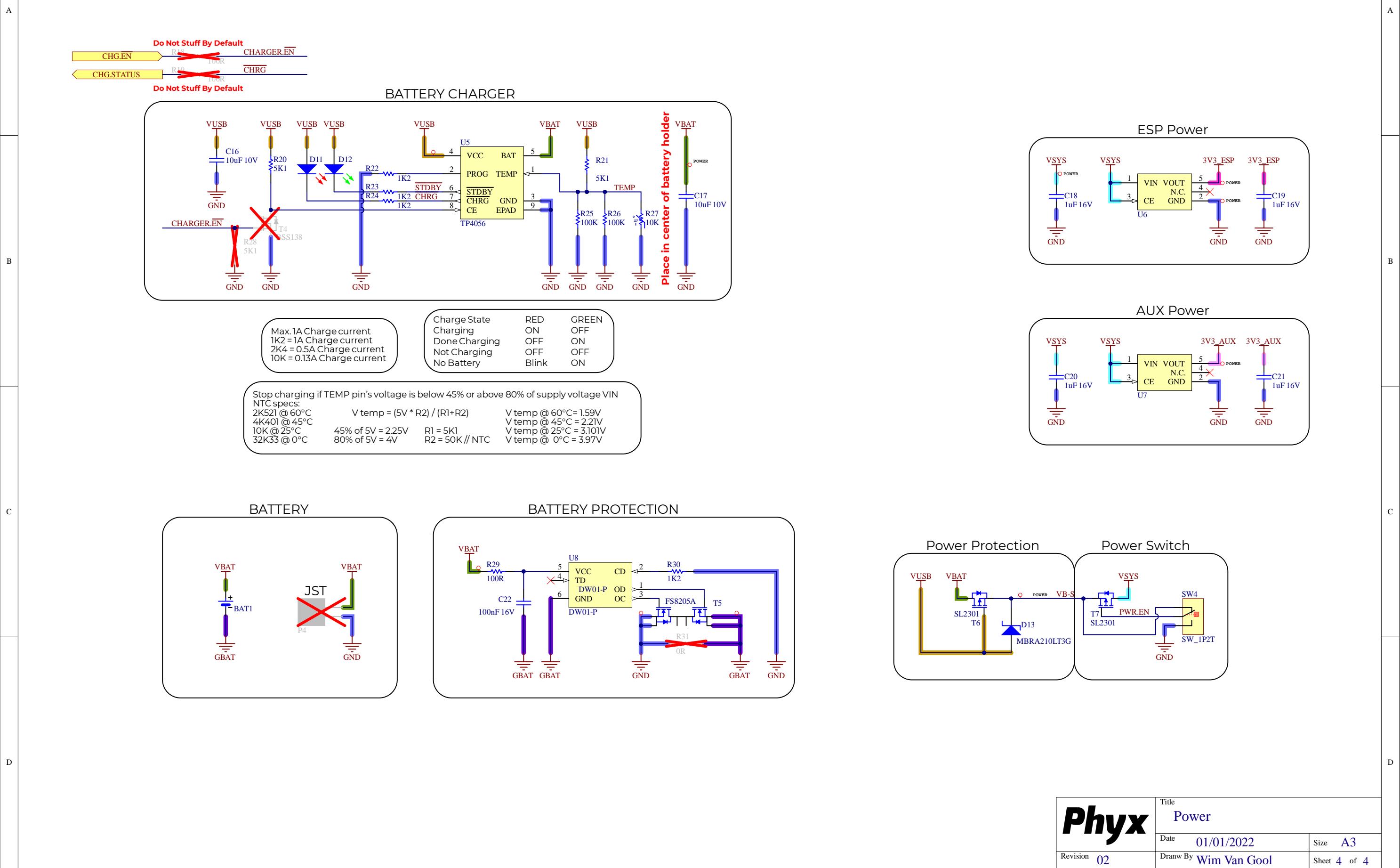
Fri3D Badge 2020

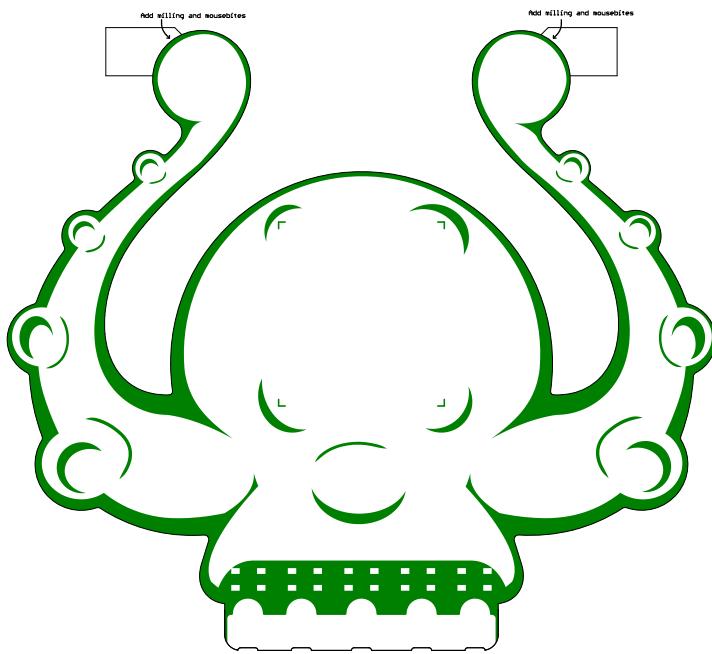
Peripherals



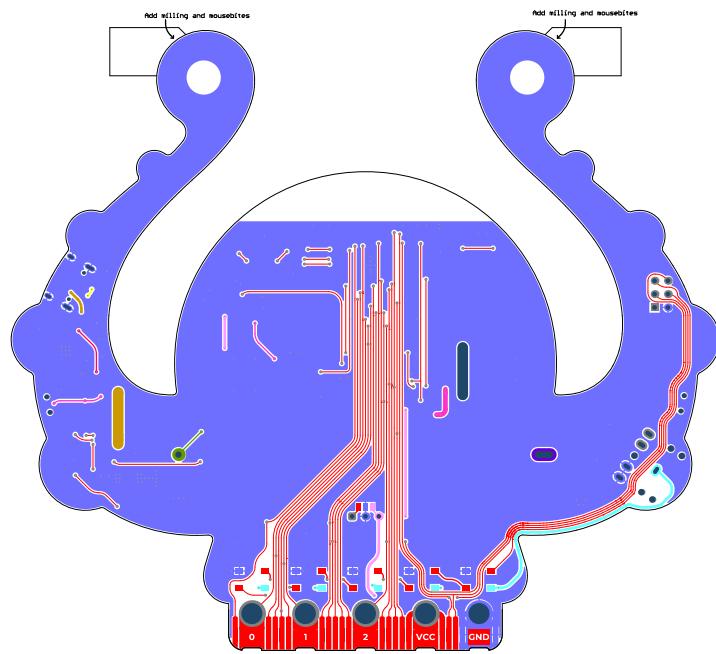
Fri3D Badge 2020

Power

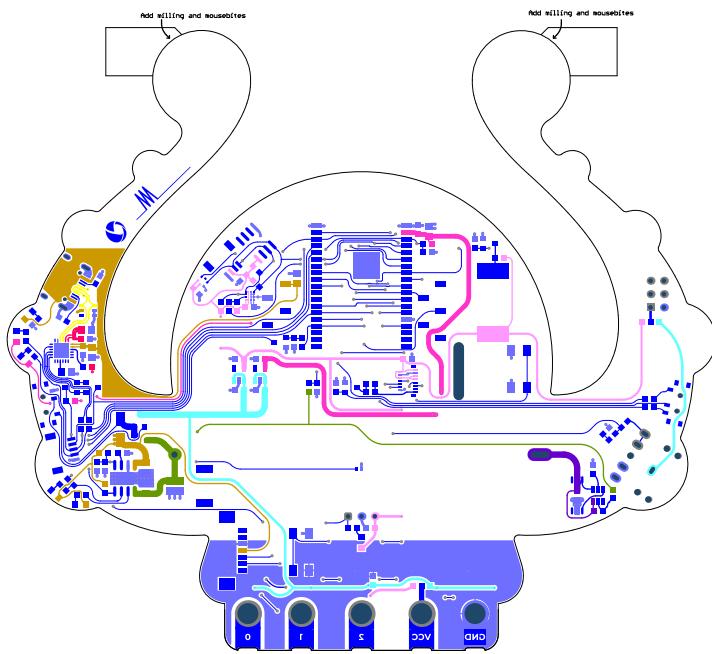




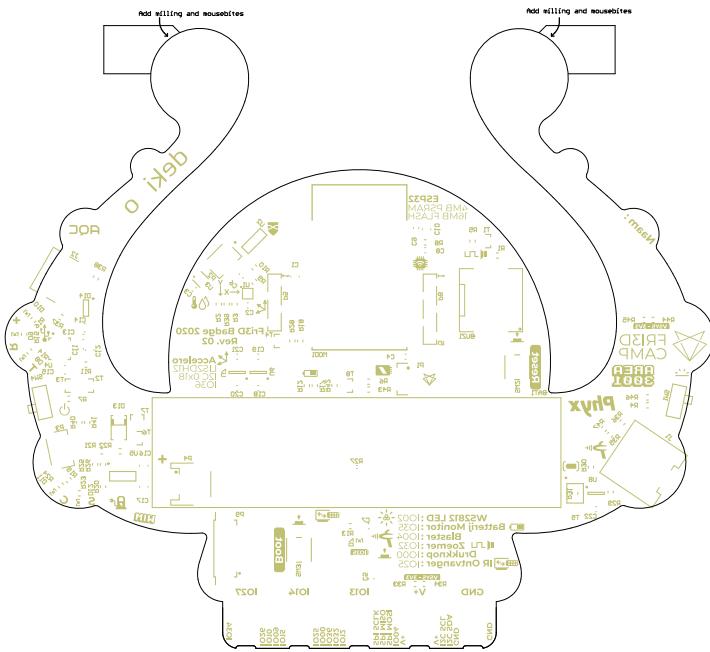
Document	Fri3D Badge 2020	
Layer Name	Top Overlay	
Date	09/01/2022	Rev 02
Designer	WimUG	Document
Fri3D_2020_02.PcbDoc		



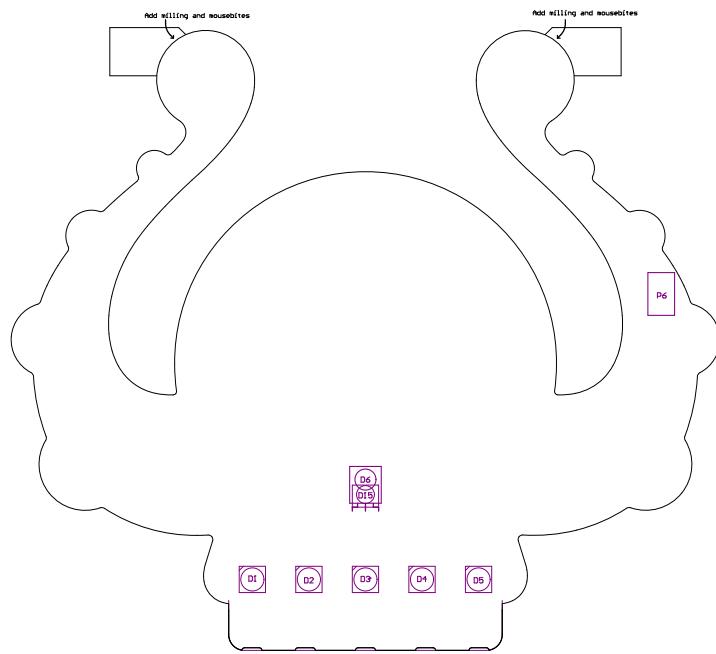
Phyx	Document	Fri3D Badge 2020
	Layer Name	Top Layer
	Date	09/01/2022
	Rev	02
Designer WimUG	Document	Fri3D_2020_02.PcbDoc



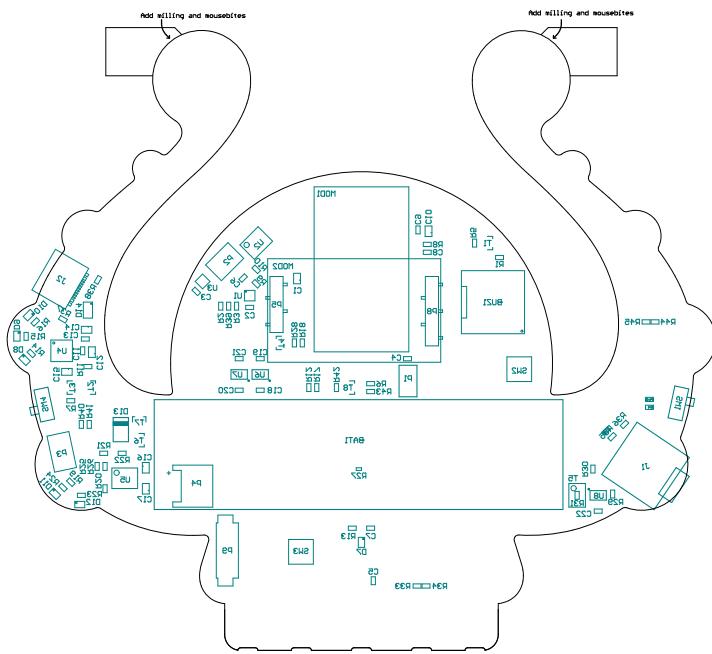
Phyx	Document	Fri3D Badge 2020
	Layer Name	Bottom Layer
	Date	09/01/2022
	Rev	02
Designer WimUG	Document	Fri3D_2020_02.PcbDoc



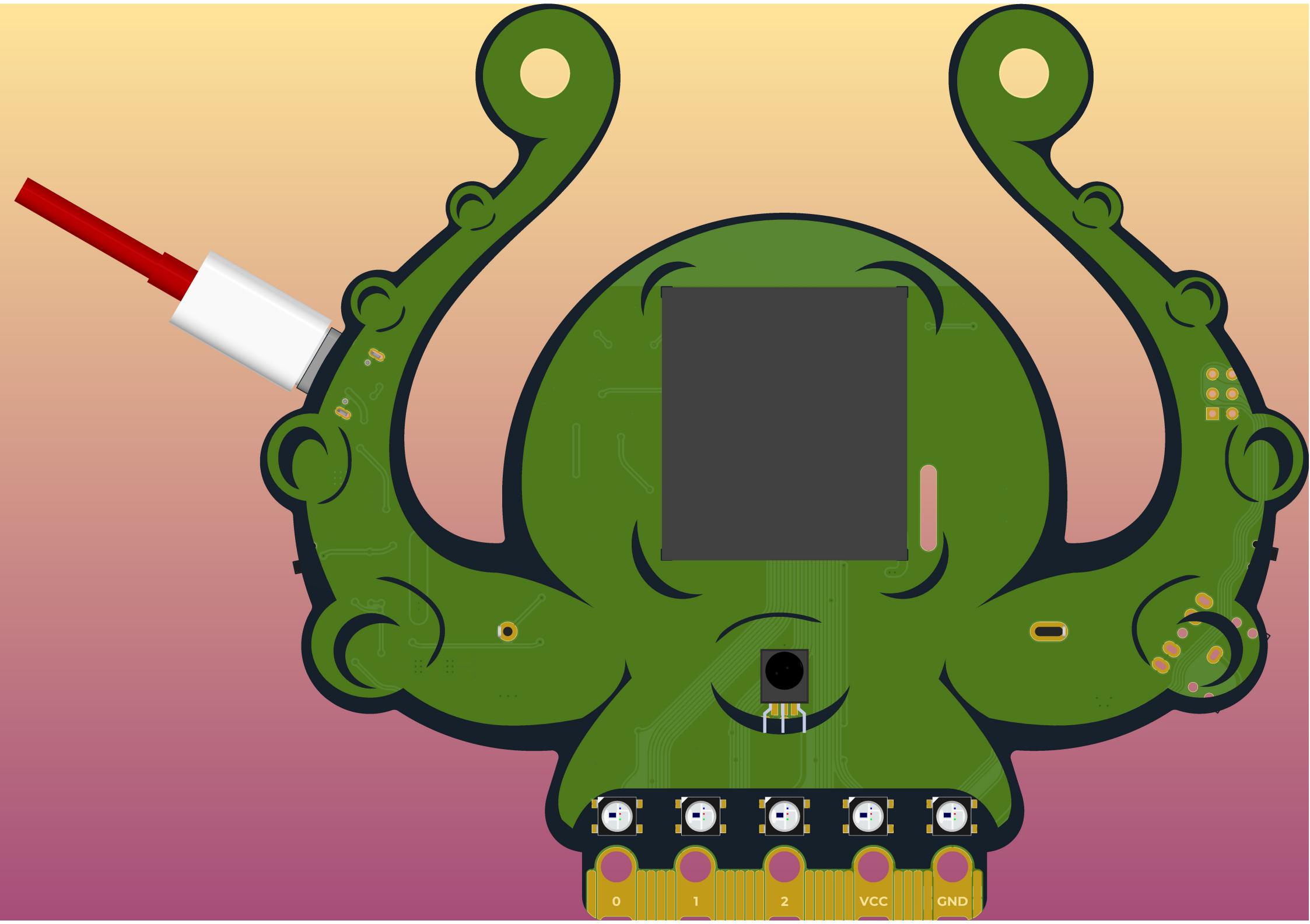
Document	Fri3D Badge 2020
Layer Name	Bottom Overlay
Date	09/01/2022
Rev	02
Designer	WimUG
Document	Fri3D_2020_02.PcbDoc



Phyx	Document	Fri3D Badge 2020
	Layer Name	Top Assembly
	Date	09/01/2022
	Rev	02
Designer WimUG	Document	Fri3D_2020_02.PcbDoc



Phyx	Document	Fri3D Badge 2020
	Layer Name	Bottom Assembly
	Date	09/01/2022
	Rev	02
Designer WimUG	Document	Fri3D_2020_02.PcbDoc



0 1 2 VCC GND

