EE102

ENDSEM REPORT

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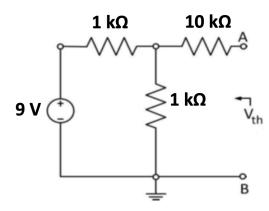
ROLL NO: 200101015

Objective:

Implement the following circuit and verify the *Thevinin's theorem* as seen from terminals A-B.

Deliverable:

Make a written report about the experiment giving details of estimation for V_{Th} and R_{Th} . Upload the same on a link created on MS-Teams portal for End-sem exam report before the deadline communicated to you.



Theoretical Calculations

o Thevenin's Voltage: that would the voltage developed.

between open terminal AhB. Since its open,

the voltage is same as developed across the

this IK resistor. (Since no current flows through

lok 2 resistor, Vn-VA = (0) con 2)

QV + VA VX W 10k2 A SIKD OV OV

$$J = QV = ((IK + IK) - \Sigma) \cdot I$$

$$I = \frac{q}{2} M A$$

True frevenir Voltage \VII = 4.5 V

· Therenin's Resistance

and voltage

-> Short circuiting independent current, Sources

The 2 two Ins are in parallel In their Combination is in series with lokes.

RTH = 10.5KJZ

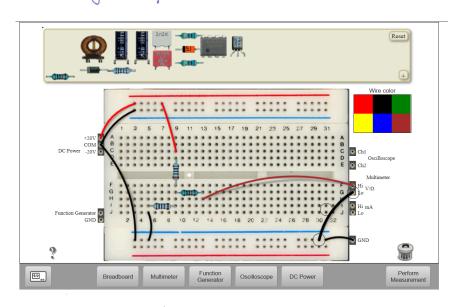
Observations

from Experiment

•		(mc/		
	Voc (in V)	Isc(in MA)	VTH=Voc (inV)	RTHE ISC (in KSZ)
	4.459	407.3	4.459	10.95

Screenshots:

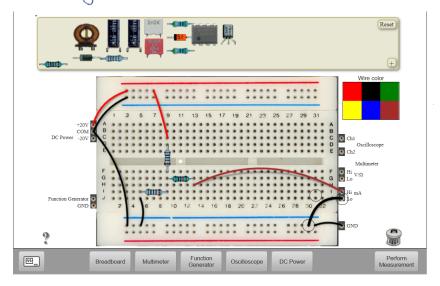
Measuring Voltage







Measuring Current





Results

Prepretical values of VTn=4.5V, RTH=10.5 KSL

Experimental values => VTn = 4.45av, RTH = 10.95 KSL

Trus the values are quite close, and the slight difference ruight be due to the unaccounted resistance of wires, current, source's internal resistance or the multimeter's resistance.