TinyK22
Rev A

Lucerne University of
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HOCHSCHULE
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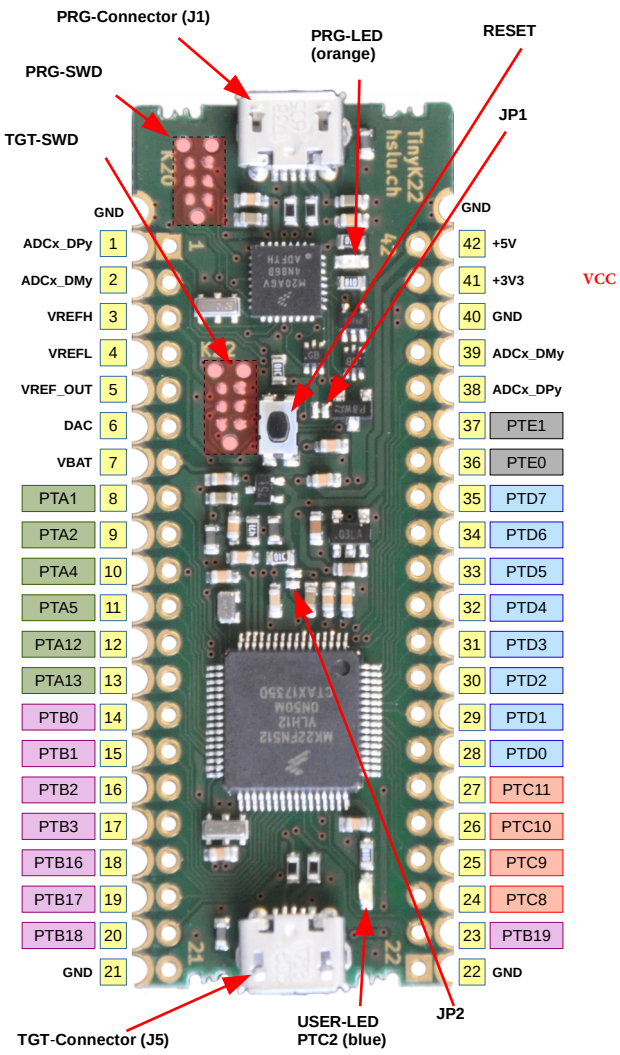
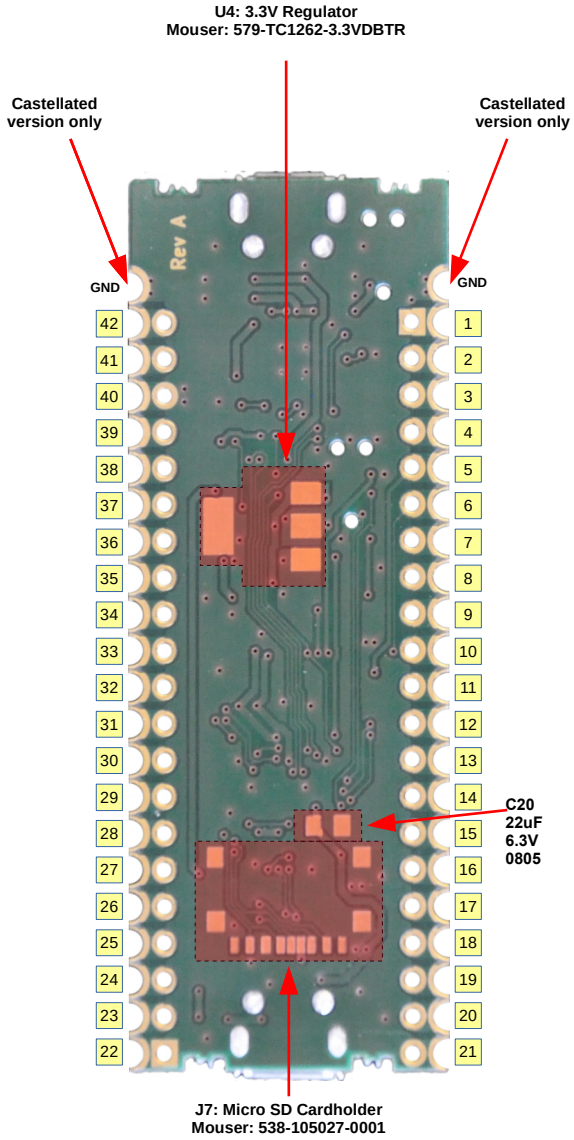
Technik & Architektur





	LPUART0	UART0	UART1	UART2	FTM0	FTM1	FTM2	FTM3	I2C0	I2C1	SPI0	SPI1	ADC0	ADC1	CMP0
PTA1		R			6										
PTA2		T			7										
PTA4					1										
PTA5					2										
PTA12						0									
PTA13						1									
PTB0						0			C				8	8	
PTB1						1			D				9	9	
PTB2					3				C				12	12	
PTB3					0				D				13	13	
PTB16		R										O			
PTB17		T										I			
PTB18							0								
PTB19							1								
PTC8								4						4b	2
PTC9								5						5b	3
PTC10								6		C				6b	
PTC11								7		D				7b	
PTD0								0							
PTD1								1			C		5b		
PTD2	R			R				2	C		O				
PTD3	T			T				3	D		I				
PTD4					4										
PTD5					5							C	6b		
PTD6		R			6							O	7b		
PTD7		T			7							I			
PTE0			T							C				4a	
PTE1			R							D		I/O		5a	

UART R:RxD, T:TxD
FTM 0..7: Channel
I2C C:SCL, D:SDA
SPI C:Clock, O:SOUT, I:SIN
ADC 4..13:Channel
CMP 2..3: Input

UART to Programmer:
UART1 or LPUART0
(PTC3 & PTC4)

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