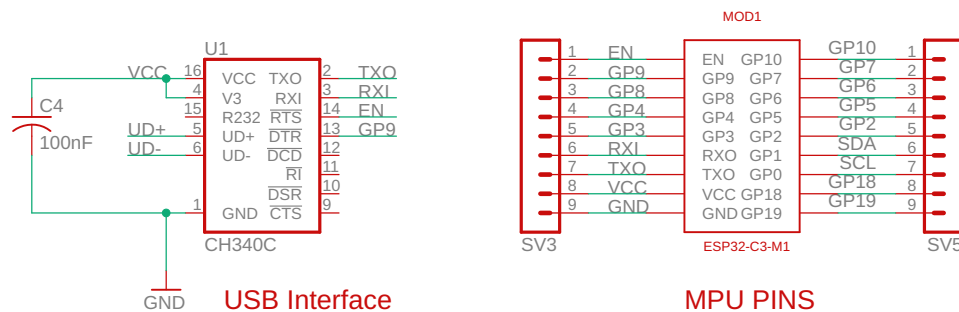
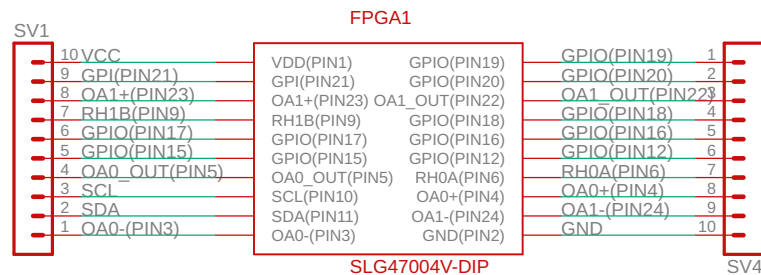
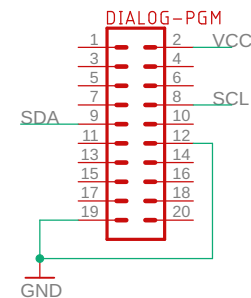


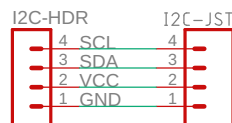
Note: Additional schematic sheets are available. What's on this sheet will probably be declared "the core" and pushed to another sheet to make room for custom circuitry here.



MPU PINS



FPGA PINS



<< CHECK THIS  
does it match the "SparkFun standard"?



open hardware

TITLE: aardvark-v0.80		Project: Aardvark
Description:		
TriEmbed Community Project Espressif ESP32-C3-M1(4M) plus Dialog SLG47004V-DIP		
Author, license: Triangle Embedded Interest Group		REV:
Repo: <a href="https://github.com/triembed/aardvark">https://github.com/triembed/aardvark</a>		0.80
Date: 2/6/22 5:28 PM		Sheet: 1/1