

```

1 ## makefile for cases that
2 ## makefile is upper directory of all source files, such as
3 ## .
4 ## makefile
5 ## ./src -----> '*.cpp'
6 ## ./obj -----> '*.o'
7 ## ./include ----> '*.h'
8
9 ## run command: './a' or 'make run'
10 ## for an explanation of ':=' , which is a variant of '='.
11
12 CXX := g++
13 CFLAGS := -Wall -g
14 TARGET := a
15
16 SRC_DIR = src
17 OBJ_DIR = obj
18
19 # $(wildcard $(SRC_DIR)/*.cpp /xxx/xxx/*.cpp): get all .cpp files from the
   '$(SRC_DIR)' directory and dir "/xxx/xxx/"
20 SRCS := $(wildcard $(SRC_DIR)/*.cpp)
21 # Change the list of Cpp source files into a list of object files by replacing
   the '.cpp'
22 # suffix with '.o' in the result, like this:
23 OBJS := $(patsubst $(SRC_DIR)/%.cpp, $(OBJ_DIR)/%.o,$(SRCS))
24
25 ## the include path
26 INC = -Iinclude
27 VPATH = ./:include
28
29
30 all: $(TARGET)
31
32 $(TARGET): $(OBJS)
33     $(CXX) -o $@ $^
34
35 ## for each using '%', not '*'
36 ## '${INC}' is used to add the '*.h' file
37 $(OBJS): $(OBJ_DIR)/%.o: $(SRC_DIR)/%.cpp
38     $(CXX) -c $(CFLAGS) ${INC} $< -o $@
39
40 run:
41     ./a
42
43 .PHONY: all clean
44
45 clean:
46     rm -rf $(OBJ_DIR)/*.o
47
48 # $^
49 # The names of all the prerequisites, with spaces between them.
50
51 # $@

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52 # The file name of the target of the rule. If the target is an archive member,
    # then '$@' is the name of the archive file. In a pattern rule that has multiple
    # targets (see Introduction to Pattern Rules),
53 # '$@' is the name of whichever target caused the rule's recipe to be run.
54
55 # $%
56 # The target member name, when the target is an archive member. See Archives.
    # For example, if the target is foo.a(bar.o) then '$%' is bar.o and '$@' is
    # foo.a.
57 # '$%' is empty when the target is not an archive member.
58
59 # $<
60 # The name of the first prerequisite. If the target got its recipe from an
    # implicit rule,
61 # this will be the first prerequisite added by the implicit rule (see Implicit
    # Rules).
62
63 # $*
64 # The stem with which an implicit rule matches (see How Patterns Match). If
    # the target is dir/a.foo.b and the target pattern is a.%.b then the stem is
    # dir/foo. The stem is useful for constructing names of related files.
65
66 # In a static pattern rule, the stem is part of the file name that matched the
    # '%' in the target pattern.
```