It creates the child process by cloning from the parent process, including all user-space data: PCICRO reg], Brognam code [File 8] The praces is changing the code that is executing and never return to the original code. Will replace: Program Code, Memory (Local unglobal v, dynamically allocated manory), pequalue (PC) However, the kernel-space info is preserved 1 PID, Macess relocationally suspend the calling process to waiting state and return when pointer to my povent (List of children) A Amay of opened file-presenu 1. Clear local v. 8 dynamically also cated memory, reset global is based 2. The pracess return from mount) Afilequest Jelhanily destroy the child process in the ternol space) 3. deregister the remove node from UP exiti) turns a process into a zombie when a process calls exiti) 2. The process return from main!) corresponding signal handling routine is invoked (remove the signal, However, DO NOT CLONE Return value of Forter), PID, Parent 1, register a signal handling routine 2. When SIGCHID comes, the), copy ternel space 2 update Lernel space (PID, Running time, [Avii]=[Freepesawes] 3. notify the parent with SIGCHID < state: Zomble only has pid plot all nodes Unfinished wont() inside the Lernel ·one of its child (running > terminated) . signal is received on new code, charge constants to the new program code. The ternel will also reset the register value (PC) Memory], Memory, Opened files [Kernals internal] 3 Types of Mode Transfer-"[Unprogrammed], exit() inside the ternel
System Call, Interrupt, Trap or Exception 2. clean up the exit process's User space memory
Nultithreaded Processes Earlier - return immediately if it has no child 3. copy user space 4. return value exect) inside the kemel process, Running time, File locks forki) inside the ternel 2. Lond PC, 5P and register from new state block 1. Save Pc, Stand register in convent state system has the ability to access certain resowces. • The Os and the hardware, How switch from one QU to the next addiess space and "Imore thread of control rectal layer of software that provides application Thread-single wrighe execution context. Dual made operation / Protection - Child the memory space of the physical machine another by controlling the translation are protected from user programs and user programmes one isolated from one process. In instance of an executing Address space . Programs execute in an address spine that is distrinct from the oftware access to hondware resources: The fight describes program state convient abstraction of complex handware device. P.C. Register, Execution Flogs, Stark majorn is a pracess consistent of an TOWN THIS WAY OF THIS TO from program virtual addresses to PC, 5P(Stack Painter), Register CPU need structure to hold machine physical addresses abstract, away most low-lavel details of system call modice, free, form, filose system call show, check open, follow, that, rundir, chawn, chund, open, follow. .hmod, getpid, panse, pipe. read, signal, stat. whiten call configure bernel or build things briver handle the interaction Provide abstractions to apps (File systems; Monage resources (Memory, CRV, storage) that is System call certel monoge all the physical devices thell render a simple command line potos segment Text segment Constants stack & Heap 1 Communication among logical entities Protected access to shared sawces Process, threads; UM, containeds) security and authentication exposed by the kernel me, wait, write houses · is a function call Global variables - Local variables Program code Dynamically Allocated Constants とおいっと 5 includes Memory

turn = other; while (turn = = other 88 interested Lother); → A Low priority process L is inside the critical region, but a high provity process H gets the CPU and want to enter the critical region. But H cannot lock (since L has not unlock). Producer—consumer Es: uoid producer 1 int item; while (trive) | item= produce_item|); MF: Loid philosopher (int i) 1 think(); take(i); eat(1); puta); J if(*5<0) fanable_interrupt();slegi(); disoble-interrupt(); HF: Lond coptain(int i) 1 if(state[i] = H & state[L]!= E & state int other; other=1-praces; interested[process]=7RUE; A Mortual Exclusion of Bounded Waiting & Pregress and posts semaphore +6) 1 disable-intermetal); *\$= **5+1; oid wait (sempthone *5) 1 disolde-interrupt(); *5=*5-1; Diving philosopher (LEFT (19+N-1)%N) Right (18+1)%N) Limbto oncorests Valiffer from oribrity inversion. Mutual Exclusion . Hold and Wait . No preemption uoid unlock (int process) | interested[process] = FALSE :] woit (8 mutes); item= remove_item(); post(8 mutes); 46: Usid take (int i) 1 wait (8 mutes); state [i] = Hungry; void consumer fint item; while (true) swatch & Fill); 48: Lord put (int 1) 1 wait 18 mutex; state [2] = Think; captain(LEFT); captain(RIGHT); post (Smintes);) wait (Ravai)); wait (Rinutex); insert-itemlitem); 40: int state[N]; semaphore multex=1, p[N]=0 in tours int interested [2] = 1 FALSE, FALSE? of (*s <0) wake-up(); enable-interrupt(); h captain(d); post(Armites); wait(Aptis); } 40: semaphore martex =1; auxil=N, fill=0; [R]!=E)15tate[v]=E; post 18p(v]);} post (Rmuteu); post(&fill); 4 3 1. Spin-based lock 2. Spin Smorter 3, Semaphore Four requirement for DeadLock void lock (int process) 1 enoble - interrupt (); } post (Qavail) : 77 THE STANTER the code segment that alless shared object. or he designed for accessing more than one Phock I Worting some praces: TCB, reg. PC, SP | Multiprogramming A>B.

Threading Model

The addition -> Sauce - diff praces: PCB, file-descriptor

The scheduler decides to schedule another pracess in One-to-One, Many-to-One, Many-to-Many

To scheduler decides to schedule another pracess in One-to-One, Many-to-One, Many-to-One, Many-to-Many

The scheduler decides to schedule another pracess in One-to-One, Many-to-One, The autcome of the computation depends on the Sys Time: CPU time spent on codes in leanelspace mem Multithreading = Multiple threads per praessocies Unfeculde Responsiveness of the praces is greater, not freenhand exhibit solveness of the process read state for the praces is greater, not freenhand exhibit is a requirement shaded of the praces is greater, not freenhand exhibit is a requirement shaded of race spaces for the process is greater, not free free or the process of the praces of race spaces that a red from that a race that a real than that a race that a race that a race that a race that a red that a race t changes to memory and IIO address tables pend most of time in CRU, user-time > sys-time [AI]. Happen whenever showed object + multiple pand mass [real-time > usertime + systime] · switching thread within a block is simple User Time: CPU trime spent on codes in veekspace mem. Multiprogramming = Multipe Jobs/Process he ready quese. Then the schedule has to load the IR shored Objects < pipe system antest of that process from the main memory to CRU shored Objects < semaphore - user eswitching thread across Idades requires Multiprocessing = Multiple CPUs Entry and ext implamentation Achaining Mutual Exclusion · PCB point to imultiple TCB Multiprocessing (B -> pend most of thme in IIO, sus-time > User-time 1'16'] process + concurrently Multithead, Halases spensive]: . Save and restore reg . Switch address space Message Pausing . coche, buffer cache & TLB misses Signal shared object Thread Woot PC is paintaing to the region (code) when in terme! reads the PID of the pracess from the Jernel . ON switches from wearspove to termal space, it switches back to the userspace memory, Thrend Metadata containule running program code Soved Reg variable Global Coole