YULIANG FU

Tsinghua University \diamond Beijing, 100084, China fu_yl@outlook.com \diamond fuyl20@mails.tsinghua.edu.cn \diamond

BRIEF BIO

Yuliang Fu is a third-year undergraduate student at Tsinghua University. He is double-majoring in Mathematics and Physics & Electrical Engineering and Automation. Since April 2022, he has been serving as an undergraduate research assistant in Pervasive HCI Lab. at Tsinghua University, supervised by Prof. Yuntao Wang. His current research interest lies in the field of Human-Computer Interaction, Ubiquitous Computing and Mobile Sensing. During his undergraduate study he has been outstanding and has been awarded with multiple awards and fellowships.

EDUCATION

Tsinghua University, Beijing, China

Aug, 2020 – Jul, 2024

BS in Mathematics and Physics + **BSE** in Electrical Engineering and Automation

- GPA: 3.87/4.0
- Secretary of Students' Science Association, Tsinghua University Weiyang College

Core Courses

• Linear Algebra (4.0/4.0), Introduction to Complex Analysis (4.0/4.0), Probability and Stochastic Processes (4.0/4.0), Fundamentals of Computer Program Design (4.0/4.0),

SCHOLARSHIPS & AWARDS

• 2021 Tsinghua Comprehensive Excellence Award

Sponsored by Scholarship of Weiyang College, Tsinghua University

2021 Tsinghua Innovation Award of Science and Technology

Sponsored by Scholarship of Tsinghua University

RESEARCH INTEREST

Fields

Human-Computer Interaction, Ubiquitous/Pervasive Computing, Mobile Sensing, IoT

RESEARCH EXPERIENCE

Pervasive HCI Group Tsinghua University, Beijing, China

Department of Computer Science and Technology

Apr., 2022 - present

Research Assistant, Advisor: Prof. Yuntao Wang

Project: Acouslink: an acoustic ranging method for home AIoT devices(Ongoing)

- Provide indoor location service utilizing ultrasonic waves
- Calibration-free method with good precision and range
- Suitable for tracking moving objects with high velocities
- Packaged unit, easy to use

SKILLSET

Programming Python, C/C++, SQL, MATLAB

Embedded System Nordic nRF52 series, TI MSP430, STM32 series

Hardware Design Verilog HDL

LANGUAGE SKILLS

Chinese Mother tongue

English TOEFL iBT 107/120