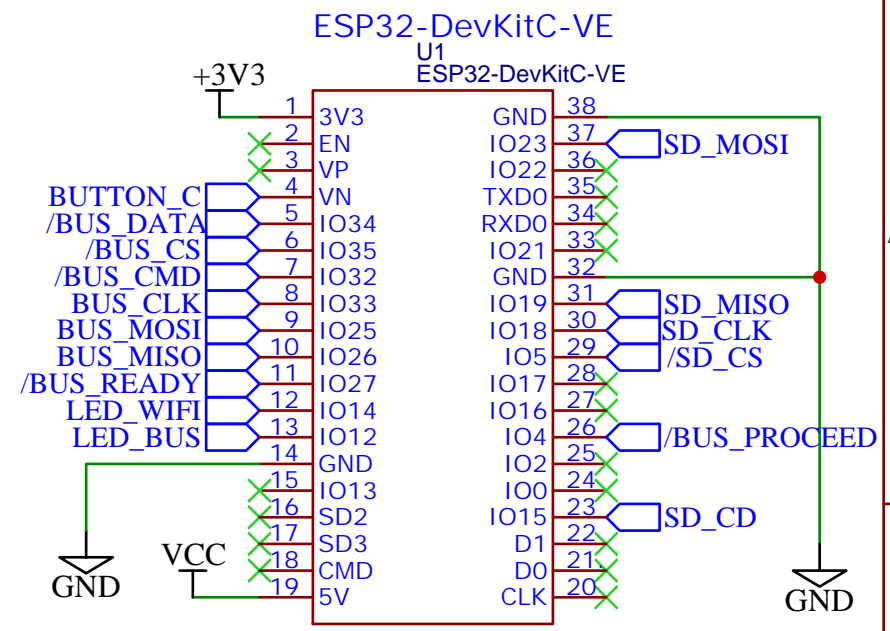
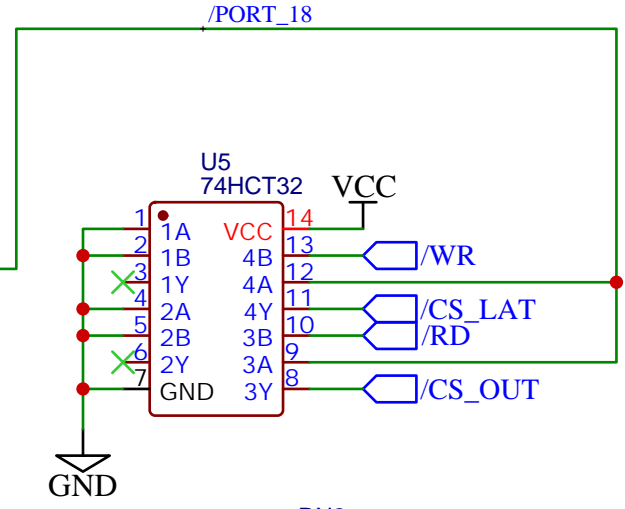
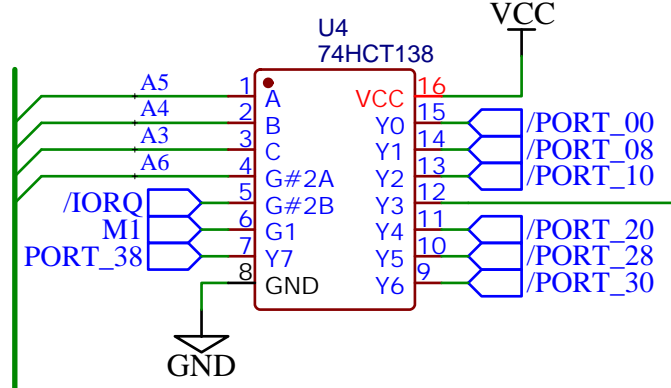
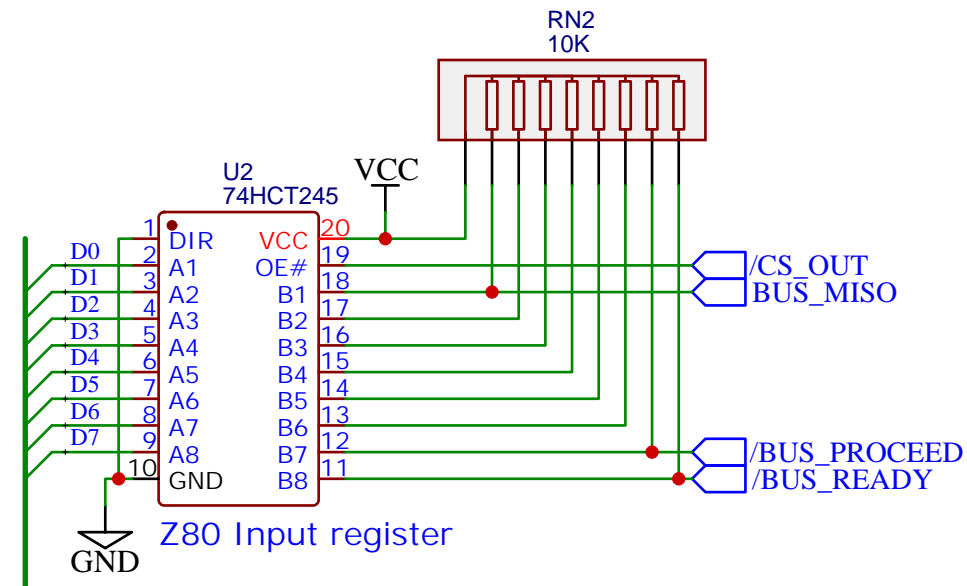
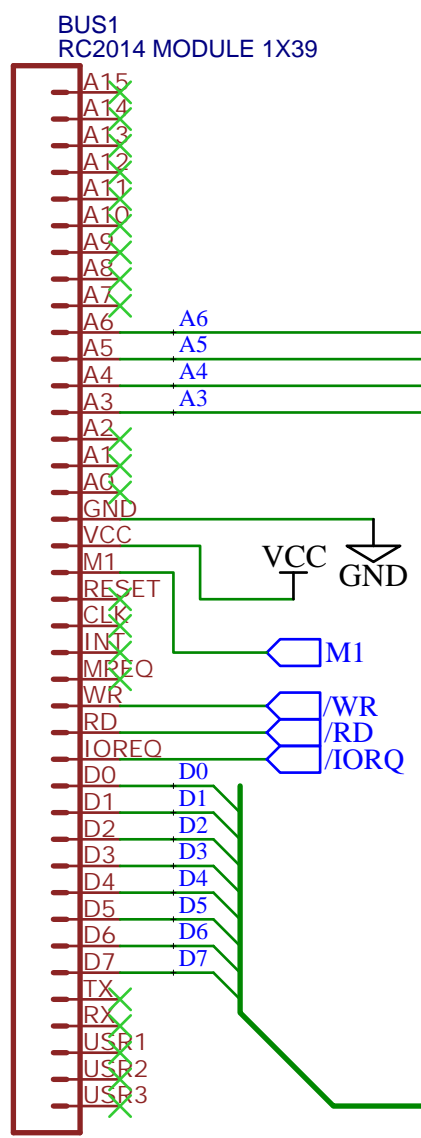
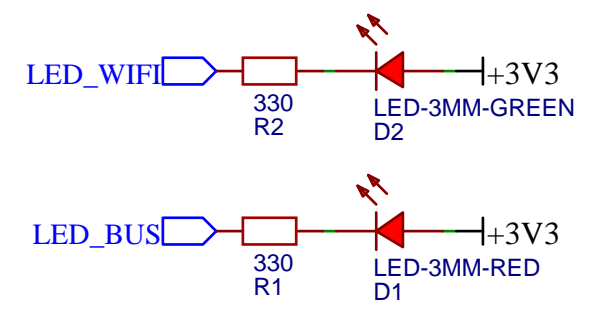


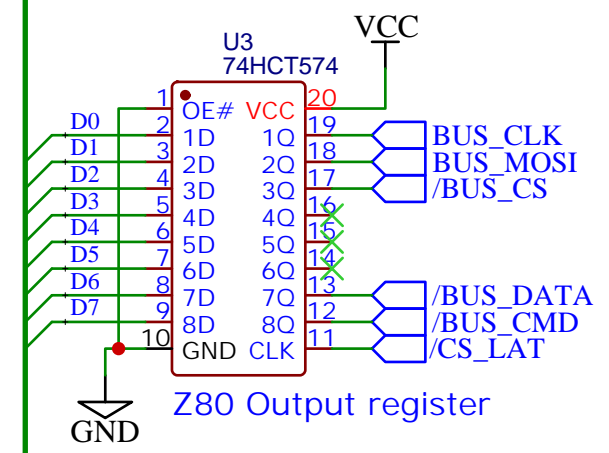
Decoupling: place close to ICs



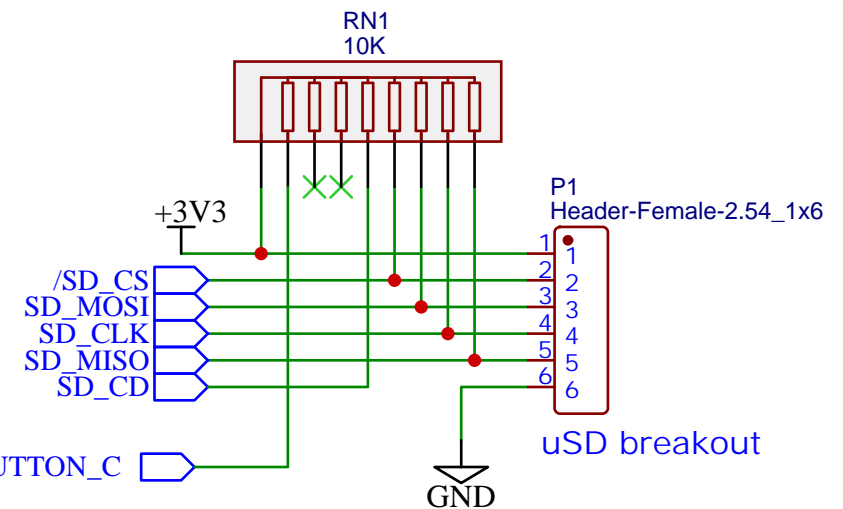
Pins are unofficially 5v tolerant



Z80 Input register



Z80 Output register



Note: Physical BUTTON_C not present, so pull up

TITLE: FujiNet RC2014 SPI		REV: 0.3A
Company: @jskists		Sheet: 1/1
Date: 2023-05-21		Drawn By: Justin Skists