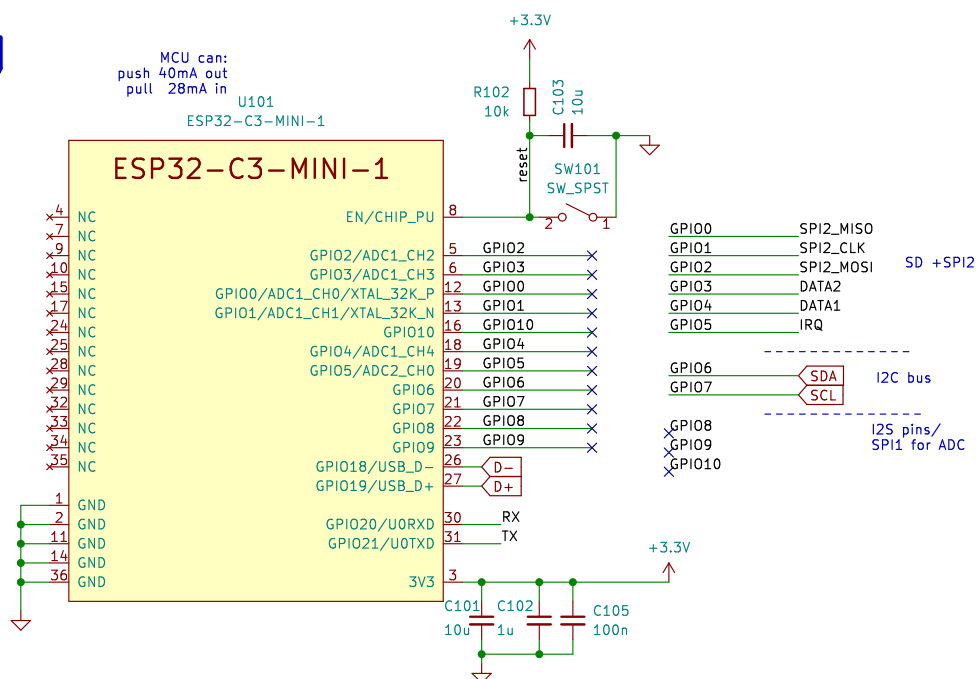


https://www.espressif.com/sites/default/files/documentation/esp32-c3_hardware_design_guidelines_en.pdf
antenna is on the left
pull up/down resistors are 45k

MCU can:
push 40mA out
pull 28mA in

pull 28mA in U101
ESP32-C3-MINI-1

ESP32-C3-MINI-1

[illegible]

SD CARD ADAPTER
J103
MEM2051-00-195-00-A

3.3V

DATA2 P1

SPI2_SS2 DATA3 P2

SPI2_MOSI P3

SPI2_CLK P4

SPI2_MISO DATA0 P7

DATA1 P8

MP1

MP2

MP3

MP4

MP5

R113
10k

SD_sense

S101
DS01C-254-S-08BE

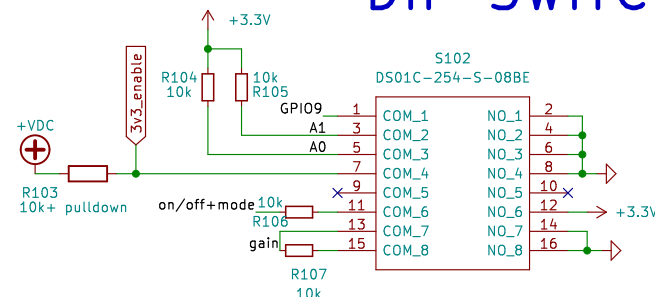
1	COM_1	2	NO_1	3	×
4	×	5	NO_2	6	×
7	COM_3	8	NO_3	9	×
10	COM_4	11	NO_4	12	×
13	COM_5	14	NO_5	15	×
16	COM_6	17	NO_6	18	×
19	COM_7	20	NO_7	21	×
22	COM_8	23	NO_8	24	×

SPI1_MISO 1
GPIO8 4
GPIO8 7
SPI1_CLK 10
SPI1_MOSI 13
GPIO10 16

COM_1 2
COM_2 5
COM_3 8
COM_4 11
COM_5 14
COM_6 17
COM_7 20
COM_8 23

NO_1 3
NO_2 6
NO_3 9
NO_4 12
NO_5 15
NO_6 18
NO_7 21
NO_8 24

GPIO8 4
DATA_IN 7
GPIO9 10
BCLK 13
GPIO10 16
LRC 19



The schematic diagram shows the MCP3202 ADC circuit. The +VDC supply is connected to the Vref pin of the ADC through a 1k resistor R101 and a 100nF capacitor C104. The Vref pin is also connected to the +VDC supply through a 100nF capacitor C106. The Vdd pin of the ADC is connected to the +VDC supply through a 100nF capacitor C106. The ADC is configured with Vref connected to the +VDC supply through R101 and C104, and Vdd connected to the +VDC supply through C106. The ADC output is connected to the SPI interface (SP, SCLK, DIN, DOUT) of the microcontroller.

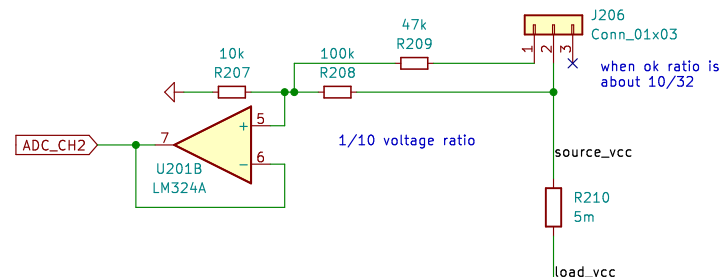
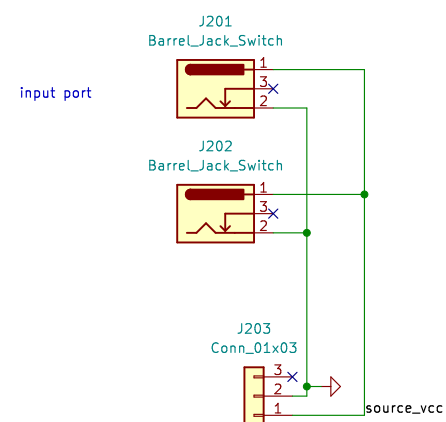
[illegible]

Title: ESP32 audio power meter SD datalogger

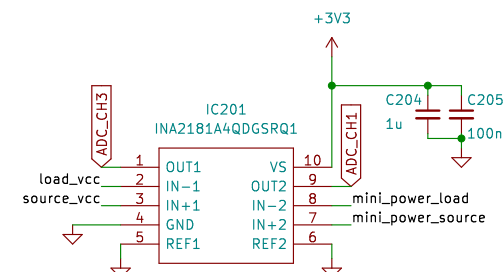
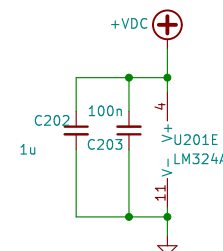
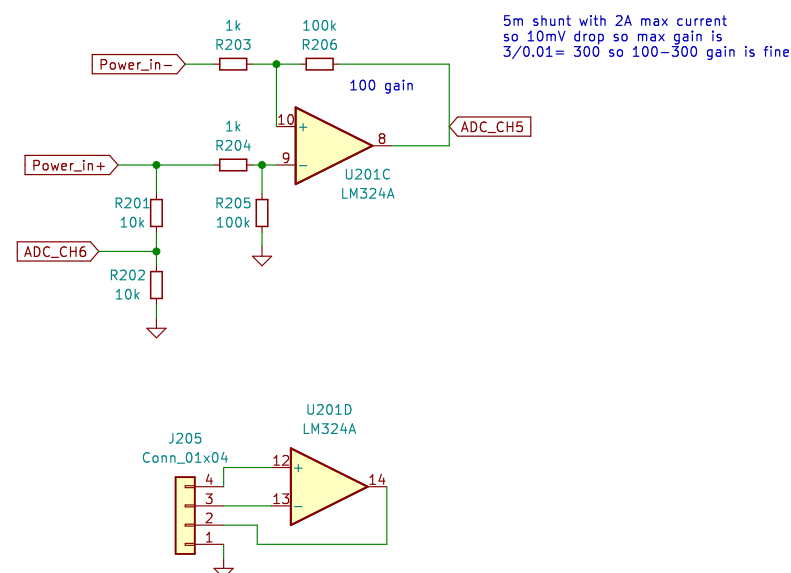
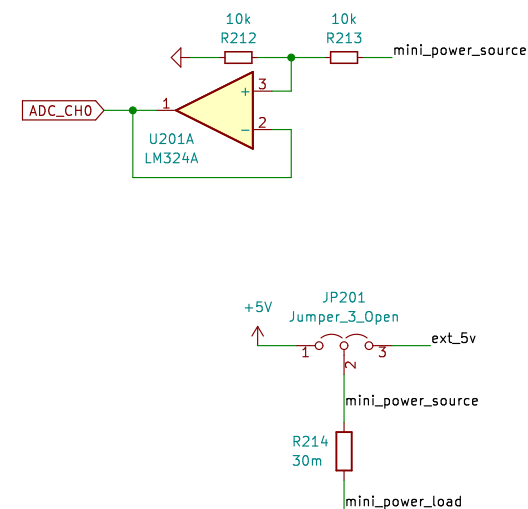
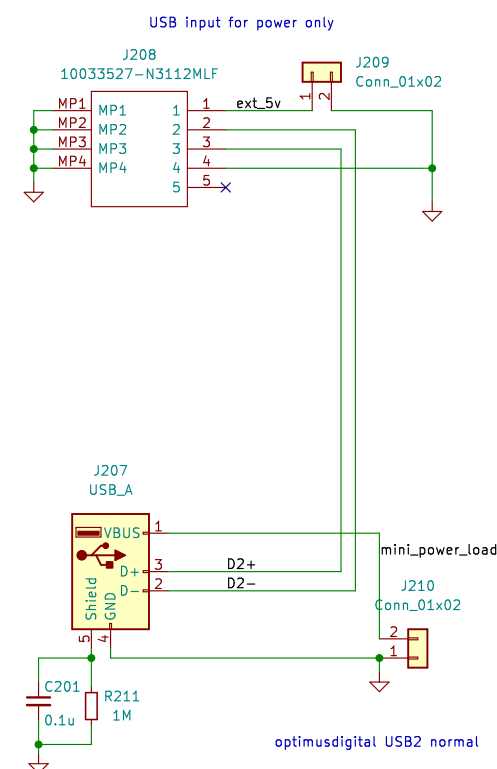
Size: A3	Date:
KiCad E.D.A. kicad (6.0.4)	

Rev: 1
Id: 1/4

LARGE CURRENT 24V 3A MAX



SMALL CURRENT 5V OR LESS



Sheet: /POWER_MEASUREMENTS/
File: untitled.kicad_sch

Title: ESP32 audio power meter SD datalogger

Size: A3
KiCad E.D.A. kicad (6.0.4)

Date:

Rev: 1

Id: 2/4

Power

