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1  ┌────────────────────────── MODULE 2PCwithBTM ───────────────────────────┐
2  EXTENDS Integers, Sequences, FiniteSets, TLC
3  CONSTANT RM,      The set of participating resource managers  $RM = 1 \dots 3$ 
4      RMMAYFAIL,
5      TMMAYFAIL  Whether TM may fail  $MAYFAIL = \text{TRUE}$  or  $\text{FALSE}$ 
6
7  *****
8  A modified version of P2TCommit at http://lamport.azurewebsites.net/tla/two-phase.html
9  Transaction manager (TM) is added.
10 *****
11 --algorithm TransactionCommit {
12   variable rmState = [ $rm \in RM \mapsto \text{"working"}$ ],
13           tmState = "init" ;
14   define {
15     canCommit  $\triangleq \forall rmc \in RM : rmState[rmc] \in \{\text{"prepared"}\}$ 
16                $\vee \exists rm \in RM : rmState[rm] \in \{\text{"committed"}\}$  for when BTM takes over
17     canAbort  $\triangleq \exists rm \in RM : rmState[rm] \in \{\text{"aborted"}, \text{"failed"}\}$ 
18                $\wedge \neg \exists rmc \in RM : rmState[rmc] = \text{"committed"}$  inconsistent if commented
19   }
20   macro Prepare( p ) {
21     await rmState[p] = "working" ;
22     rmState[p] := "prepared" ; }
23
24   macro Decide( p ) {
25     either { await tmState = "commit" ;
26             rmState[p] := "committed" ; }
27
28     or { await rmState[p] = "working"  $\vee$  tmState = "abort" ;
29          rmState[p] := "aborted" ; }
30   }
31
32   macro Fail( p ) {
33     if ( RMMAYFAIL ) rmState[p] := "failed" ;
34   }
35
36   fair process ( RManager  $\in RM$  ) {
37     RS: while ( rmState[self]  $\in \{\text{"working"}, \text{"prepared"}\}$  ) {
38       either Prepare(self) or Decide(self) or Fail(self) }
39   }
40
41   fair process ( TManager = 0 ) {
42     TS: either { await canCommit ;
43                TC: tmState := "commit" ;
44                F1: if ( TMMAYFAIL ) tmState := "hidden" ; }
45
46     or { await canAbort ;
47          TA: tmState := "abort" ;
48          F2: if ( TMMAYFAIL ) tmState := "hidden" ; }
49   }

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51  fair process ( BTManager = 10 ) {
52    BTS: either { await canCommit ∧ tmState = "hidden" ;
53                BTC: tmState := "commit" ; }

55    or { await canAbort ∧ tmState = "hidden" ;
56         BTA: tmState := "abort" ; }
57  }
58 }

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*****

62  BEGIN TRANSLATION
63  VARIABLES rmState, tmState, pc

65  define statement
66  canCommit  $\triangleq$      $\forall rmc \in RM : rmState[rmc] \in \{ \text{"prepared"} \}$ 
67                    $\vee \exists rm \in RM : rmState[rm] \in \{ \text{"committed"} \}$ 
68  canAbort  $\triangleq$      $\exists rm \in RM : rmState[rm] \in \{ \text{"aborted"}, \text{"failed"} \}$ 
69                    $\wedge \neg \exists rmc \in RM : rmState[rmc] = \text{"committed"}$ 

72  vars  $\triangleq$   $\langle rmState, tmState, pc \rangle$ 

74  ProcSet  $\triangleq$   $(RM) \cup \{0\} \cup \{10\}$ 

76  Init  $\triangleq$  Global variables
77            $\wedge rmState = [rm \in RM \mapsto \text{"working"}]$ 
78            $\wedge tmState = \text{"init"}$ 
79            $\wedge pc = [self \in ProcSet \mapsto \text{CASE } self \in RM \rightarrow \text{"RS"}$ 
80                                      $\square \quad self = 0 \rightarrow \text{"TS"}$ 
81                                      $\square \quad self = 10 \rightarrow \text{"BTS"}]$ 

83  RS(self)  $\triangleq$   $\wedge pc[self] = \text{"RS"}$ 
84                $\wedge \text{IF } rmState[self] \in \{ \text{"working"}, \text{"prepared"} \}$ 
85               THEN  $\wedge \vee \wedge rmState[self] = \text{"working"}$ 
86                    $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"prepared"}]$ 
87                $\vee \wedge \vee \wedge tmState = \text{"commit"}$ 
88                    $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"committed"}]$ 
89                $\vee \wedge rmState[self] = \text{"working"} \vee tmState = \text{"abort"}$ 
90                    $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"aborted"}]$ 
91                $\vee \wedge \text{IF } RMMAYFAIL \wedge \neg \exists rm \in RM : rmState[rm] = \text{"failed"}$ 
92                   THEN  $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"failed"}]$ 
93                   ELSE  $\wedge \text{TRUE}$ 
94                    $\wedge \text{UNCHANGED } rmState$ 
95                $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"RS"}]$ 
96  ELSE  $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}]$ 
97   $\wedge \text{UNCHANGED } rmState$ 

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98 $\wedge \text{UNCHANGED } tmState$
 100 $RManager(self) \triangleq RS(self)$
 102 $TS \triangleq \wedge pc[0] = \text{"TS"}$
 103 $\wedge \vee \wedge canCommit$
 104 $\wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TC"}]$
 105 $\vee \wedge canAbort$
 106 $\wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TA"}]$
 107 $\wedge \text{UNCHANGED } \langle rmState, tmState \rangle$
 109 $TC \triangleq \wedge pc[0] = \text{"TC"}$
 110 $\wedge tmState' = \text{"commit"}$
 111 $\wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F1"}]$
 112 $\wedge \text{UNCHANGED } rmState$
 114 $F1 \triangleq \wedge pc[0] = \text{"F1"}$
 115 $\wedge \text{IF } TMMAYFAIL$
 116 $\quad \text{THEN } \wedge tmState' = \text{"hidden"}$
 117 $\quad \text{ELSE } \wedge \text{TRUE}$
 118 $\quad \wedge \text{UNCHANGED } tmState$
 119 $\wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}]$
 120 $\wedge \text{UNCHANGED } rmState$
 122 $TA \triangleq \wedge pc[0] = \text{"TA"}$
 123 $\wedge tmState' = \text{"abort"}$
 124 $\wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F2"}]$
 125 $\wedge \text{UNCHANGED } rmState$
 127 $F2 \triangleq \wedge pc[0] = \text{"F2"}$
 128 $\wedge \text{IF } TMMAYFAIL$
 129 $\quad \text{THEN } \wedge tmState' = \text{"hidden"}$
 130 $\quad \text{ELSE } \wedge \text{TRUE}$
 131 $\quad \wedge \text{UNCHANGED } tmState$
 132 $\wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}]$
 133 $\wedge \text{UNCHANGED } rmState$
 135 $TManager \triangleq TS \vee TC \vee F1 \vee TA \vee F2$
 137 $BTS \triangleq \wedge pc[10] = \text{"BTS"}$
 138 $\wedge \vee \wedge canCommit \wedge tmState = \text{"hidden"}$
 139 $\wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"BTC"}]$
 140 $\vee \wedge canAbort \wedge tmState = \text{"hidden"}$
 141 $\wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"BTA"}]$
 142 $\wedge \text{UNCHANGED } \langle rmState, tmState \rangle$
 144 $BTC \triangleq \wedge pc[10] = \text{"BTC"}$
 145 $\wedge tmState' = \text{"commit"}$

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146       $\wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"Done"}]$ 
147       $\wedge \text{UNCHANGED } rmState$ 

149   $BTA \triangleq \wedge pc[10] = \text{"BTA"}$ 
150       $\wedge tmState' = \text{"abort"}$ 
151       $\wedge pc' = [pc \text{ EXCEPT } ![10] = \text{"Done"}]$ 
152       $\wedge \text{UNCHANGED } rmState$ 

154   $BTManager \triangleq BTS \vee BTC \vee BTA$ 

156   $Next \triangleq TManager \vee BTManager$ 
157       $\vee (\exists self \in RM : RManager(self))$ 
158       $\vee \text{Disjunct to prevent deadlock on termination}$ 
159       $((\forall self \in ProcSet : pc[self] = \text{"Done"}) \wedge \text{UNCHANGED } vars)$ 

161   $Spec \triangleq \wedge Init \wedge \square [Next]_{vars}$ 
162       $\wedge \forall self \in RM : WF_{vars}(RManager(self))$ 
163       $\wedge WF_{vars}(TManager)$ 
164       $\wedge WF_{vars}(BTManager)$ 

166   $Termination \triangleq \diamond (\forall self \in ProcSet : pc[self] = \text{"Done"})$ 

168  END TRANSLATION

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The invariants:

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173   $TypeOK \triangleq$ 
    The type-correctness invariant
177       $\wedge rmState \in [RM \rightarrow \{\text{"working"}, \text{"prepared"}, \text{"committed"}, \text{"aborted"}, \text{"failed"}\}]$ 
178       $\wedge tmState \in \{\text{"init"}, \text{"commit"}, \text{"abort"}, \text{"hidden"}\}$ 

180   $Consistency \triangleq$ 
    A state predicate asserting that two  $RM$ s have not arrived at conflicting decisions.
185       $\forall rm1, rm2 \in RM : \neg \wedge rmState[rm1] = \text{"aborted"}$ 
186       $\wedge rmState[rm2] = \text{"committed"}$ 

189   $NotCommitted \triangleq \forall rm \in RM : rmState[rm] \neq \text{"committed"}$ 

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191  \ * Modification History
    \ * Last modified Wed Dec 13 14:34:34 EST 2017 by mad
    \ * Last modified Fri Nov 17 12:18:24 EST 2017 by murat
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