

**Bachelor of Science (B.Sc. I.T.) Semester–I (C.B.S.) Examination****SYSTEM ANALYSIS DESIGN****Paper–III**

Time : Three Hours]

[Maximum Marks : 50

- N.B. :—** (1) **All** questions are compulsory.  
 (2) All questions carry equal marks.  
 (3) Draw neat and labelled diagram wherever necessary.

**1. EITHER**

- (a) Explain Brainstorming and Document Search techniques. 5  
 (b) Explain the concept of system and subsystem. 5

**OR**

- (c) What is Feasibility study ? Explain different types of feasibility studies. 5  
 (d) Explain the phases of SDLC with suitable diagram. 5

**2. EITHER**

- (a) Write short notes on input and output design. 5  
 (b) What is a form ? What are the principles of form design ? 5

**OR**

- (c) Explain principles of Code Design. 5  
 (d) Explain Decision tree with suitable example. 5

**3. EITHER**

- (a) Explain Cold Turkey and Parallel Conversion Method. 5  
 (b) Explain System Evaluation in detail. 5

**OR**

- (c) Explain Operational Training in detail. 5  
 (d) What is Testing ? Explain Test Evaluation and Acceptance in detail. 5

**4. EITHER**

- (a) Write short notes on :  
     (i) Software Reliability  
     (ii) Software Quality 5

- (b) Explain Maintenance Process Models. 5

**OR**

- (c) Explain Basic Issues in Software Reuse. 5  
 (d) Explain any one Project Estimation Technique with example. 5

**5. Attempt all :**

- (a) Explain Role of System Analyst. 2½  
 (b) Explain Data Dictionary with suitable example. 2½  
 (c) Explain change strategies in detail. 2½  
 (d) Explain Risk Management. 2½