

**Bachelor of Science (B.Sc. I.T.) Semester-I (C.B.S.) Examination**

**SYSTEM ANALYSIS AND DESIGN**

**Paper—III**

Time : Three Hours]

[Maximum Marks : 50

**Note :—** All questions are compulsory and carry equal marks.

**EITHER**

1. (a) What is SDLC ? Explain different stages of SDLC. 5
- (b) What is system ? Explain the components of computerized information system. 5

**OR**

- (c) Who is System Analyst ? Explain the role of System Analyst. 5
- (d) List, the different data collection techniques. Explain any two in detail. 5

**EITHER**

2. (a) Write short note on input and output design. 5
- (b) Explain Structured English with suitable example. 5

**OR**

- (c) What is DFD ? Explain symbols used in it, with suitable example. 5
- (d) Write short notes on :
  - (i) Types of codes
  - (ii) Types of forms. 5

**EITHER**

3. (a) Explain operational training and its related activities. 5
- (b) Explain different levels of testing. 5

**OR**

- (c) List different types of conversion. Explain modular and sequential methods of conversion. 5
- (d) Explain System Evolution with its objectives. 5

**EITHER**

4. (a) Write short note on Software Reliability and Software Quality. 5
- (b) What is Risk Management ? What are the basic issues in software reuse ? 5

**OR**

- (c) Explain metrics for project size estimation. 5
- (d) Write notes on :
  - (i) PERT
  - (ii) CPM. 5

5. Attempt all :
  - (a) Explain prototyping. 2½
  - (b) Explain decision table with suitable example. 2½
  - (c) What are change strategies ? 2½
  - (d) Explain maintenance process model. 2½