Bachelor of Science (B.Sc.I.T.) Semester—III (C.B.S.) Examination MICROPROCESSOR AND ALP

Paper—I

| e : T | hree Hours] | [Maximum Marks: 50 |
|-------|---|---|
| N.B | .:— (1) All questions are compulsory and carry equal marks. | |
| | (2) Draw neat and labelled diagram wherever necessary. | |
| EIT | HER | |
| (A) | Explain the architecture of 8086 µp in brief. | 5 |
| (B) | Give the flag register format of 8086 µp and explain. | 5 |
| OR | | |
| (C) | Explain any three addressing modes of 8086. | 5 |
| (D) | Write a short note on assembler directives. | 5 |
| EIT | HER | |
| (A) | Explain the minimum and maximum mode of operation of 8086. | 5 |
| (B) | How is the interfacing of memory done with 8086 ? Explain. | 5 |
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| | EIT (A) (B) OR (C) (D) EIT (A) (B) OR (C) (D) EIT (A) (B) OR (C) (D) EIT (A) (B) (C) (D) Atte (A) (B) (C) | EITHER (A) Explain the architecture of 8086 μp in brief. (B) Give the flag register format of 8086 μp and explain. OR (C) Explain any three addressing modes of 8086. (D) Write a short note on assembler directives. EITHER (A) Explain the minimum and maximum mode of operation of 8086. (B) How is the interfacing of memory done with 8086? Explain. |