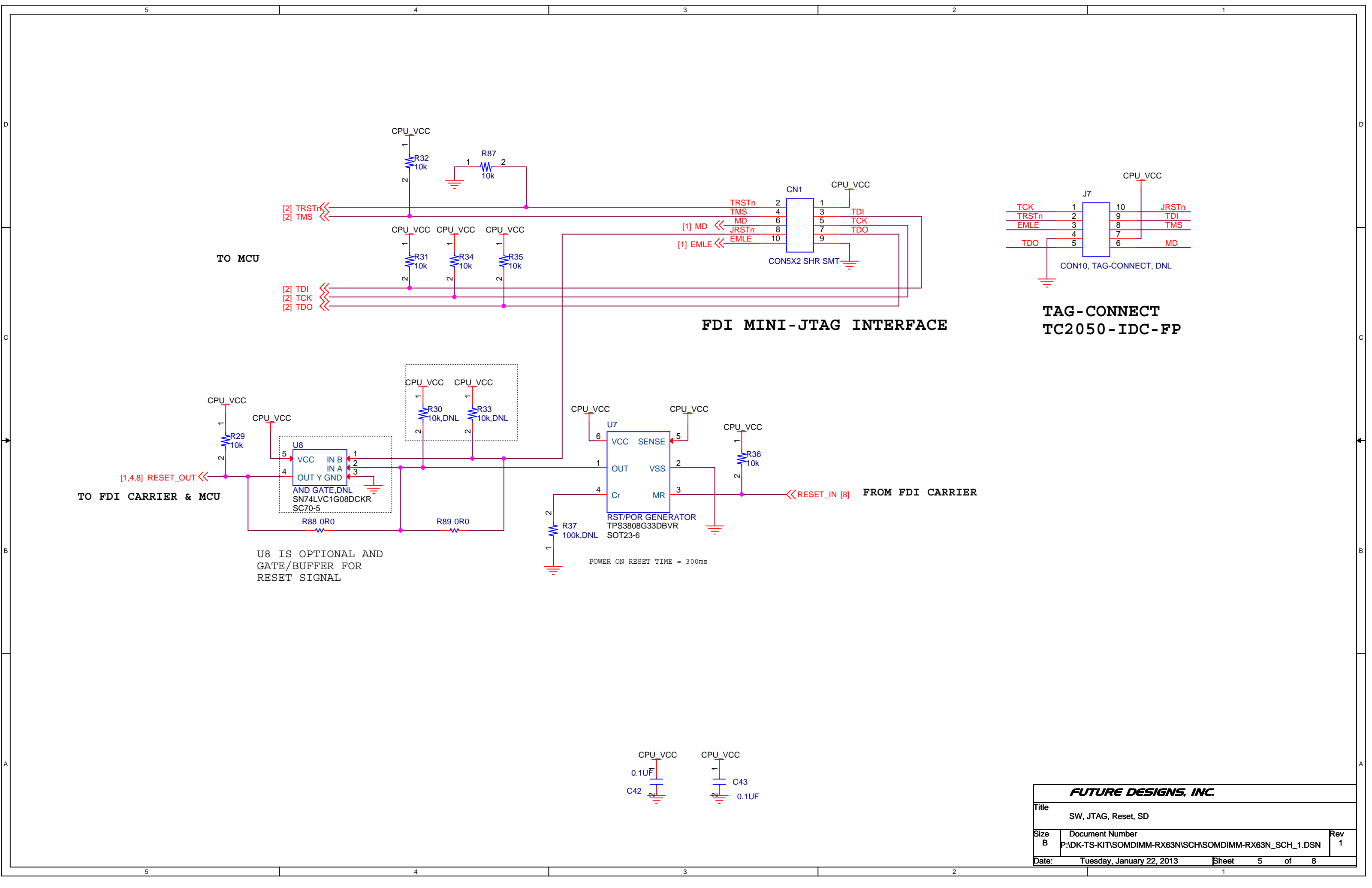
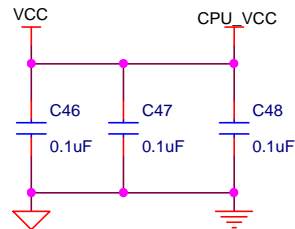
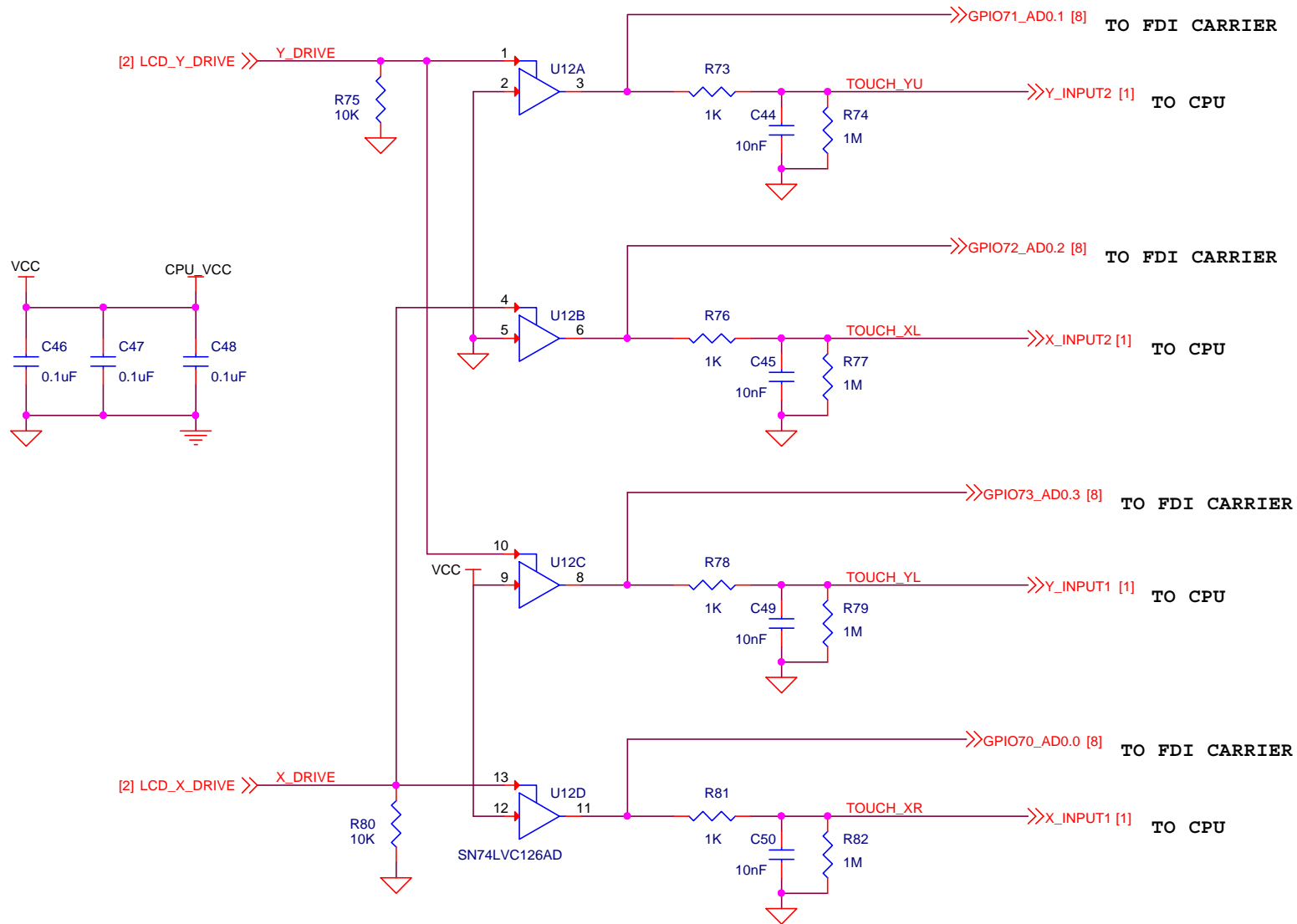
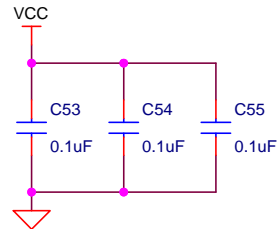
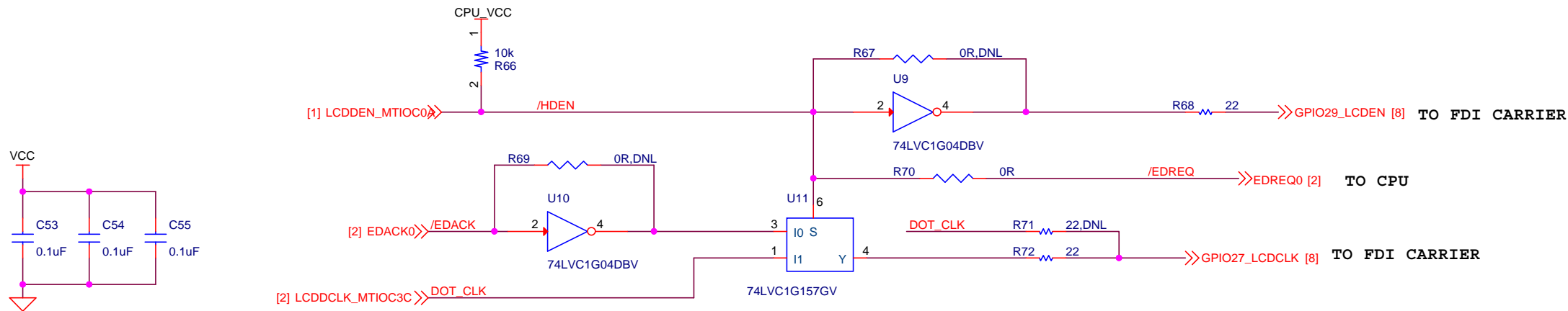


ETHERNET

FUTURE DESIGNS, INC.		
Title		
ETHERNET		
Size	Document Number	Rev
B	P:\DK-TS-KIT\SOMDIMM-RX63N\SCH\SOMDIMM-RX63N_SCH_1.DSN	1
Date:	Tuesday, January 22, 2013	Sheet 4 of 8



FUTURE DESIGNS, INC.		
Title SW, JTAG, Reset, SD		
Size B	Document Number P:\DK-TS-KIT\SOMDIMM-RX63N\SCH\SOMDIMM-RX63N_SCH_1.DSN	Rev 1
Date:	Tuesday, January 22, 2013	Sheet 5 of 8



REV 2 - SPLIT THE CPU<>TP SIGNALS

<i>FUTURE DESIGNS, INC.</i>			
Title			
LCD TIMING GENERATION & TOUCH INTERFACE			
Size	Document Number		Rev
	P:\DK-TS-KITS\OMDIMM-RX63N\SCH\OMDIMM-RX63N_SCH_1.DSN		1
Date:	Tuesday, January 22, 2013	Sheet 7 of 8	

