

1. Description

1.1. Project

Project Name	SPO
Board Name	custom
Generated with:	STM32CubeMX 6.6.0
Date	07/08/2022

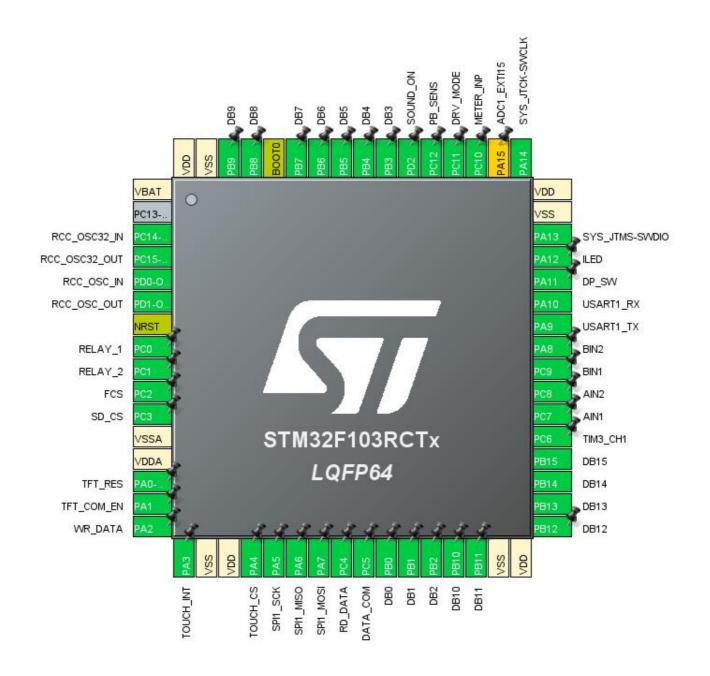
1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103RCTx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	Arm Cortex-M3

2. Pinout Configuration



3. Pins Configuration

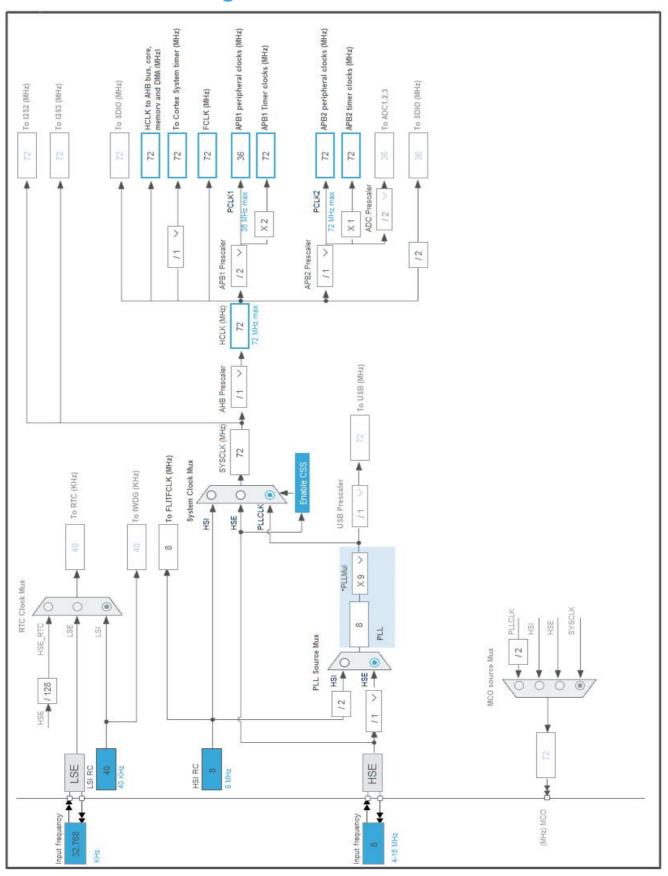
Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP64	(function after		Function(s)	
	reset)			
1	VBAT	Power		
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	PC0 *	I/O	GPIO_Output	RELAY_1
9	PC1 *	I/O	GPIO_Output	RELAY_2
10	PC2 *	I/O	GPIO_Output	FCS
11	PC3 *	I/O	GPIO_Output	SD_CS
12	VSSA	Power		
13	VDDA	Power		
14	PA0-WKUP *	I/O	GPIO_Output	TFT_RES
15	PA1 *	I/O	GPIO_Output	TFT_COM_EN
16	PA2 *	I/O	GPIO_Output	WR_DATA
17	PA3	I/O	GPIO_EXTI3	TOUCH_INT
18	VSS	Power		
19	VDD	Power		
20	PA4 *	I/O	GPIO_Output	TOUCH_CS
21	PA5	I/O	SPI1_SCK	
22	PA6	I/O	SPI1_MISO	
23	PA7	I/O	SPI1_MOSI	
24	PC4 *	I/O	GPIO_Output	RD_DATA
25	PC5 *	I/O	GPIO_Output	DATA_COM
26	PB0 *	I/O	GPIO_Output	DB0
27	PB1 *	I/O	GPIO_Output	DB1
28	PB2 *	I/O	GPIO_Output	DB2
29	PB10 *	I/O	GPIO_Output	DB10
30	PB11 *	I/O	GPIO_Output	DB11
31	VSS	Power		
32	VDD	Power		
33	PB12 *	I/O	GPIO_Output	DB12
34	PB13	I/O	SPI2_SCK	DB13
35	PB14	I/O	SPI2_MISO	DB14
36	PB15	I/O	SPI2_MOSI	DB15
37	PC6	I/O	TIM3_CH1	

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP64	(function after		Function(s)	
	reset)			
38	PC7 *	I/O	GPIO_Output	AIN1
39	PC8 *	I/O	GPIO_Output	AIN2
40	PC9 *	I/O	GPIO_Output	BIN1
41	PA8 *	I/O	GPIO_Output	BIN2
42	PA9	I/O	USART1_TX	
43	PA10	I/O	USART1_RX	
44	PA11 *	I/O	GPIO_Input	DP_SW
45	PA12 *	I/O	GPIO_Output	ILED
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
50	PA15 **	I/O	ADC1_EXTI15	
51	PC10 *	I/O	GPIO_Input	METER_INP
52	PC11 *	I/O	GPIO_Output	DRV_MODE
53	PC12 *	I/O	GPIO_Input	PB_SENS
54	PD2 *	I/O	GPIO_Output	SOUND_ON
55	PB3 *	I/O	GPIO_Output	DB3
56	PB4 *	I/O	GPIO_Output	DB4
57	PB5 *	I/O	GPIO_Output	DB5
58	PB6 *	I/O	GPIO_Output	DB6
59	PB7 *	I/O	GPIO_Output	DB7
60	BOOT0	Boot		
61	PB8 *	I/O	GPIO_Output	DB8
62	PB9 *	I/O	GPIO_Output	DB9
63	VSS	Power		
64	VDD	Power		

^{*} The pin is affected with an I/O function

^{**} The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	SPO
Project Folder	E:\MyData\Projects\water_treatment_controller\SPO
Toolchain / IDE	MDK-ARM V5.32
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	Yes
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	No
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	Yes

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_SPI1_Init	SPI1
4	MX_SPI2_Init	SPI2
5	MX_TIM3_Init	TIM3
6	MX_USART1_UART_Init	USART1

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103RCTx
Datasheet	DS5792_Rev12

6.2. Parameter Selection

Temperature	25
Vdd	3.3

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

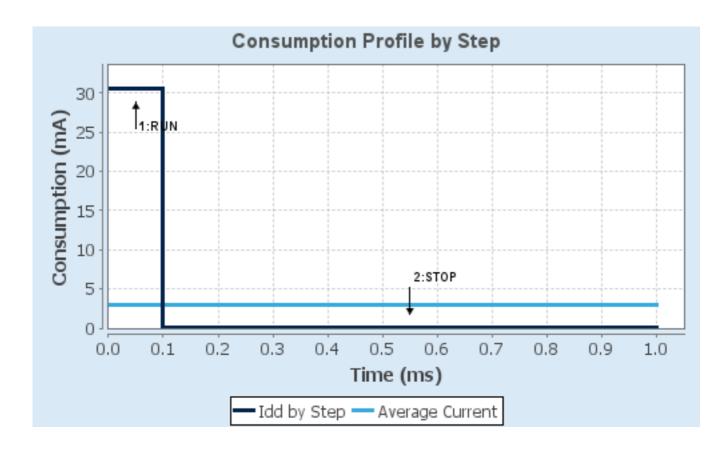
6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	30.5 mA	25 µA
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Ta Max	100.47	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	3.07 mA
Battery Life	1 month, 15 days,	Average DMIPS	61.0 DMIPS
	15 hours		

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

7.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

7.2. SPI1

Mode: Full-Duplex Master

7.2.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 4 *

Baud Rate 18.0 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

7.3. SPI2

Mode: Full-Duplex Master

7.3.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 2

Baud Rate 18.0 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

7.4. SYS

Debug: Serial Wire

Timebase Source: SysTick

7.5. TIM3

Channel1: PWM Generation CH1

7.5.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 0

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 65535

Internal Clock Division (CKD) No Division auto-reload preload Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx_EGR)

PWM Generation Channel 1:

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

7.6. USART1

Mode: Asynchronous

7.6.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	n/a	High *	
	PA6	SPI1_MISO	Input mode	No pull-up and no pull-down	n/a	
	PA7	SPI1_MOSI	Alternate Function Push Pull	n/a	High *	
SPI2	PB13	SPI2_SCK	Alternate Function Push Pull	n/a	High *	DB13
	PB14	SPI2_MISO	Input mode	No pull-up and no pull-down	n/a	DB14
	PB15	SPI2_MOSI	Alternate Function Push Pull	n/a	High *	DB15
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
TIM3	PC6	TIM3_CH1	Alternate Function Push Pull	n/a	Low	
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	High *	
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	n/a	
Single Mapped Signals	PA15	ADC1_EXTI15	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	
GPIO	PC0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RELAY_1
	PC1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RELAY_2
	PC2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	FCS
	PC3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SD_CS
	PA0-WKUP	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TFT_RES
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TFT_COM_EN
	PA2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	WR_DATA
	PA3	GPIO_EXTI3	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	TOUCH_INT
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TOUCH_CS
	PC4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RD_DATA
	PC5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DATA_COM

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max Speed	User Label
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB0
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB1
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB2
	PB10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB10
	PB11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB11
	PB12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB12
	PC7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	AIN1
	PC8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	AIN2
	PC9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	BIN1
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	BIN2
	PA11	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	DP_SW
	PA12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	ILED
	PC10	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	METER_INP
	PC11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DRV_MODE
	PC12	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	PB_SENS
	PD2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SOUND_ON
	PB3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB3
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB4
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB5
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB6
	PB7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB7
	PB8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB8
	PB9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DB9

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Prefetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
EXTI line3 interrupt	unused			
TIM3 global interrupt	unused			
SPI1 global interrupt	unused			
SPI2 global interrupt	unused			
USART1 global interrupt	unused			
EXTI line[15:10] interrupts		unused		

8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

* User modified value

9. System Views

- 9.1. Category view
- 9.1.1. Current



10. Docs & Resources

Type Link

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_embedded_software_solutions.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_eval-

tools_portfolio.pdf

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- microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf
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& Software

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& Software

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