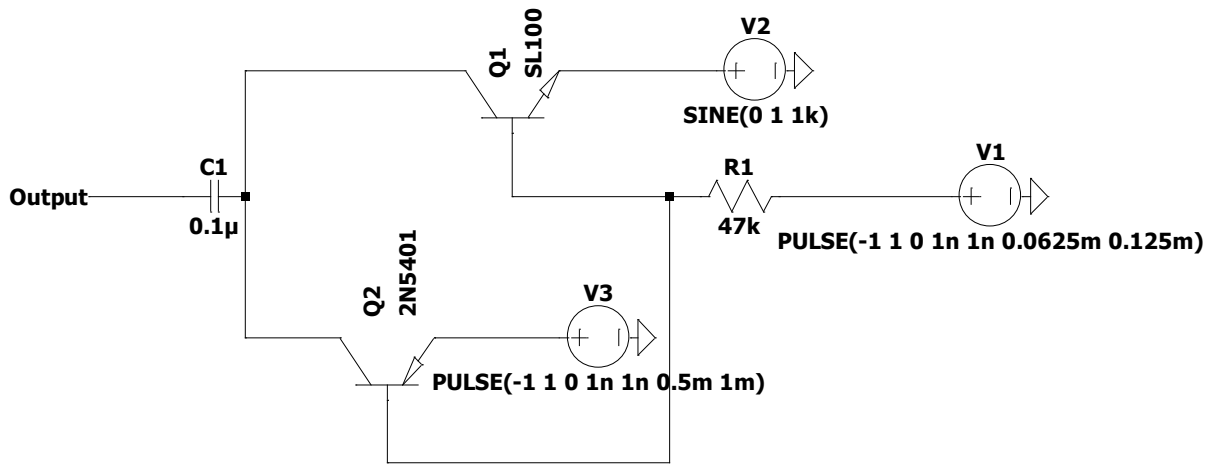
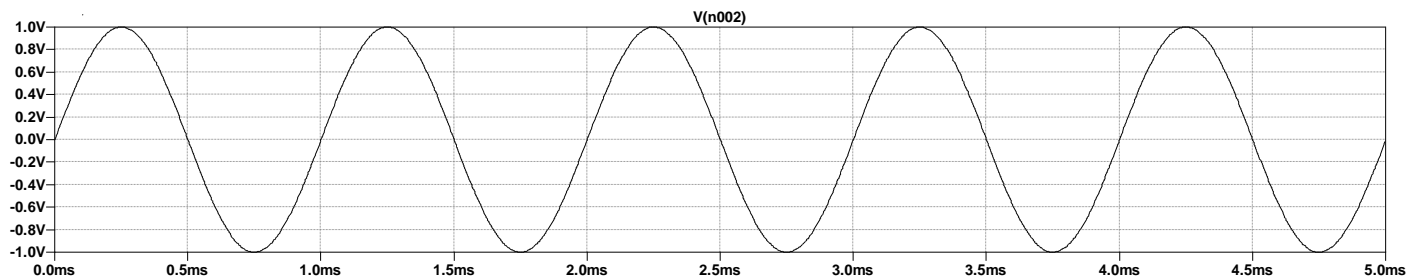


## Time Division Multiplexing circuit

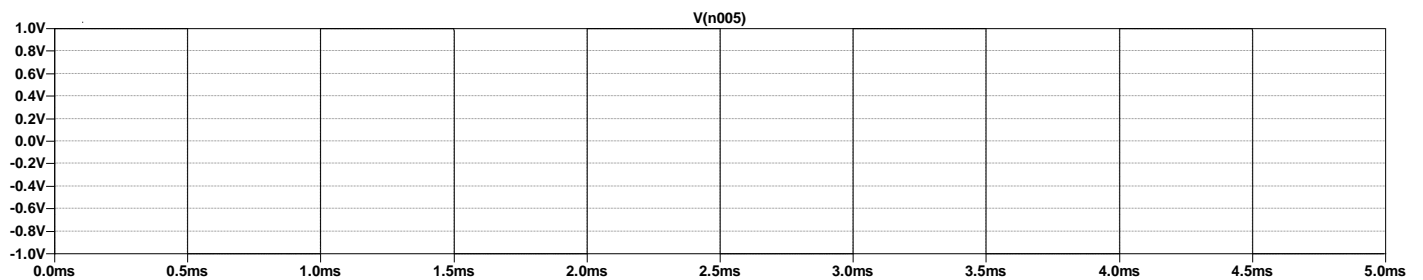
G.Karishni  
CB.EN.U4ECE19116



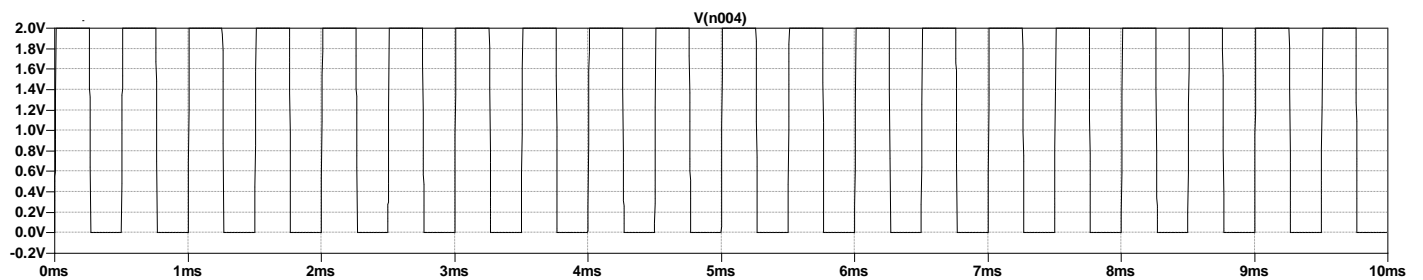
Signal 1



Signal 2



Switching signal



## Time Division Multiplexing output

