Section 1: Instruction for running program

This simulator has been set up using Java programming language. To run the simulator, run the class called 'Main.java' or just Main (if the extension has been hidden). This class can be found on the folder Assignment4 > src. Pass the file name including its extension on the command line, make sure that the file you are trying to run has been saved on the folder called 'Files' as the simulator has been set up to only run files in that folder.

The simulator has been set up to run a single cache configuration by default. To run the simulator with a different configuration, change the value of the variable 'configuration' which can be found at the top of the Main class. Change the value to 'multilevel' to run a multi-level cache or to 'singlelevel' to run a single cache.

After changing the configuration type, change the block size and/or the bytes per block to your desired values. To do this, change the value(s) of the following variables:

To change size of the L1 cache, change the values of these variables,

- l1NumberOfBlocks
- l1BytesPerBlock

To change size of the L2 cache, change the values of these variables,

- 12NumberOfBlocks
- 12BytesPerBlock

If the selected configuration is a single-cache, you do **NOT** have to change the values of the L2 cache, as it will have no effect on your results.

The comments of the code can be found inside the code.

TO RUN ON AN IDE, FOLLOW THE FIRST TWO COMENTS ON THE MAIN CLASS.

Section 2: Proof of simulators functionality correctness

The simulator's functionality has been tested and proven to be working correctly, single level's cache configuration functionality was tested using the 'small_set.addr' file and the results produced were the same as those of the given example. Setting the size to 16 blocks and 32 bytes / block, the obtained results can be found on page 2, on the first attached screenshot.

The multi level's cache configuration functionality was tested using the 'small_L2_set.addr' file and the results produced were the same as those of the given example. Setting the size to L1 cache: 16 blocks and 16 bytes / block and L2 cache: 64 blocks and 64 bytes / block. The obtained results can be found on page 3, on the second attached screenshot.



