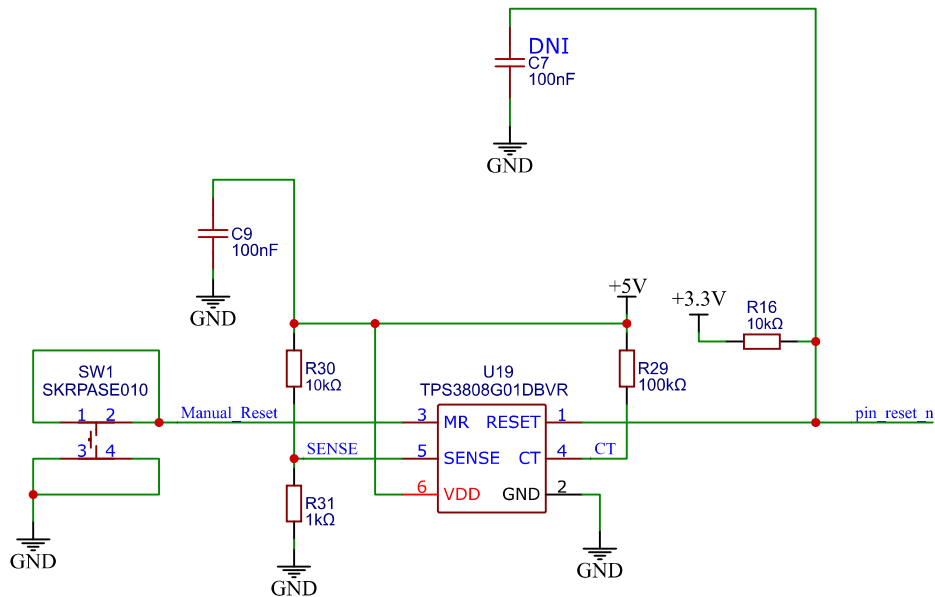
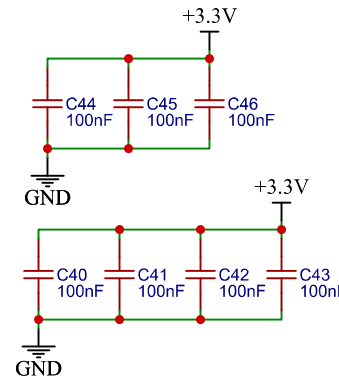
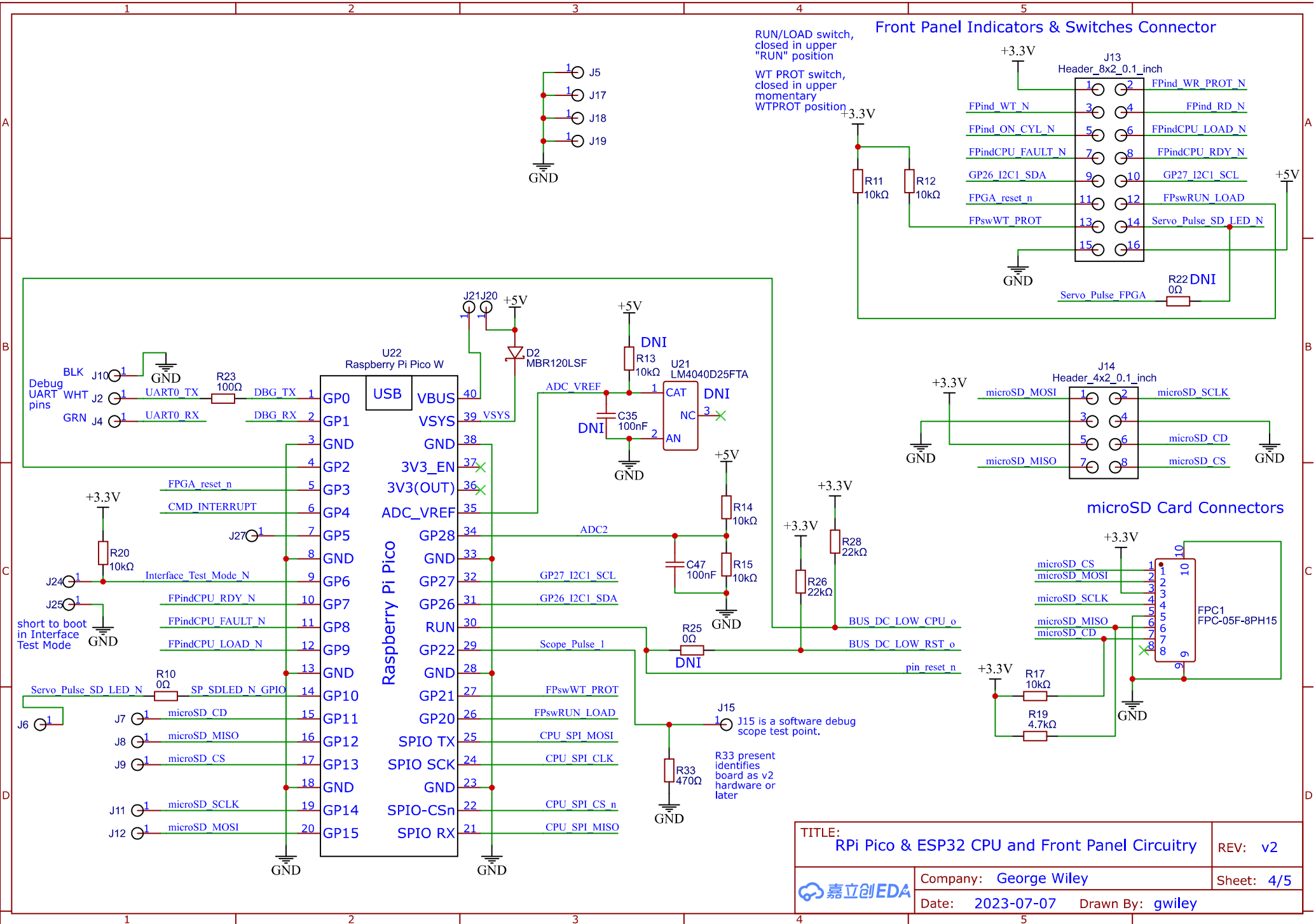


- H1 MOUNTING_HOLE_M3_CAP_NO_PTH_NARROW
- 1 1 X
- H2 MOUNTING_HOLE_M3_CAP_NO_PTH_NARROW
- 1 1 X
- H3 MOUNTING_HOLE_M3_CAP_NO_PTH_NARROW
- 1 1 X
- H4 MOUNTING_HOLE_M3_CAP_NO_PTH_NARROW
- 1 1 X
- H5 MOUNTING_HOLE_M3_CAP_NO_PTH_NARROW
- 1 1 X

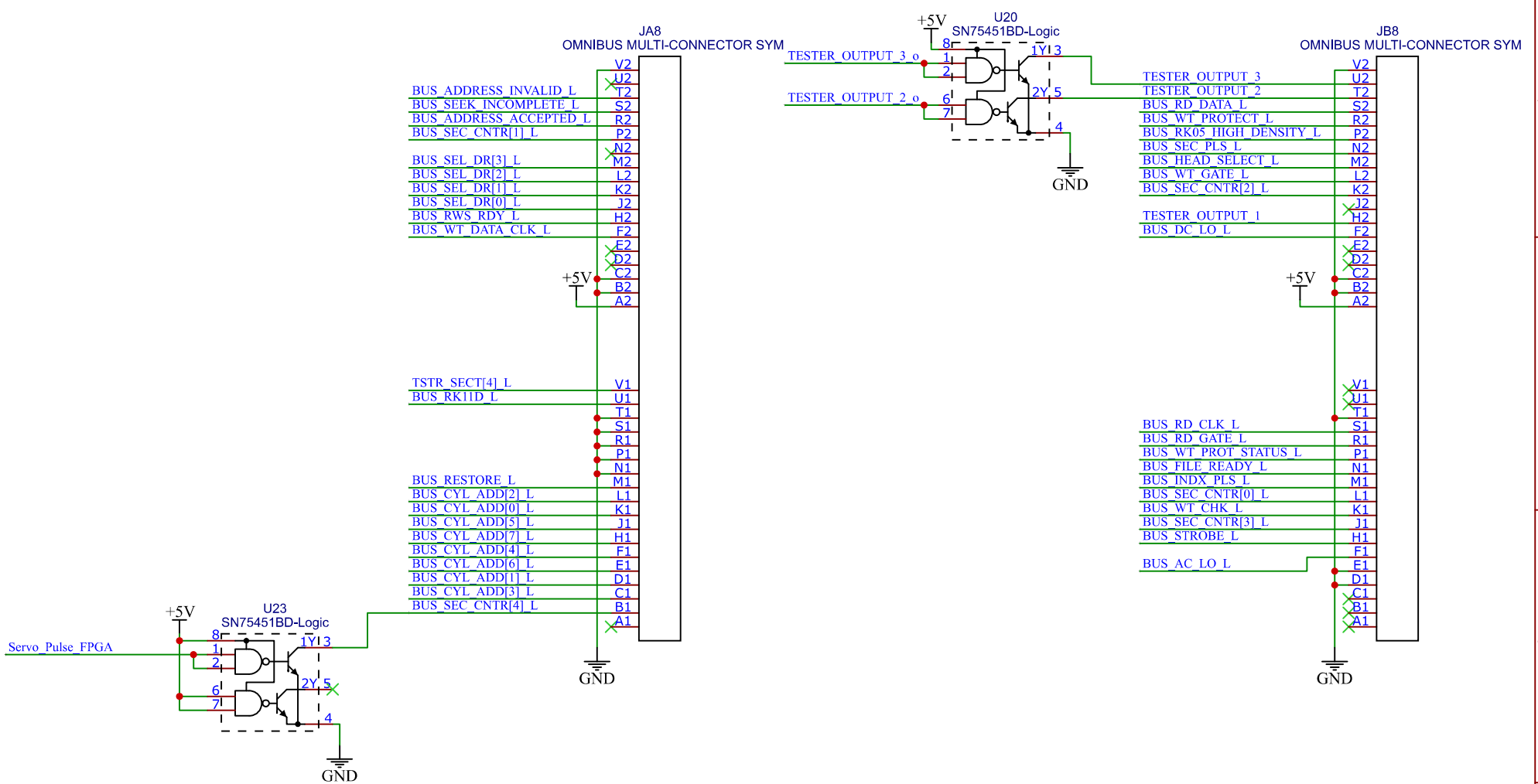
One bypass cap for each DRAM power pin.



TITLE: Voltage Regulators and Reset		REV: v2
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	Date: 2023-07-07	Drawn By: gwiley



TITLE: RPi Pico & ESP32 CPU and Front Panel Circuitry		REV: v2
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TITLE: RK05 Bus Connectors		REV: v2
嘉立创EDA	Company: George Wiley	Sheet: 5/5
	Date: 2023-07-07	Drawn By: gwiley