

## SDRAM Controller State Machine

### Memory Data Addressing

Unused* 00	Cylinder: 0 – 202	Head 0,1	16-sector: 0 – 15 12-sector: 0 – 11	16-sector, 12-bit (RK8E): 0 – 192 12-sector, 16-bit (RK11D): 0 – 256 12-sector, 18-bit (RK11E): 0 – 288
23 22:21	8	14 13 12	4	9:8
0 0	CYLINDER	HD	SECTOR	WORD
BS1 BS0	A12	A5 A4 A3	A0	A8
Bank	Row Address			Column Address

\* Will use bank 01 for RK05F mode.

