# Graham Gobieski

5000 Forbes Avenue	
Gates-Hillman Center	
Pittsburgh, Pennsylvania 15213	
2017 - Present	
	2013 - 2017
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### Research

### **Energy-efficient architectures for Low Power Embedded Systems**

• Developed the vector-dataflow execution model and implemented MANIC, an Prof. Brandon Lucia energy-efficient vector-dataflow co-processor

• Took full-stack approach: custom compiler, LibC, functional simulator, complete RTL for MANIC and scalar core

Paper accepted to MICRO 52

Prof. Nathan Beckmann, Carnegie Mellon University 2017 - Present

### **Neural Network Inference on Intermittent Embedded Systems**

- Built SONIC & TAILS systems that leverage the regular structure of linear algebra operations to accelerate inference
- Optimized network structure for embedded devices and built automated testing framework with MSP430 and Powercast harvester
- Papers accepted to ASPLOS'19 and SysML'18

### "Shuffler: Fast and Deployable Continuous Code Re-randomization"

Helped create system to defend against code-reuse attacks

Paper accepted to OSDI 2016

# David Williams-King, Prof. Junfeng Yang

### "Clickable poly (ionic liquids): A materials platform for transfection"

• Studied novel class of polymers with applications to fuel cells

Paper appeared in Angewandte Chemie 128

# Columbia University 2015-2016 Prof. Luis Campos

Columbia University

## **Professional Experience**

### **MongoDB Software Engineer Intern**

 Assisted in building proxy service that translated/compiled SQL queries into the MongoDB query language

2016

2013-2016

July, 2020

# Fellowships/Awards

### Apple Scholar in AI/ML

Awarded prestigious fellowship from Apple for work on on-device ML

- Fellowship is awarded to twelve PhD students globally

### **Technical Skills**

- Programming languages: C, C++, System Verilog, Python
- Hardware synthesis utilizing System Verilog and Cadence CAD tools
- Embedded systems including those based on MSP430, RISCV, ARM