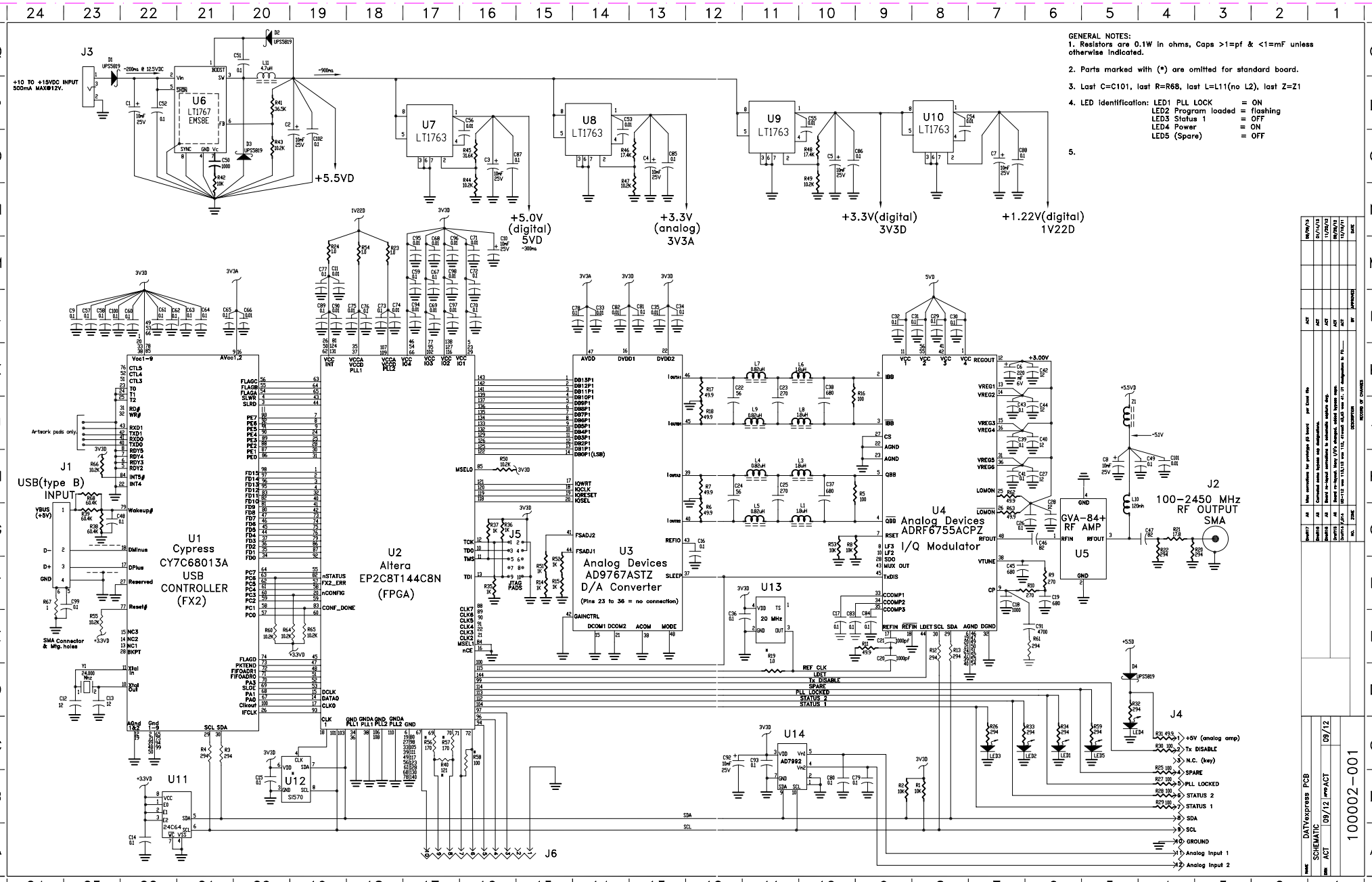
GENERAL NOTES:
1. Resistors are 0.1W in ohms, Caps >1=pf & <1=mF unless otherwise indicated.
2. Parts marked with (*) are omitted for standard board.
3. Last C=C101, last R=R68, last L=L11(no L2), last Z=Z1
4. LED identification: LED1 PLL LOCK = ON
LED2 Program loaded = flashing
LED3 Status 1 = OFF
LED4 Power = ON
LED5 (Spare) = OFF
5.
J3
+10 TO +15VDC INPUT 500mA MAX@12V.
U6 LT1767 EMS8E
+5.5VD
U7 LT1763
+5.0V (digital) 5VD
U8 LT1763
+3.3V (analog) 3V3A
U9 LT1763
+3.3V(digital) 3V3D
U10 LT1763
+1.22V(digital) 1V22D
J1 USB(type B) INPUT
U1 Cypress CY7C68013A USB CONTROLLER (FX2)
U2 Altera EP2C8T144C8N (FPGA)
U3 Analog Devices AD9767ASTZ D/A Converter
(Pins 23 to 36 = no connection)
U4 Analog Devices ADRF6755ACPZ I/Q Modulator
U5 GVA-84+ RF AMP
J2 100-2450 MHz RF OUTPUT SMA
J4
+5V (analog amp)
Tx DISABLE
N.C. (key)
SPARE
PLL LOCKED
STATUS 2
STATUS 1
SDA
SCL
GROUND
Analog Input 1
Analog Input 2
J6
U11
U12 SI570
U13 20 MHz
U14 AD7992
J5
DATVexpress PCB
SCHEMATIC
100002-001