



PGED1

PGEC1

⟨RB2

RB3

RB4

RB5

RB6

RB7

RB9

RB10

RB11

RB12

RB13

RB14

₹RB15

NOTE 3

TO PREVENT ERRATIC PERFORMANCE AT HIGHER CLOCK RATES C2 MUST BE LOW ESR, TYPICALLY <4 OHMS 4 OHMS.



