Objective Questions

1)The nominal value of the dc supply voltage for CMOS is _____.
a) +3 V

b) +5 V

c) +9 V

d) +12 V

2) The output of an AND gate with three inputs, A, B, and C, is HIGH when _____.

a) A = 1, B = 1, C = 0

b) A = 0, B = 0, C = 0

c) A = 1, B = 1, C = 1

d) A = 1, B = 0, C = 1

3) If a 3-input NOR gate has eight input possibilities, how many of those possibilities will result in a HIGH output?

a) 1

b) 2

c) 7

d) 8

4) The output of an OR gate with three inputs, A, B, and C, is LOW when _____.
a) A = 0, B = 0, C = 0

b) A = 0, B = 0, C = 1

c) A = 0, B = 1, C = 1

d) all of the above

5)Which of the following logical operations is represented by the + sign in Boolean algebra?

a) inversion

b) AND

c) OR

d) complementation

6) Output will be a LOW for any case when one or more inputs are zero for a(n):

a) OR gate

b) NOT gate

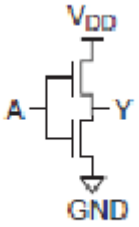
c) AND gate

d) NOR gate

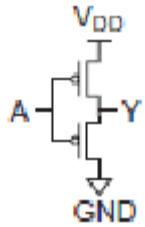
7) What does CMOS stand for in the context of electronic circuits?

A) Central Mode of Switching
B) Complementary Metal-Oxide-Semiconductor
C) Circuitry for Modular Output Systems
D) Continuous Microprocessor Operation System

8) The CMOS gate circuit of NOT gate is:



a)



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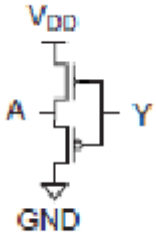
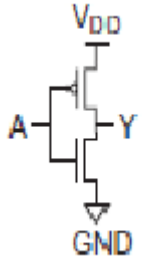


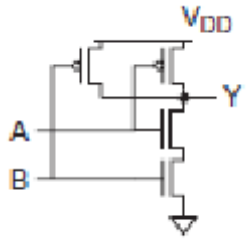
c)



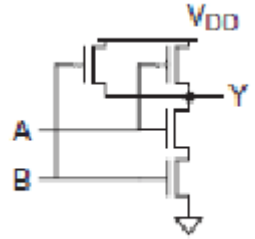
d)

9) In CMOS logic circuit the p-MOS transistor acts as:
a) Pull down network
b) Pull up network
c) Load
d) Short to ground

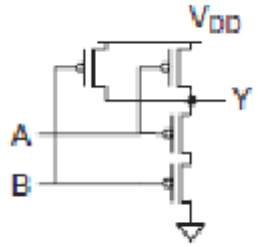
10) The CMOS logic circuit for NAND gate is:



a)



b)



c)
d) None of the mentioned

11) In CMOS logic circuit the n-MOS transistor acts as:
a) Load
b) Pull up network
c) Pull down network
d) Not used in CMOS circuits

12) Both NAND and NOR gates can be used in gate logic.
a) true
b) false

Answers:1-b,2-c,3-a,4-a,5-c,6-c,7-b,8-d,9-b,10-a,11-c,12-a