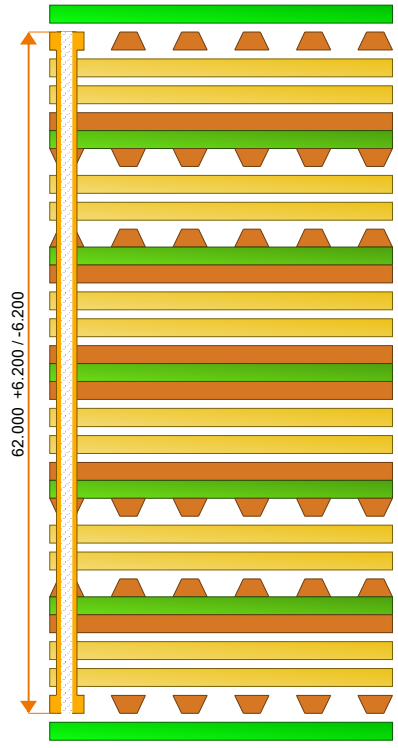

Layer	Stack up	Supplier	Description	Type	Base Thickness	εr	Processed Thickness	Impedance ID	Mask Thickness
		Taiyo	PSR-4000BN (semi gloss)	soldermask		4.500	0.800		0.800
1		Isola	1/2 oz copper foil	foil	0.600		1.600	1, 2	
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.240		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.240		
2					1.200		1.200		
		Isola	FR408HR .004 (106/1080-59) 1/1 core*	core	4.000	3.570	4.000		
3					1.200		1.200	3	
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
4					1.200		1.200	4, 5	
		Isola	FR408HR .004 (106/1080-59) 1/1 core*	core	4.000	3.570	4.000		
5					1.200		1.200		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
6					1.200		1.200		
		Isola	FR408HR .004 (106/1080-59) 1/1 core*	core	4.000	3.570	4.000		
7					1.200		1.200		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
8					1.200		1.200		
		Isola	FR408HR .004 (106/1080-59) 1/1 core*	core	4.000	3.570	4.000		
9					1.200		1.200	6	
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.180		
10					1.200		1.200	7	
		Isola	FR408HR .004 (106/1080-59) 1/1 core*	core	4.000	3.570	4.000		
11					1.200		1.200		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.240		
		Isola	FR408HR 106(75) pre-preg	pre-preg	2.300	3.230	2.240		
12		Isola	1/2 oz copper foil	foil	0.600		1.600	8, 9	
		Taiyo	PSR-4000BN (semi gloss)	soldermask		4.500	0.800		0.800

Copper Thickness = 15.200 | Dielectric Thickness = 46.400 | Solder Mask Thickness = 1.600 | Stack Up Thickness = 61.600 | Stack Up Thickness with Soldermask = 63.200

Structure Image	Impedance ID	Structure Name	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Ground Strip Separation (D1)	Target Impedance	Calculated Impedance	Tol (+/- %)	MIN Trace FAIL	MAX Trace FAIL	Measured Trace	OP Initial
	1	Coated Microstrip 1B	1	2	0	8.500	8.000	0.000	0.000	50.000	50.410	10.000				
	2	Edge Coupled Coated Microstrip 1B	1	2	0	5.000	4.500	5.000	0.000	100.000	100.690	10.000				
	3	Offset Stripline 1B2A	3	2	5	5.000	4.500	0.000	0.000	50.000	50.260	10.000				

StackName: Ac Stack 5212020 1103 ciq	Version:	Revision:	Modification:	Date of Revision:	Editor	Page 1/2
Date: 5/21/2020	Associated Documents:					
Author: Neil Abraham						
Department: CI Eng						
Site:						

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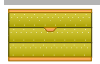
Structure Image	Impedance ID	Structure Name	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Ground Strip Separation (D1)	Target Impedance	Calculated Impedance	Tol (+/- %)	MIN Trace FAIL	MAX Trace FAIL	Measured Trace	OP Initial
	4	Offset Stripline 1B2A	4	2	5	5.000	4.500	0.000	0.000	50.000	50.260	10.000				
	5	Edge Coupled Offset Stripline 1B2A	4	2	5	3.800	3.300	6.100	0.000	100.000	100.850	10.000				
	6	Offset Stripline 1B2A	9	8	11	5.000	4.500	0.000	0.000	50.000	50.260	10.000				
	7	Offset Stripline 1B2A	10	8	11	5.000	4.500	0.000	0.000	50.000	50.260	10.000				
	8	Coated Microstrip 1B	12	11	0	8.500	8.000	0.000	0.000	50.000	50.410	10.000				
	9	Edge Coupled Coated Microstrip 1B	12	11	0	8.300	7.800	4.500	0.000	80.000	80.580	10.000				

Drill Image	1st Layer	2nd Layer	Column Position	Drill Type	Different Hole Sizes	Data Filenames	Minimum Size
	1	12	1	Mechanical PTH	0		20.000

Notes

Supplier	Supplier Description	Description	Type	Total Quantity
Taiyo	PSR-4000BN (semi gloss)	PSR-4000BN (semi gloss)	soldermask	2
Isola	1/2 oz copper foil	1/2 oz copper foil	foil	2
Isola	FR408HR 106(75) pre-preg	FR408HR 106(75) pre-preg	pre-preg	12
Isola	FR408HR .004 (106/1080-59) 1/1 core*	FR408HR .004 (106/1080-59) 1/1 core*	core	5

No. of Panels = 1 | Circuits Per Panel = 1 | Price Per Circuit = 20.00 |

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