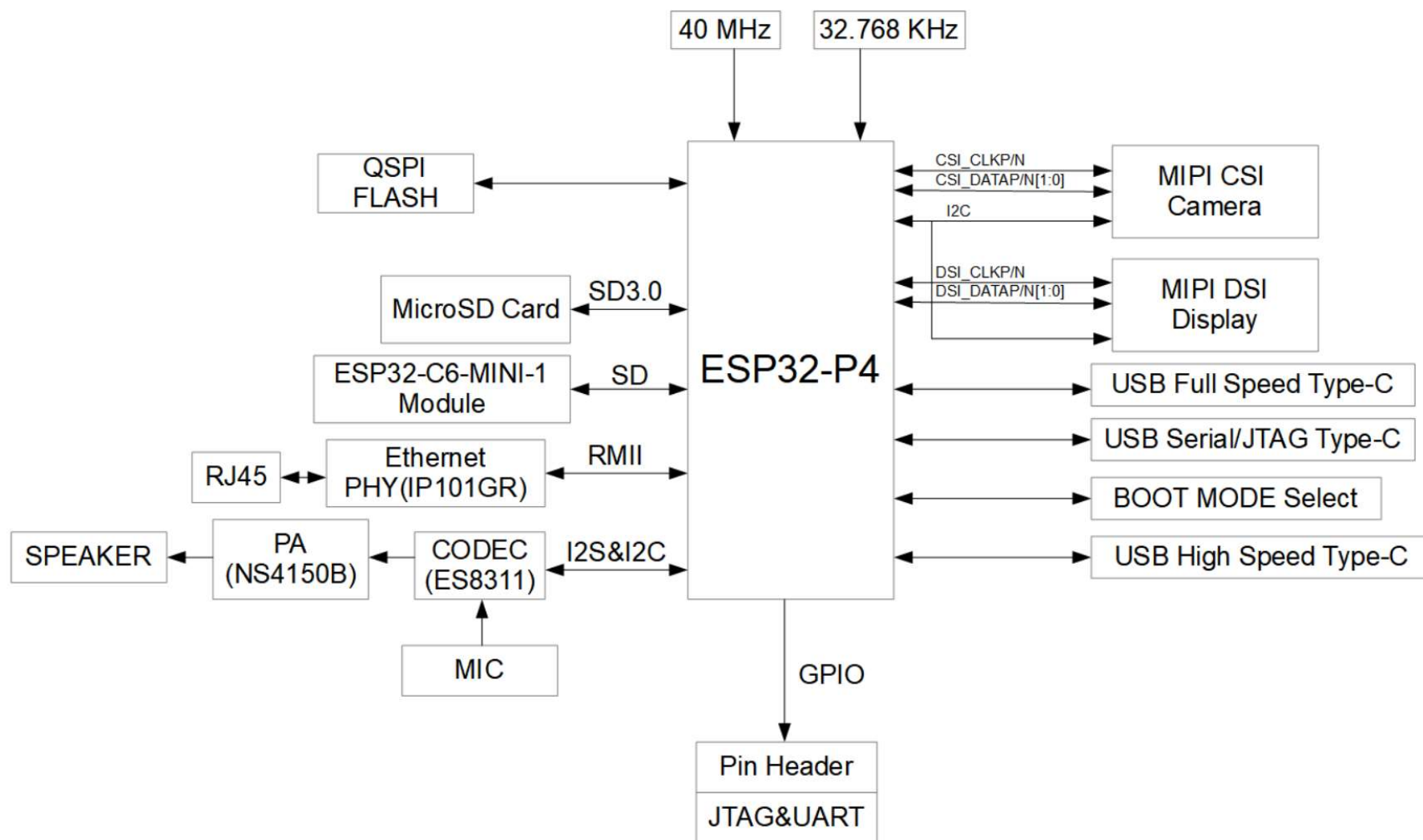
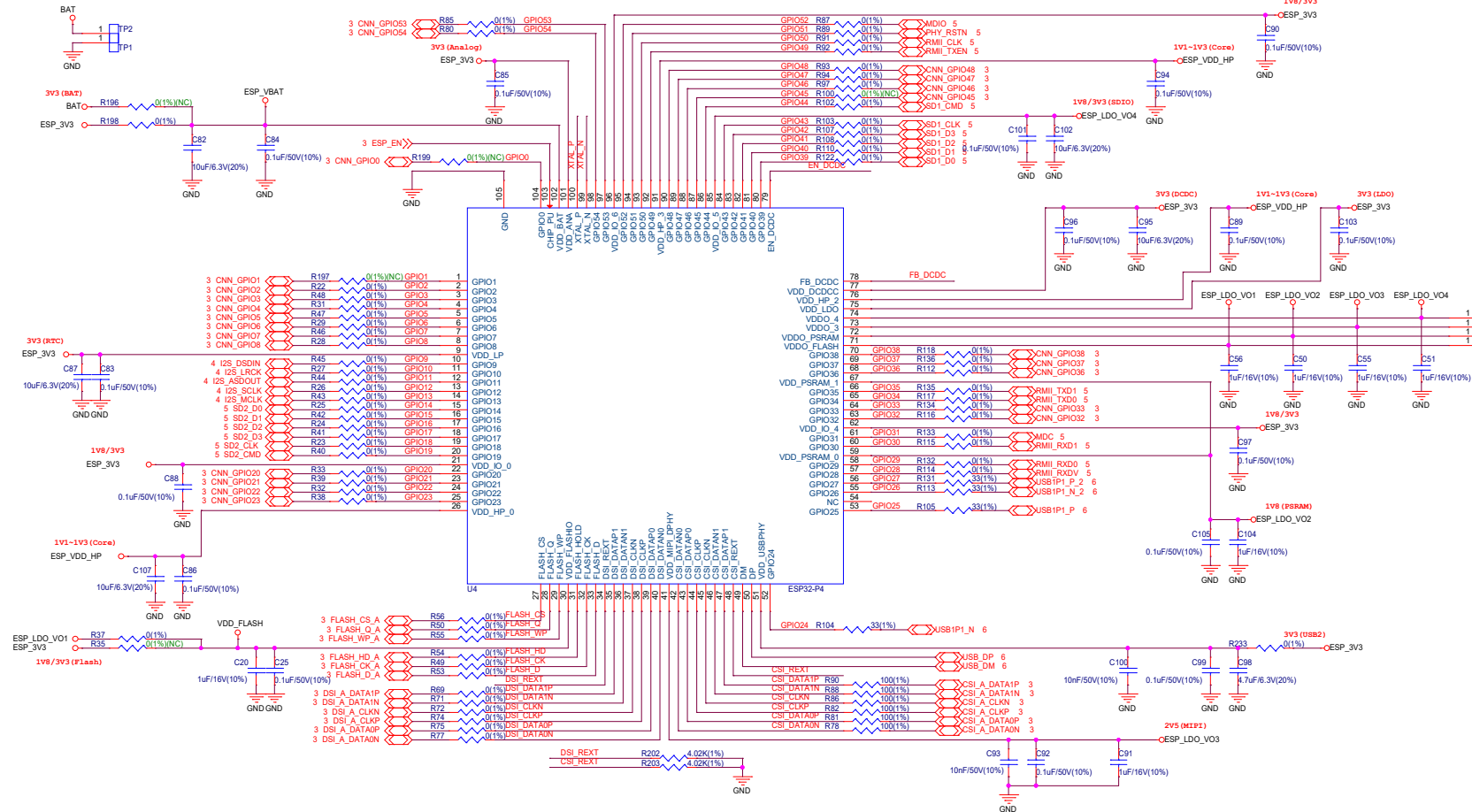
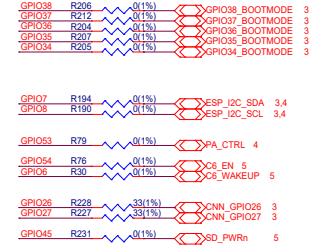
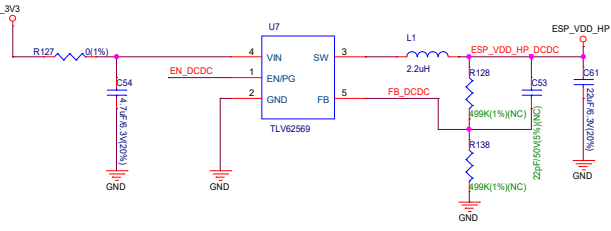
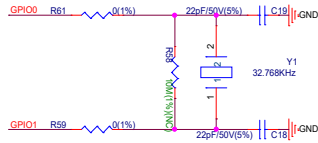
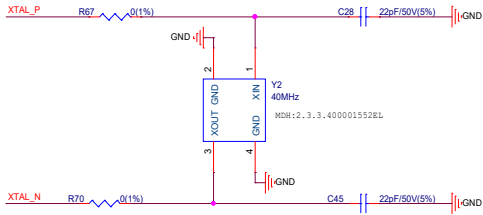
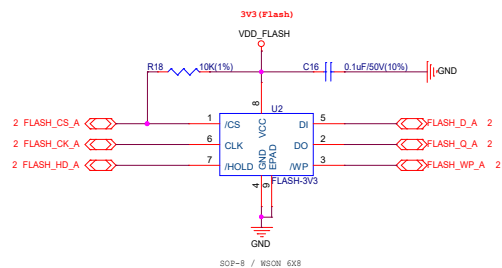


Design Block:

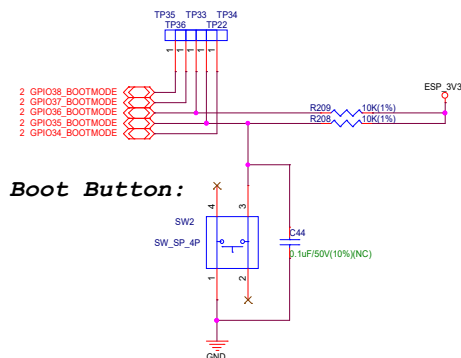




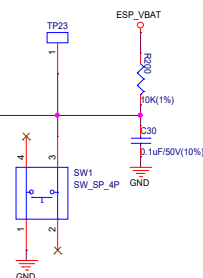
FLASH:



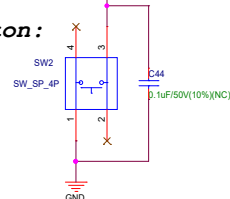
Boot Strapping Pins:



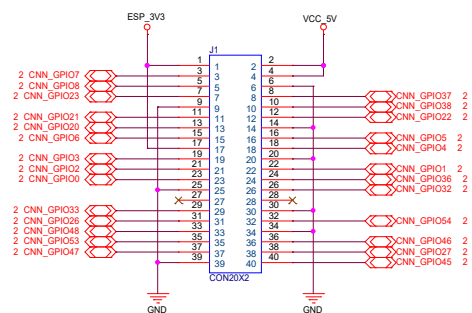
Reset Button:



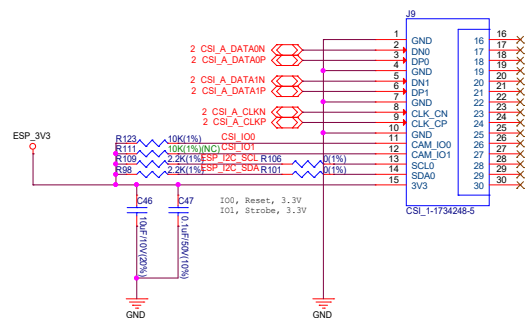
Boot Button:



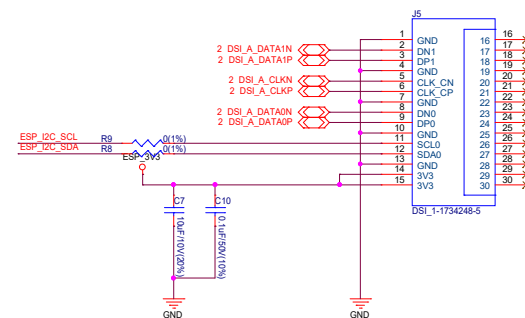
Pin Header For GPIOs:



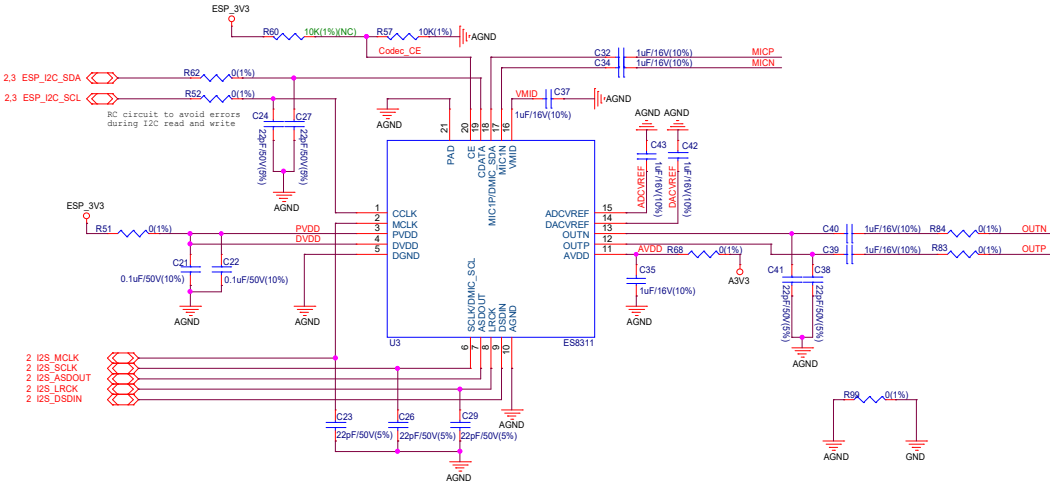
CSI Connector:



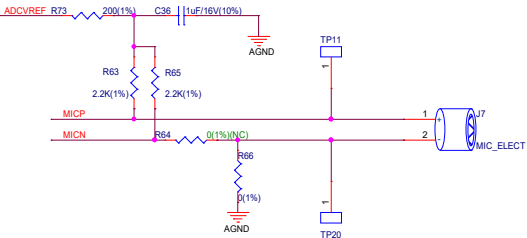
DSI Connector:



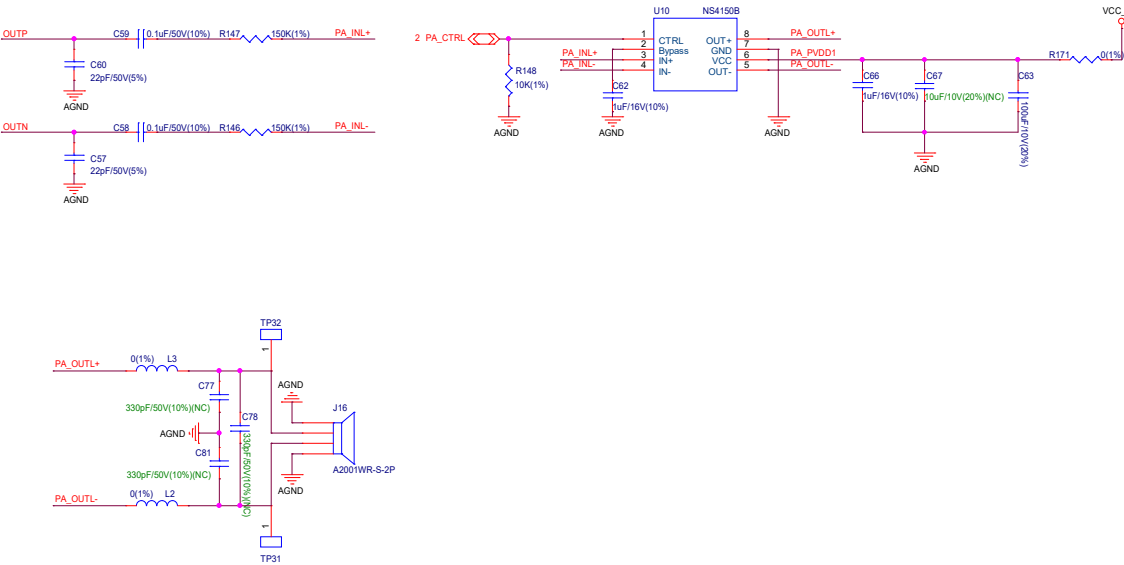
Codec :



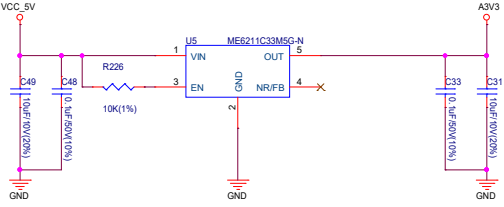
MIC :



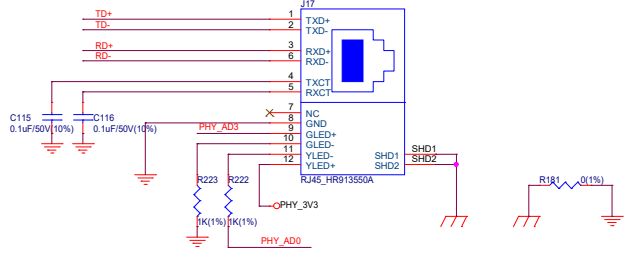
PA&SPEAKER :



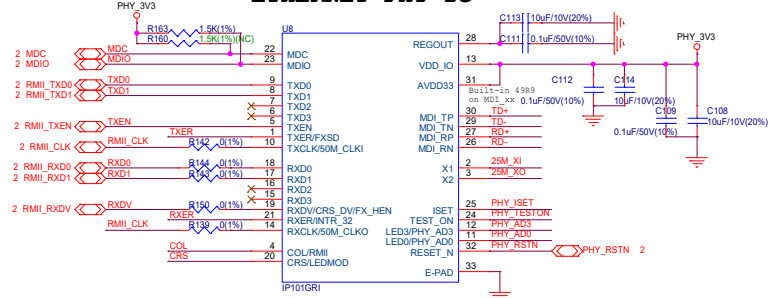
3.3V LDO For AVDD:



RJ45 with Trans

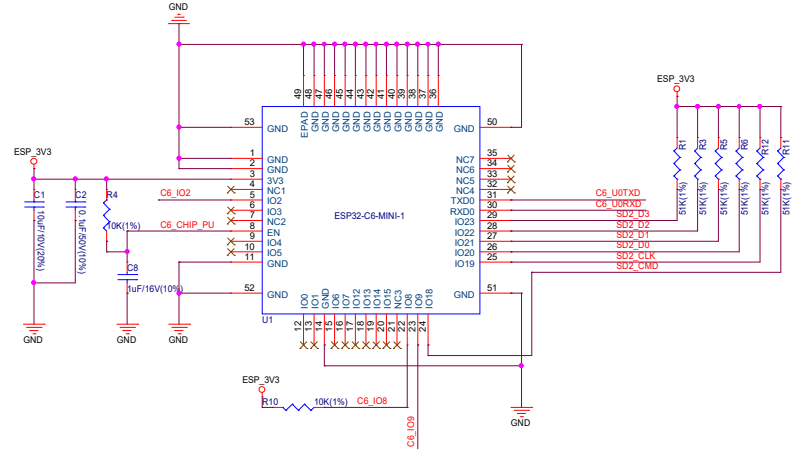


ETHERNET PHY IC

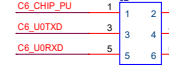


PHY_RSTN is active low.
Set low to PHY_RSTN pin for at least 10ms. Will reset all functions available in IP101GR1.

WiFi & BT Module:



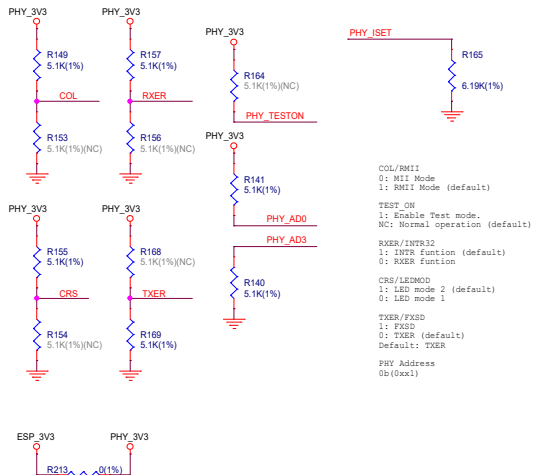
ESP-Prog Connector



MicroSD Card:

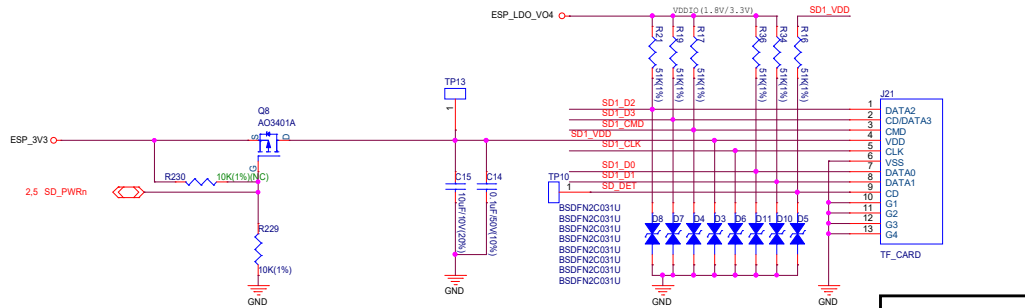


PHY MODE CONFIG



COL/RMII
0: RMII Mode
1: RMII Mode (default)
TEST_ON
1: Enable Test mode.
NC: Normal operation (default)
0: RXER function
CRS/LEDMOD
1: LED mode 2 (default)
0: LED mode 1
TXER/FXSD
1: FXSD
0: TXER (default)
Default: TXER
PHY Address
0b(0x01)

MicroSD Card:



MicroSD Card SPI mode:
1. DAT3 works as CS(chip select);
2. CMD works as DI(Data in);
3. CLK works as Clock;
4. DAT0 works as DO(Data out).

ESPRESSIF

乐鑫科技 (上海) 股份有限公司

File ESP32-P4_FUNCTION_EV_BOARD

Size Page Name

C 05_Ethernet_SOMMC_WIFI

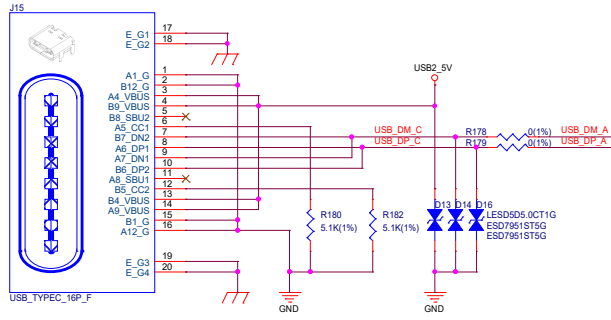
Date: Wednesday, March 05, 2025

Rev 1.5.2

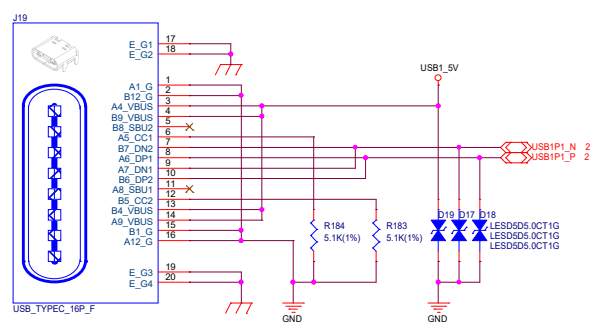
Proprietary

Sheet 5 of 6

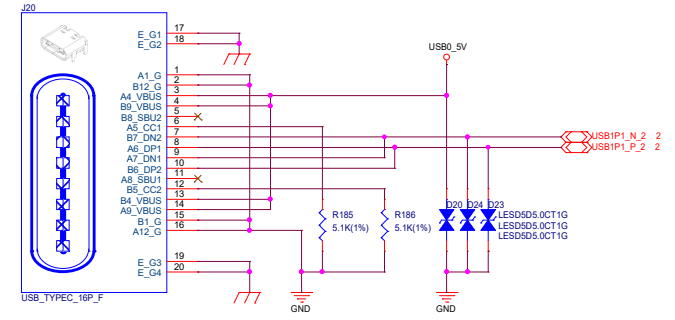
USB High Speed & Power in:



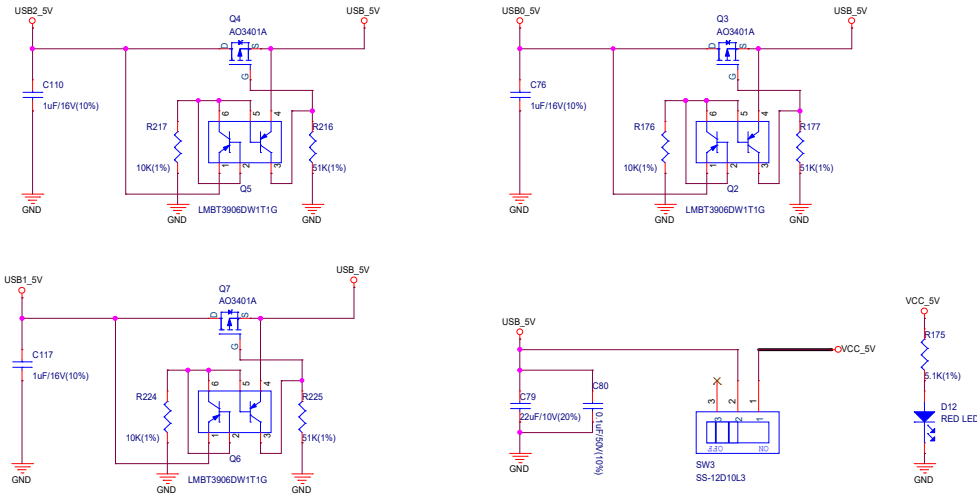
USB Serial/JTAG & Power in:



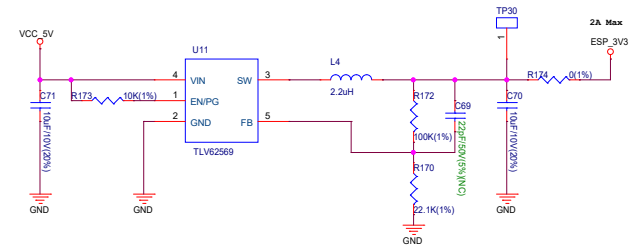
USB Full Speed Slave & Power in:



Ideal Diodes:



3V3 DC-DC:



USB High Speed Host:

Jumper@J10

