Sick **Yoon**

7 Lorraine Drive Suite 609, Toronto ON, Canada M2N 7H2

✉ shicky@gmail.com

Skills

ASIC	Verilog, System Verilog, OVM, UVM, Perl
Web	Python3/Tornado, Java/Netty/Guice, Scala/Play/Spring, Nginx, Redis
Database	MongoDB, PostgreSQL, MySQL, Oracle 11g, Hibernate
Windows	C++, Win32 API, C#, WPF, .NET, WDK, Forensic
Embedded	C++/C/Python, Atmel Studio, AVRDUDE, Cadsoft Eagle
Version Control	GIT, SVN, Perforce
Certificate	Sun Certified Java Programmer (SCJP)

Experience

Cyber Command, Ministry of National Defense (South Korea) — *Seoul, S.Korea*

SOFTWARE ENGINEER — *Feb. 2015 - Present*

- Mandatory military service
- Surveillance windows application in C++/C#/WDK
- Web Server in Python3/Tornado/PostgreSQL
- Surveillance Proxy in Java/Netty/Guice

BunFace Studio — *Toronto, Canada*

ANDROID DEVELOPER — *Oct. 2015 - Present*

- Unit Converter (released)
- Fish Game in Unity2D (ongoing)

Internet of Flow Inc. — *Toronto, Canada*

CO-FOUNDER — *Jan. 2013 - Present*

- Embedded platform for monitoring water consumption
- Web application for monitoring water consumption Python3/Tornado/MongoDB

ASIC Design Engineer 2 — *Markham, Canada*

ADVANCED MICRO DEVICES INC. — *Aug. 2013 - Oct. 2014*

- PCIe post-silicon debugging
- PCS IP (PCIe, USB, SATA) design verification in C++/UVM/OVM
- IEEE Standards: PCIe, PCS

Research Associate — *Toronto, Canada*

UNIVERSITY OF TORONTO, ADVANCED PHOTOVOLTAICS & DEVICES LAB — *May 2013 - July 2013*

- Wireless sensor network in C/Python
- Web application in Java/Scala/Play/MongoDB
- Schematic/PCB design

Software Engineer — *Mississauga, Canada*

AVERSAN INC. — *Nov. 2012 - May 2013*

- eHealth Ontario validation framework in Java/Spring/Hibernate/Maven/Oracle 11g
- Automated web test tool based on Selenium

Research Assistant *Toronto, Canada*

University of Toronto, Advanced Photovoltaics & Devices Lab *Jan. 2012 - Oct. 2012*

- Mesh sensor network based on Wifi

ASIC Design Engineer (PEY) *Markham, Canada*

Advanced Micro Devices Inc. *May 2012 - Sep. 2011*

- DFx IP (DFT/DFD) design verficiation in C++/Perl
- IEEE Standards: JTAG, P1500

Education

University of Toronto *Toronto, Canada*

M.Eng. Computer Engineering *2012 - 2013*

- Leave of absence due to mandatory military service

University of Toronto *Toronto, Canada*

B.S. Computer Engineering *2007 - 2012*

References available upon request