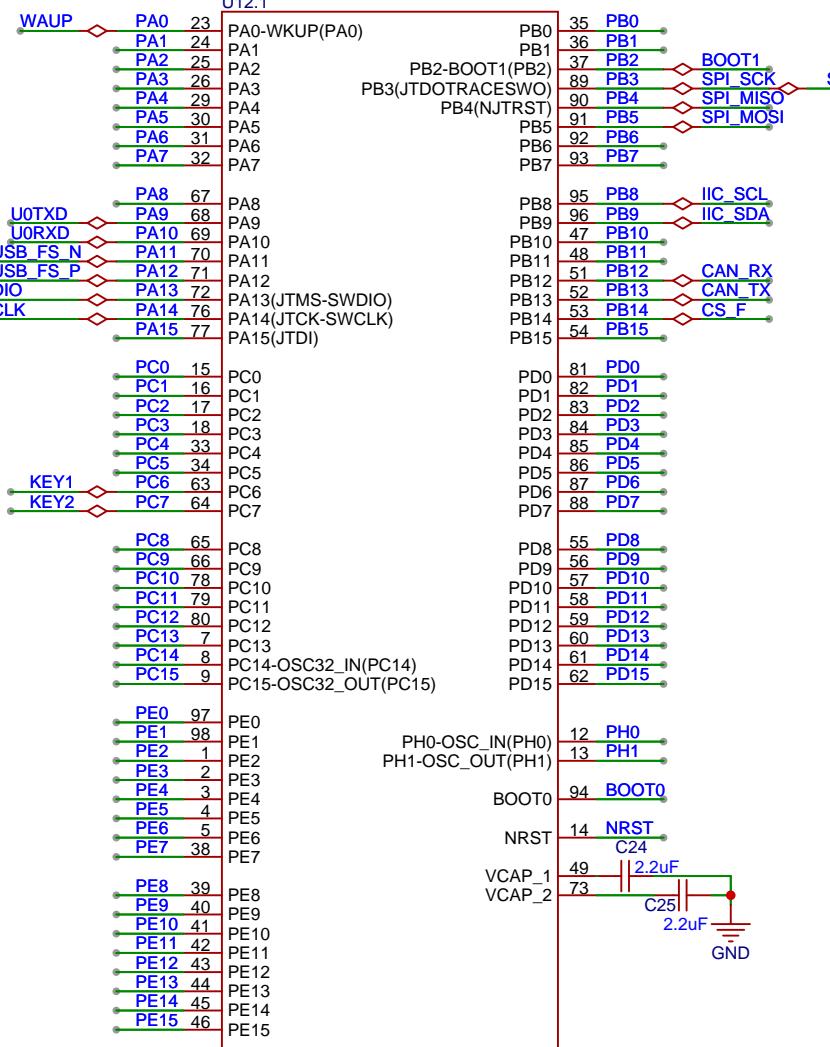
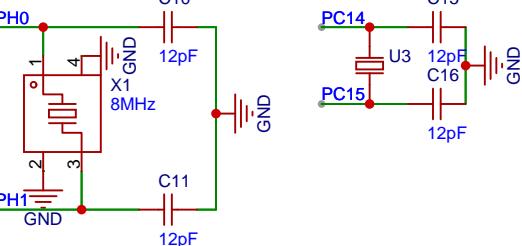


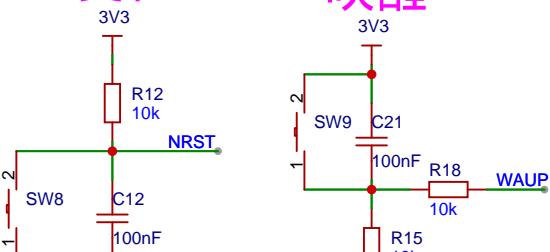
stm32



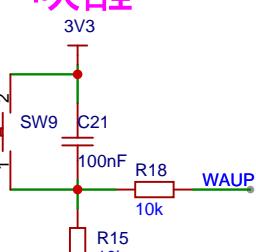
晶振



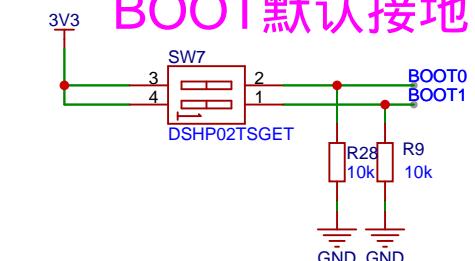
复位



唤醒

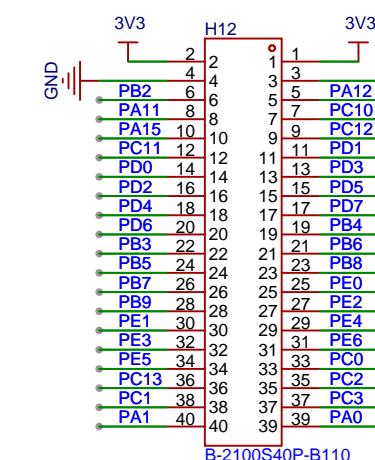


BOOT默认接地



PA13、PA14在烧录口引出
PC14、PC15、PH0、PH1是晶振脚，没引出
BOOT0、NRST没有引出
PB、PD、PE全部引出

总之，这两排没有引出的引脚是：
PA13\PA14\PC14\PC15\PH0\PH1\BOOT0



原理图

F4核心板_2

创建日期	2025-11-25
更新日期	2025-11-25

板子

F4核心板_2

绘图	
审阅	

绘制

F4核心板_2

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审阅

版本	
尺寸	