Anand Saha

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Objective: Bring deep learning research into practice as a research software engineer.

Highlights

- 14 years of industry experience in building enterprise products and services
- Currently focusing on bringing cutting edge deep learning research into the industry
- Exposure to comprehending and implementing research papers
- Hands on with effective transfer learning techniques
- Focusing on computer vision, with auxiliary interest in nlp and generative models

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Technologies

Languages: C/C++, Python
Frameworks: PyTorch, Tensorflow, Keras, V-REP
Libraries: NumPy, Pandas, Matplotlib, Scikit-Learn, OpenCV

Experience

NURTURE.AI
NIPS paper implementation competition participant   Dec'17
Implemented and validated the paper *Training Deep Networks Without Learning Rates Through Coin Betting* in PyTorch. (http://bit.ly/nips-cocob)

FAST.AI
International Fellow   Oct'17 - Dec'17
Implemented the paper *Fixing Weight Decay Regularization in Adam* in the fastai library in PyTorch. (http://bit.ly/fastai-adamw)

UDACITY
ML Nanodegree Capstone   Jul'17 - Aug'17
Ideated, designed and implemented a reinforcement learning based system to train a robotic arm to pick and place items using Q-learning. This was done in the V-REP simulation environment. (http://bit.ly/rl-capstone)

FLYTBASE
AI Engineer   Jun'17 - Sep'17
Created an image processing pipeline to extract information from high resolution aerial images, catering to uses cases like object detection and counting, in domains like wildlife conservation and inventory management. Made use of libraries like Tensorflow and various deep learning models.

LEARNING BREAK
AI Research Engineering   Apr'17 - Current
Quit my job in March'17 to focus full time into AI and deep learning with the intention of bringing research in this area into practice. Towards that, built my own deep learning rig,

took certification courses online, worked on CV and RL projects, implemented research papers, completed cs231n assignments, started writing blog, did consultancy for startup etc.

VERITAS TECHNOLOGIES LLC

Principal Software Engineer   Oct'15 - Mar'17

Worked on the cloud stack of NetBackup for data backups to cloud platforms like AWS and OpenStack. Had been first scrum master of my team on Veritas' transition to agile. Served as tech lead of the team to coordinate feature engineering activities with product owner, architects and leadership.

SYMANTEC

Principal Software Engineer   Mar'12 - Sep'15

Worked on various features of NetBackup , including SharePoint protection, virtualized assets protection etc.

TATA COMMUNICATIONS

Senior Software Engineer   Sep'07 - Feb'12

Developer and maintainer of TATA Communications's machine learning based system to predict voice traffic patterns and generate optimised dial plans for the upcoming hour, having direct impact on revenue.

GAVS TECHNOLOGIES

Software Engineer   May'03 - Aug'07

Implemented various bespoke projects spanning technologies like C/C++, .NET, WiMax and Symbian OS. In addition to software development, wrote tech proposals, presented solutions to customers, managed teams.

Relevant Certifications

- Fast.ai deep learning course via international fellowship   Oct'17 - Dec'17
        Computer Vision, Language Models, Transfer Learning techniques
- Udacity's Machine Learning Nanodegree   Apr'17 - Jul'17
        Supervised, Unsupervised, Reinforcement, Deep Learning, Tools and Techniques
- Udacity's AI Nanodegree (Computer Vision concentration)   Apr'17 - Dec'17
        Game Playing, Search, Simulated Annealing, Constraint Satisfaction, Logic and Reasoning, Planning, Bayes Nets, Hidden Markov Models, Deep Learning
- Coursera Cloud Computing Specialization (part 1 to 4)   2016
        Concepts, Applications, Networking

Education

National Institute of Technology, Tiruchirappalli   1999 - 2003
        BE, Instrumentation and Control Engineering
Indian Institute of Sciences (IISc), Bangalore   Summer of 2002
        I did my 3rd year summer project here studying scheduling in Real Time Operating Systems.
Indian Institute of Technology (IIT), Bombay   Summer of 2001
        I did my 2nd year summer project here, worked on FPGA implementation of a Network Interface Controller for Network Printer.