

STAND UP CALL DETAILS



GROUP NO-1

NAME ASMITA KAJROLKAR **PS NO** 99004398

02/07/21

1

WORK DONE TILL YESTERDAY

Confirmation of module to be worked on

Found information on the topic Circular Convolution using FFT

Thought about the problem statement and the inputs required and prepared SWOT Analysis

2

WORK GOING TO DO TODAY

Update Low Level and High Level Requirements

Create UML designs

3

PROBLEMS FACED

Deciding of the module

Limitation on the input that can be taken in the module

STAND UP CALL DETAILS



GROUP NO-1

NAME KARISHMA SAVANT **PS NO** 99004900

02/07/21

1

WORK DONE TILL YESTERDAY

Worked on requirements

Decided high and low level requirements

2

WORK GOING TO DO TODAY

UML Diagrams

3

PROBLEMS FACED

Distinguishing high and low level requirements

Structuring the UML diagram to best represent the module

STAND UP CALL DETAILS



GROUP NO-1

NAME VYAS KAUSHIK

PS NO 99004903

02/07/21

1

WORK DONE TILL YESTERDAY

Worked on requirements

Did SWOT Analysis

2

WORK GOING TO DO TODAY

UML Diagrams

Implentation of code for cable capacities

3

PROBLEMS FACED

Identifying the requirements for modules

Structuring the UML Diagrams

STAND UP CALL DETAILS



GROUP NO-1

NAME RAHUL MANE

PS NO 99004904

02/07/21

1

WORK DONE TILL YESTERDAY

Worked on Requirements

Worked on Low and High level requirements

2

WORK GOING TO DO TODAY

UML Diagrams

Code Implementation

3

PROBLEMS FACED

Coming up with a great idea

Structuring the UML Diagrams

STAND UP CALL DETAILS



GROUP NO-1

NAME HUSAIN KHAN

PS NO 99004902

02/07/21

1

WORK DONE TILL YESTERDAY

Worked on low and high level requirements
Figured low level and high level

2

WORK GOING TO DO TODAY

UML Diagrams

3

PROBLEMS FACED

Structuring the UML Diagrams